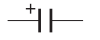
SCHEMATIC DIAGRAM

MODEL : 32WL58P

WARNING: BEFORE SERVICING THIS CHASSIS, READ THE "X-RAY RADIATION PRECAUTION", "SAFETY PRECAUTION" AND "PRODUCT SAFETY NOTICE" ON THE MANUAL FOR THIS MODEL.

CAUTION: The international hazard symbols "⚠" in the schematic diagram and the parts list designate components which have special characteristics important for safety and should be replaced only with types identical to those in the original circuit or specified in the parts list. The mounting position of replacements is to be identical with originals. Before replacing any of these components, read carefully the PRODUCT SAFETY NOTICE on the MANUAL for this model. Do not degrade the safety of the receiver through improper servicing.

NOTE:

1. RESISTOR Resistance is shown in ohm [K = 1.000, M = 1.000.000]. All resistors are 1/6W and 5% tolerance carbon resistor, unless otherwise noted as the following marks.
   1/2R = Metal or Metal oxide of 1/2 watt
   1/2S = Carbon compsistion of 1/2 watt
   1RF = Fuse resistor of 1 watt
   10W = Cement of 10 watt
   K = ±10% G = ±2% F = ±1%
2. CAPACITOR Unless otherwise noted in schematic, all capacitor values less than 1 are expressed in F, and the values more than 1 in pF.
   All capacitors are ceramic 50V, unless otherwise noted as the following marks.
    Electolytic capacitor Mylar capacitor
3. The parts indicated with "⚠" have special characteristics, and should be replaced with identical parts only.
4. Voltages read with DIGITAL MULTI-METER from point indicated to chassing ground, using a color bar signal with all controls at normal, line voltage at 220 volts.
5. Waveforms are taken receiving color bar signal with enough sensitivity.
6. Voltage reading shown are nominal values and may vary ±20% except H.V.

■ SCHEMATIC DIAGRAM STRUCTURE:

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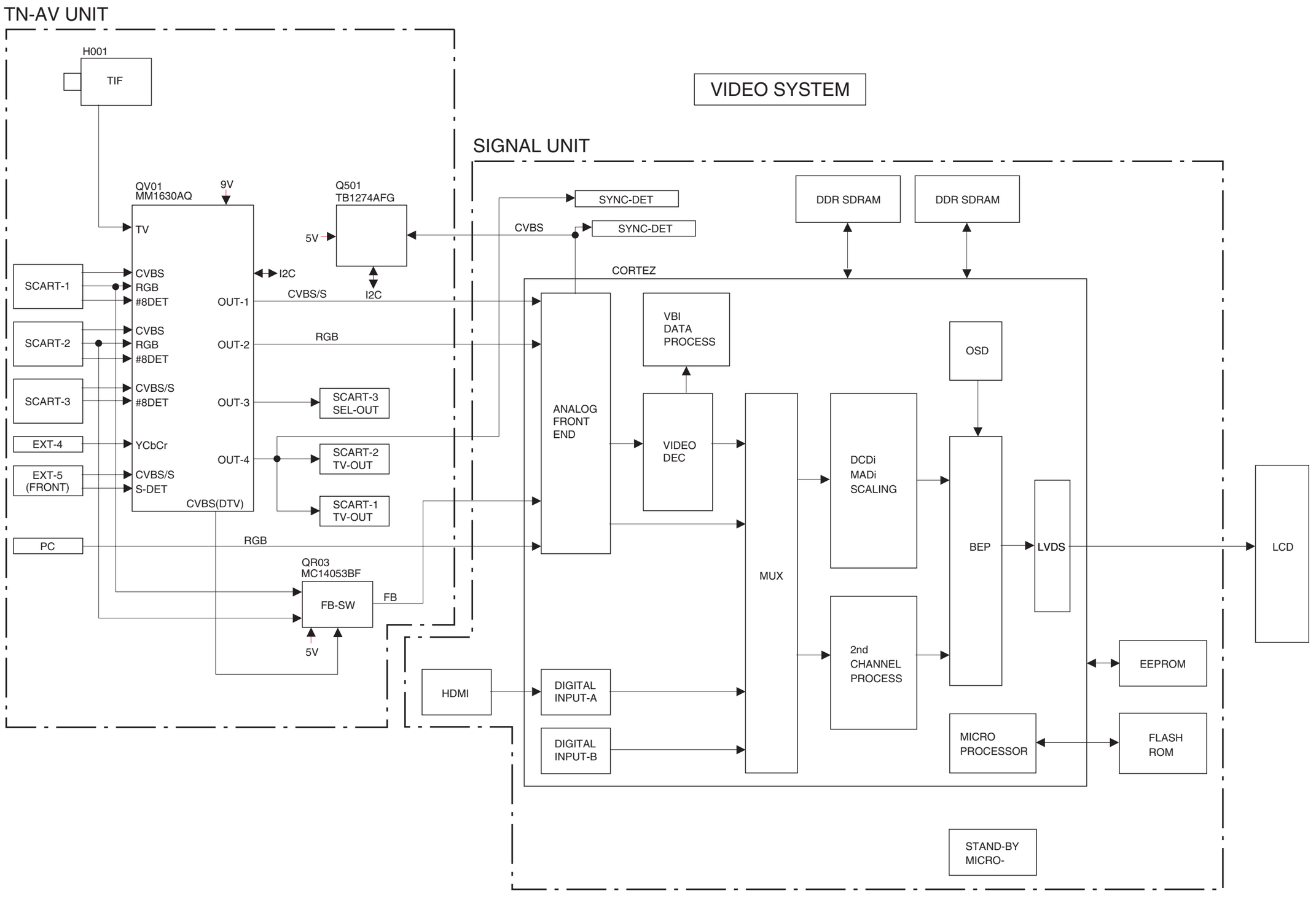
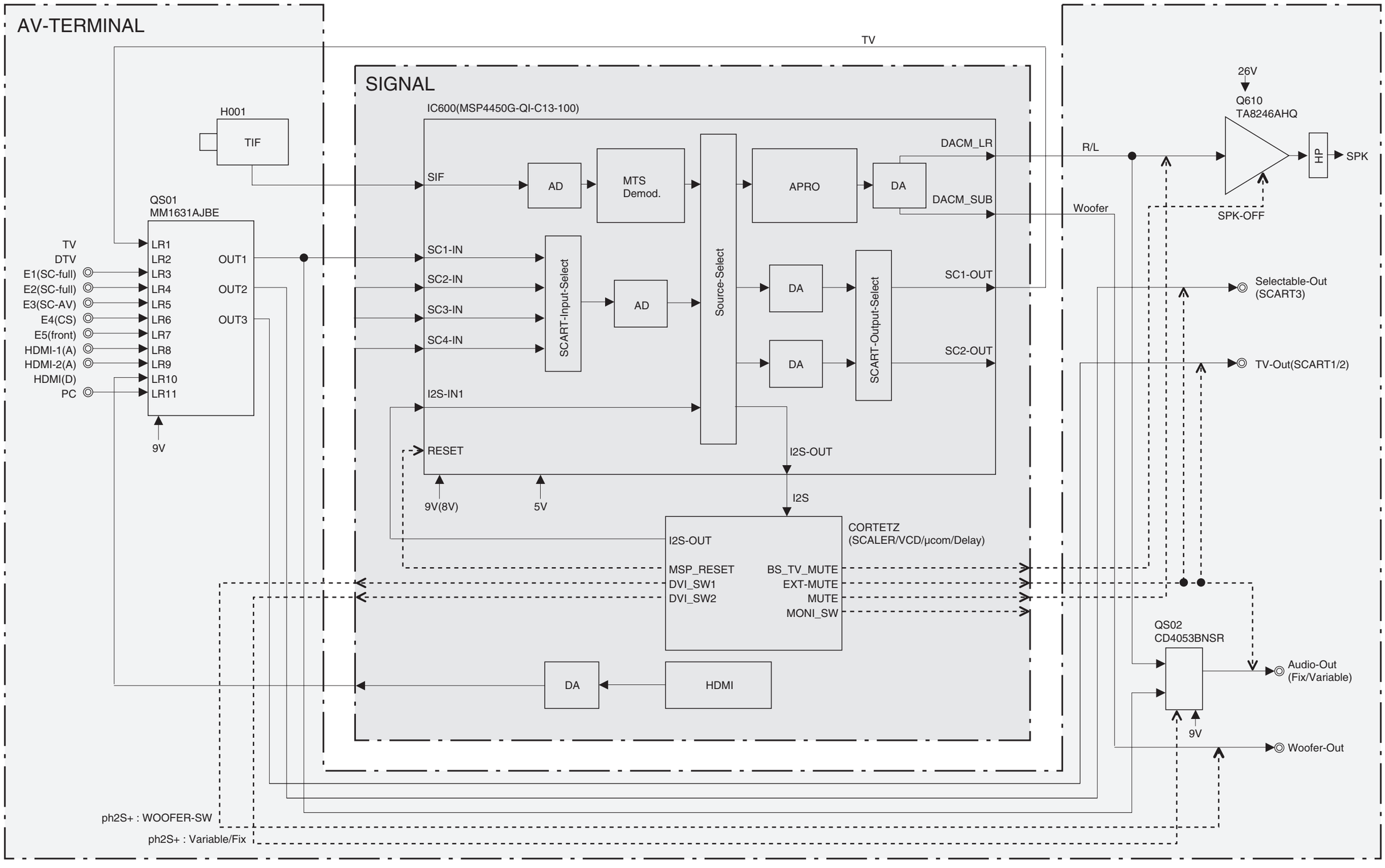
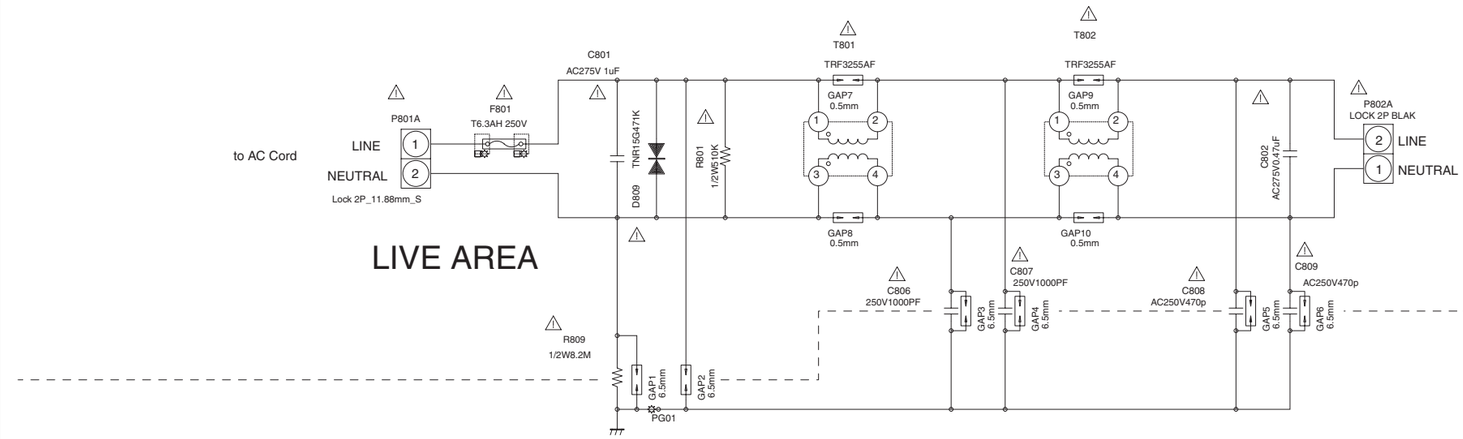
CIRCUIT BLOCK DIAGRAM

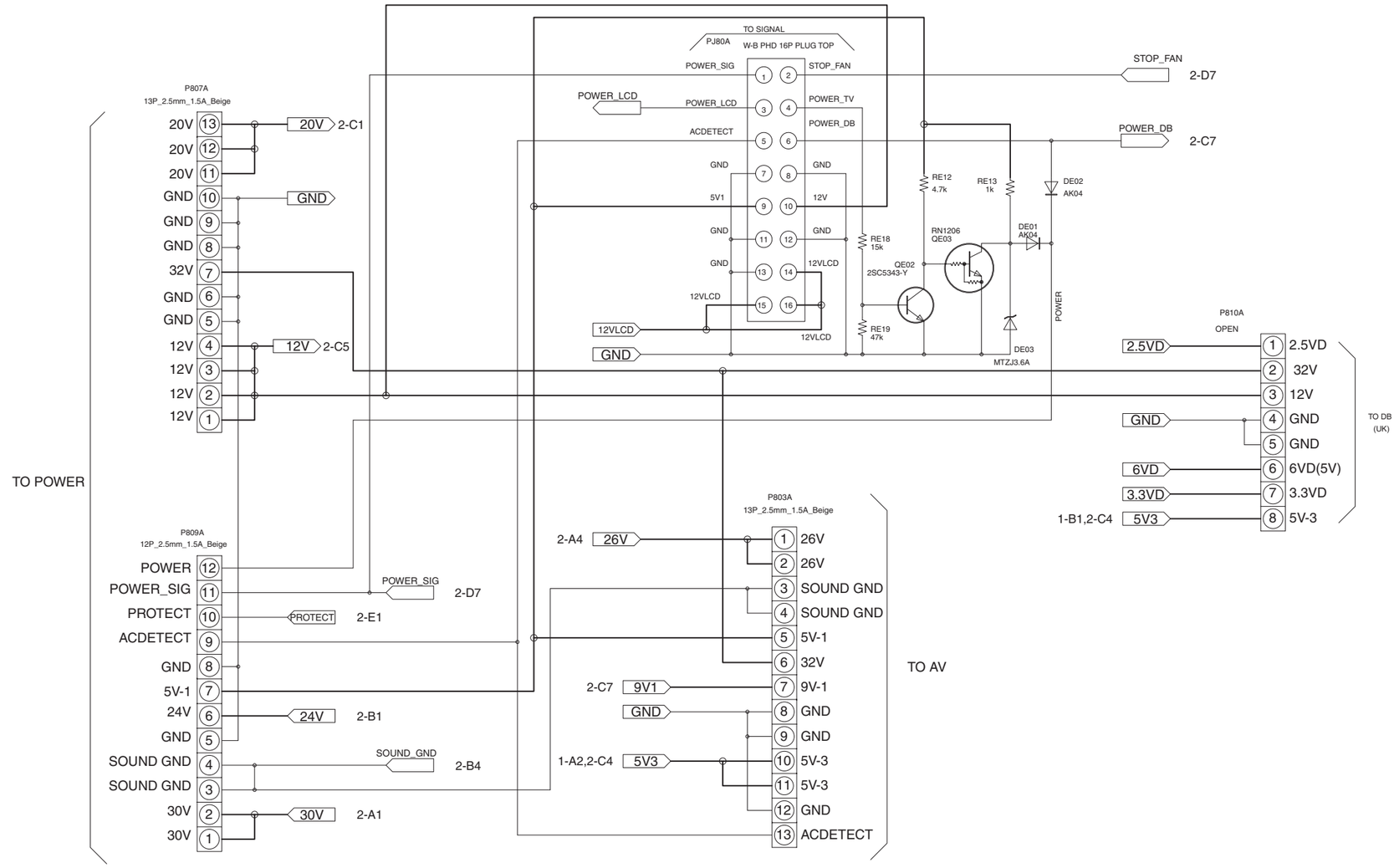


AUDIO SYSTEM

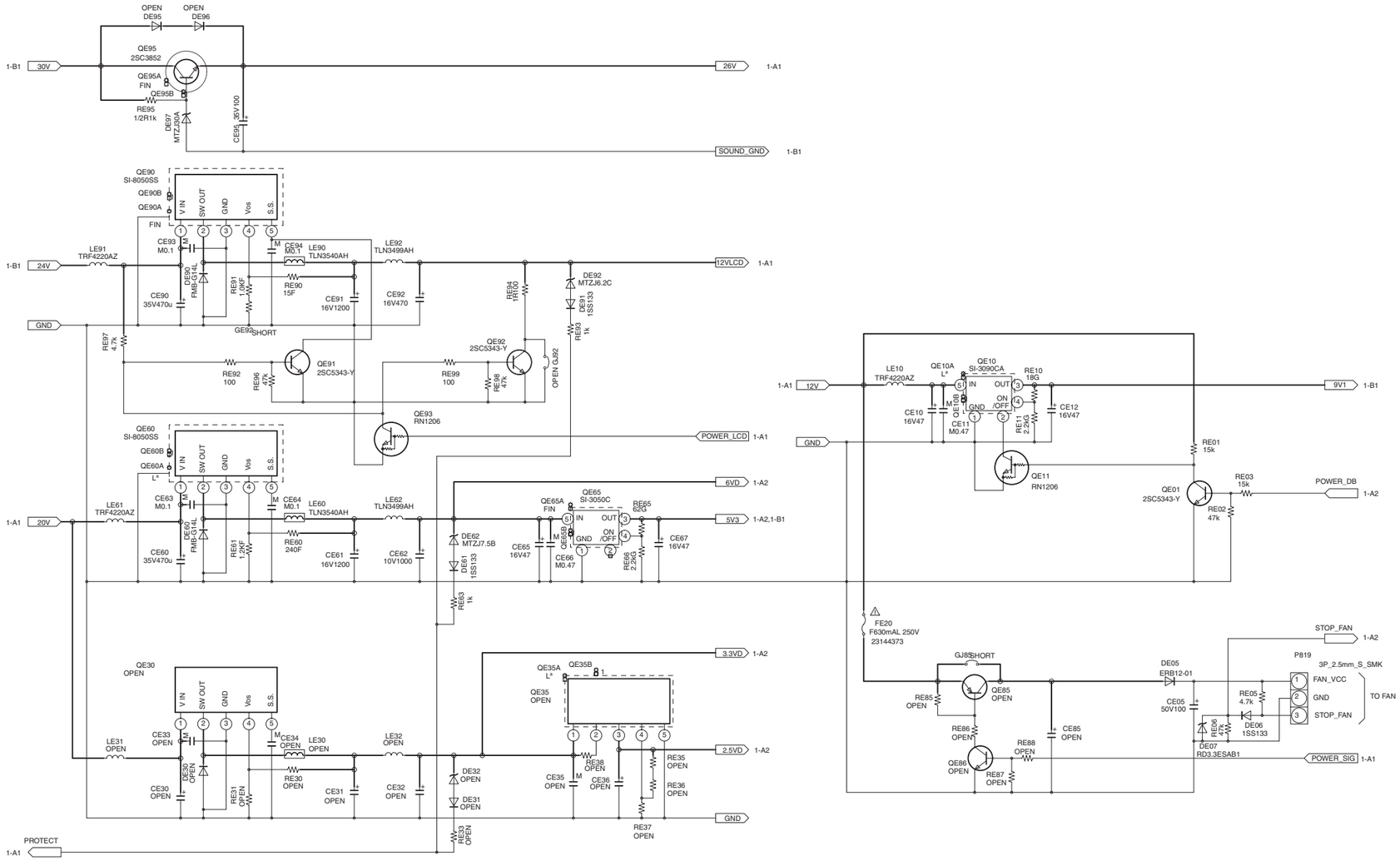




32WL58P
AC INPUT

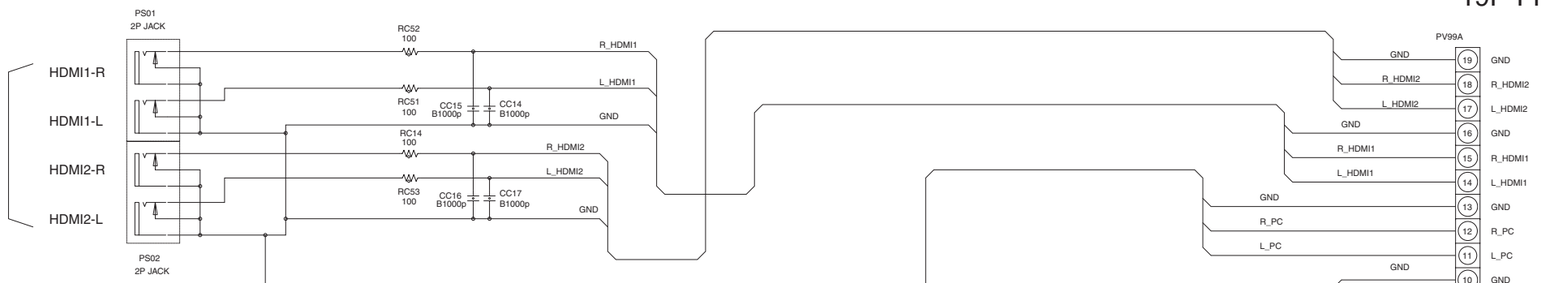


32WL58P
LOW_B (CONNECTOR)
SHEET-1/2

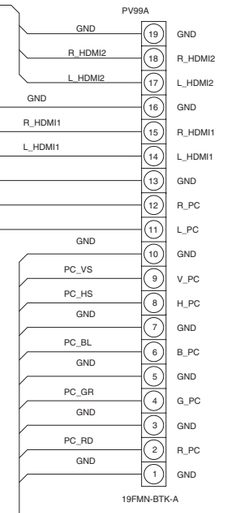


32WL58P
LOW_B (LOWB)
SHEET-2/2

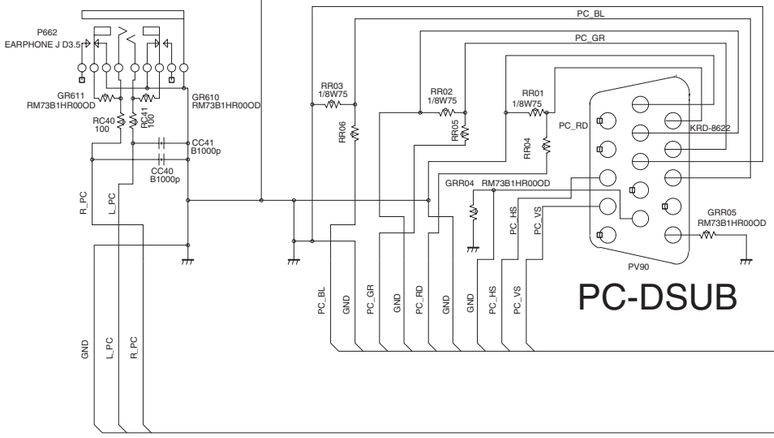
HDMI Audio



19P-FFC

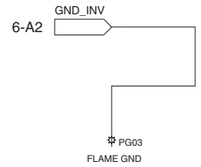


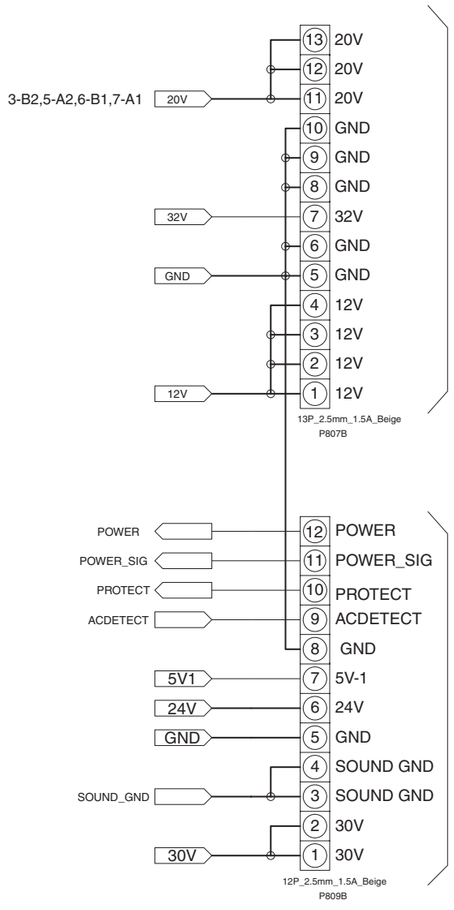
PC SOUND

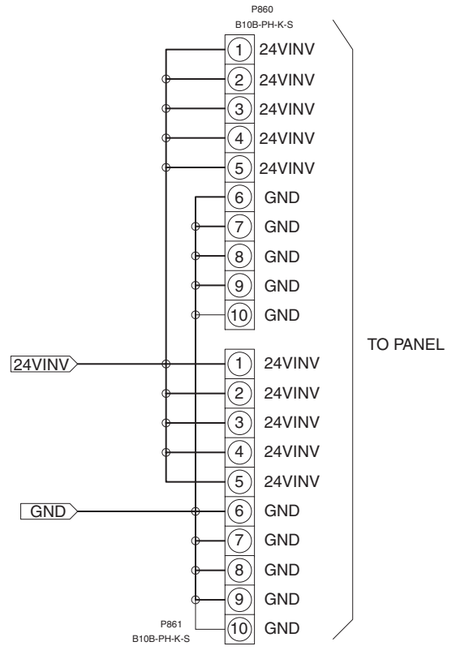


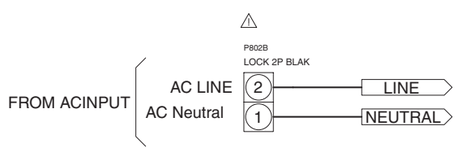
PC-DSUB

32WL58P
PC-HDMI-IN



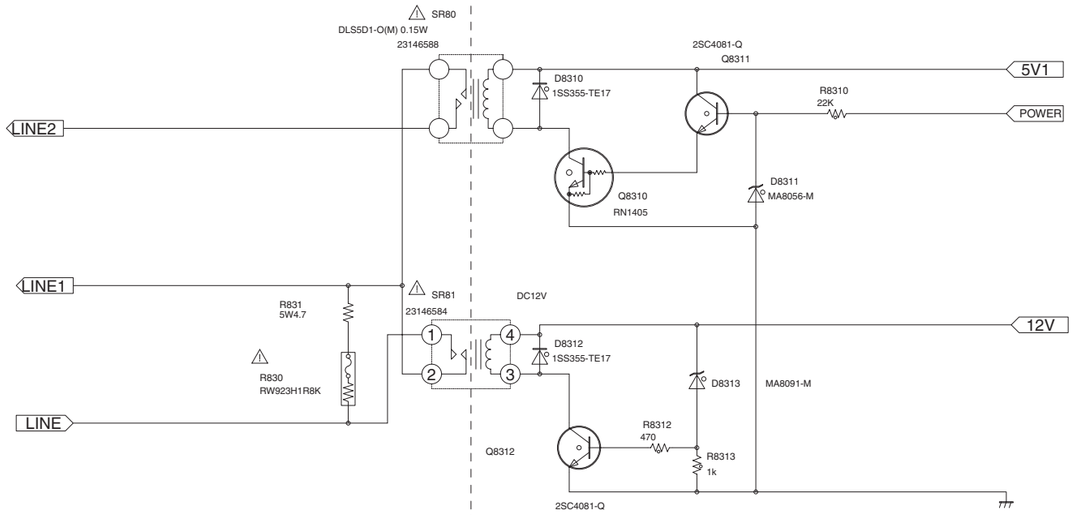






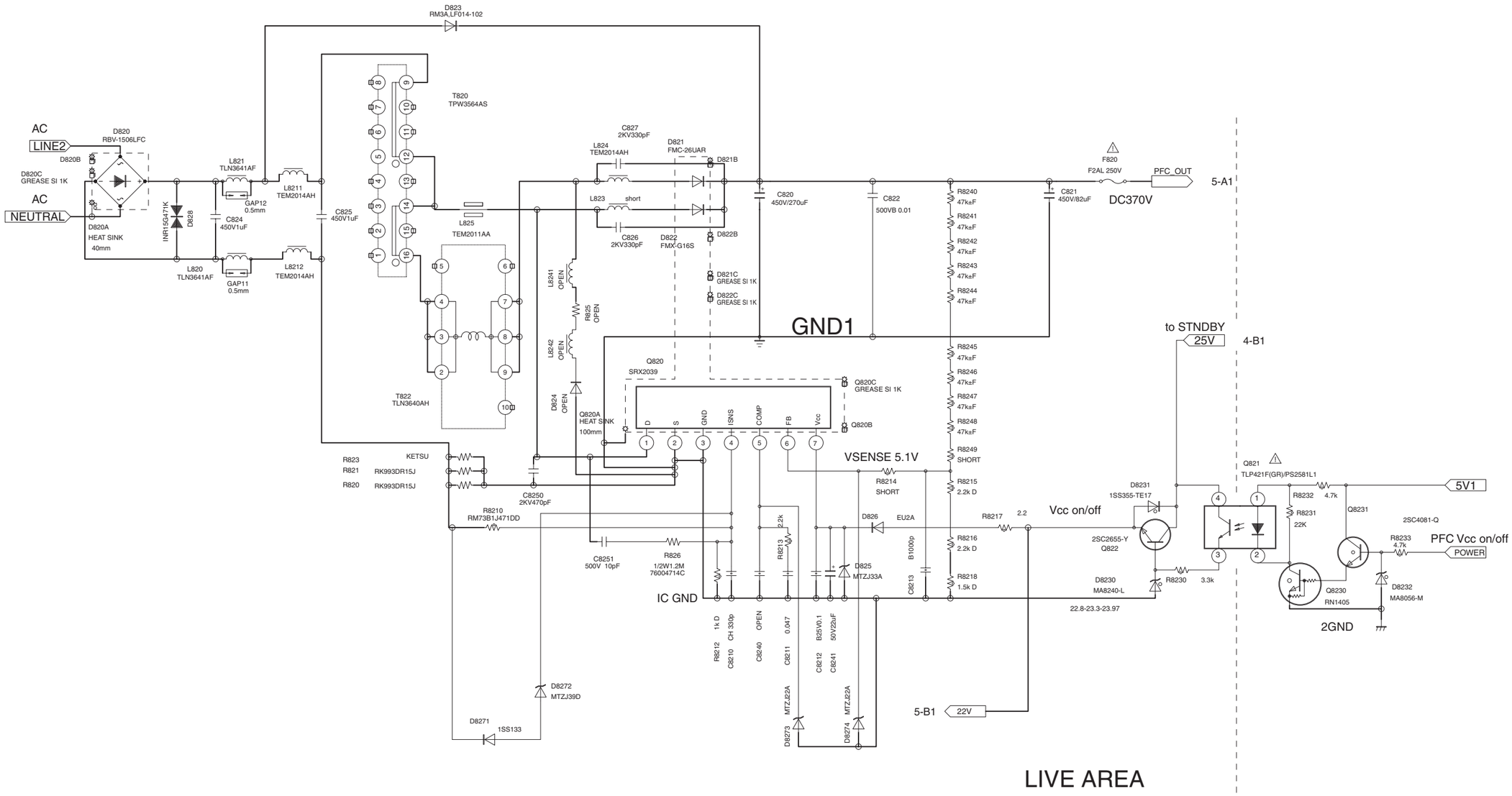
LIVE AREA

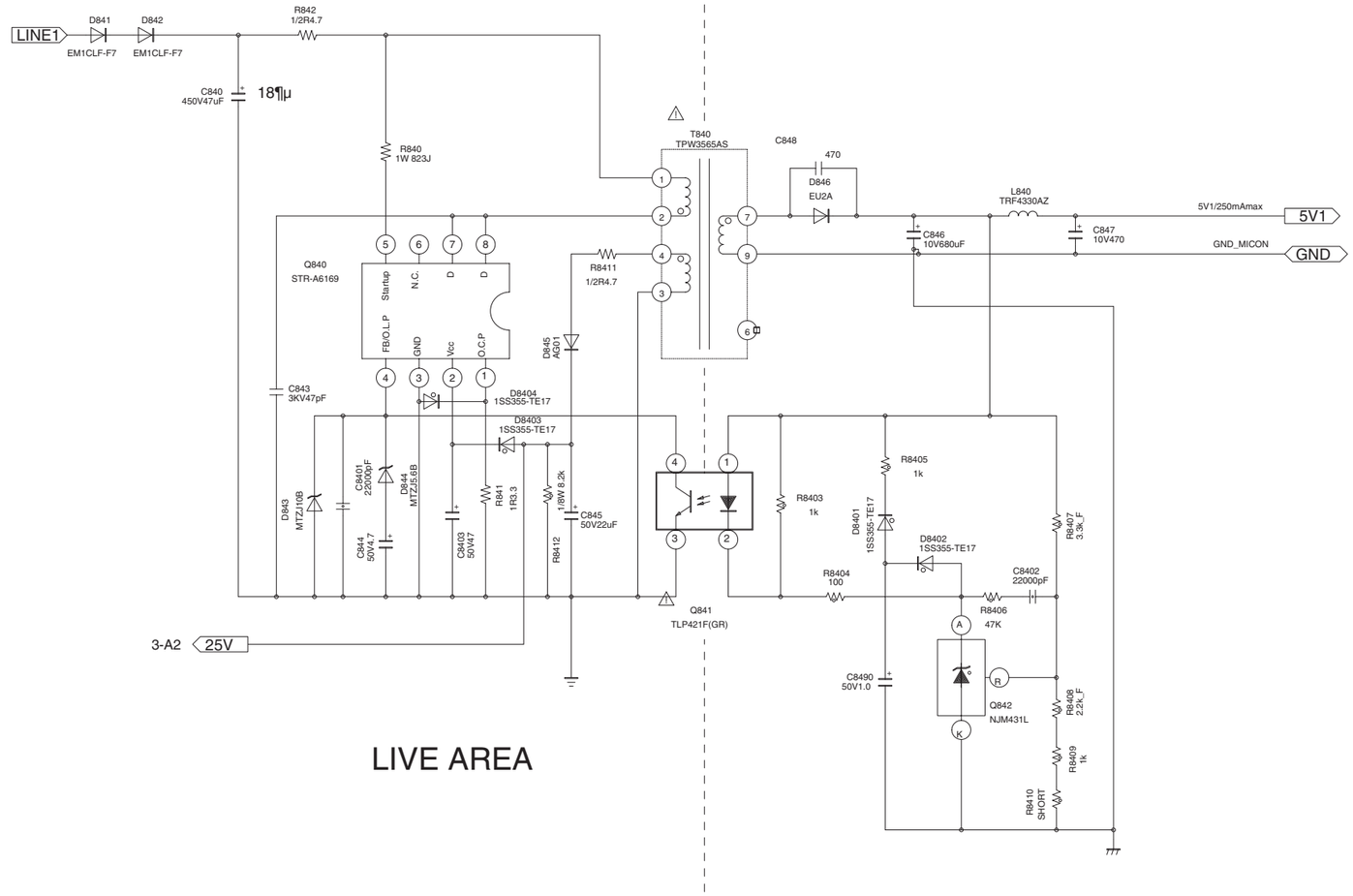
32WL58P
POWER (CONNECTOR)
SHEET-1/7



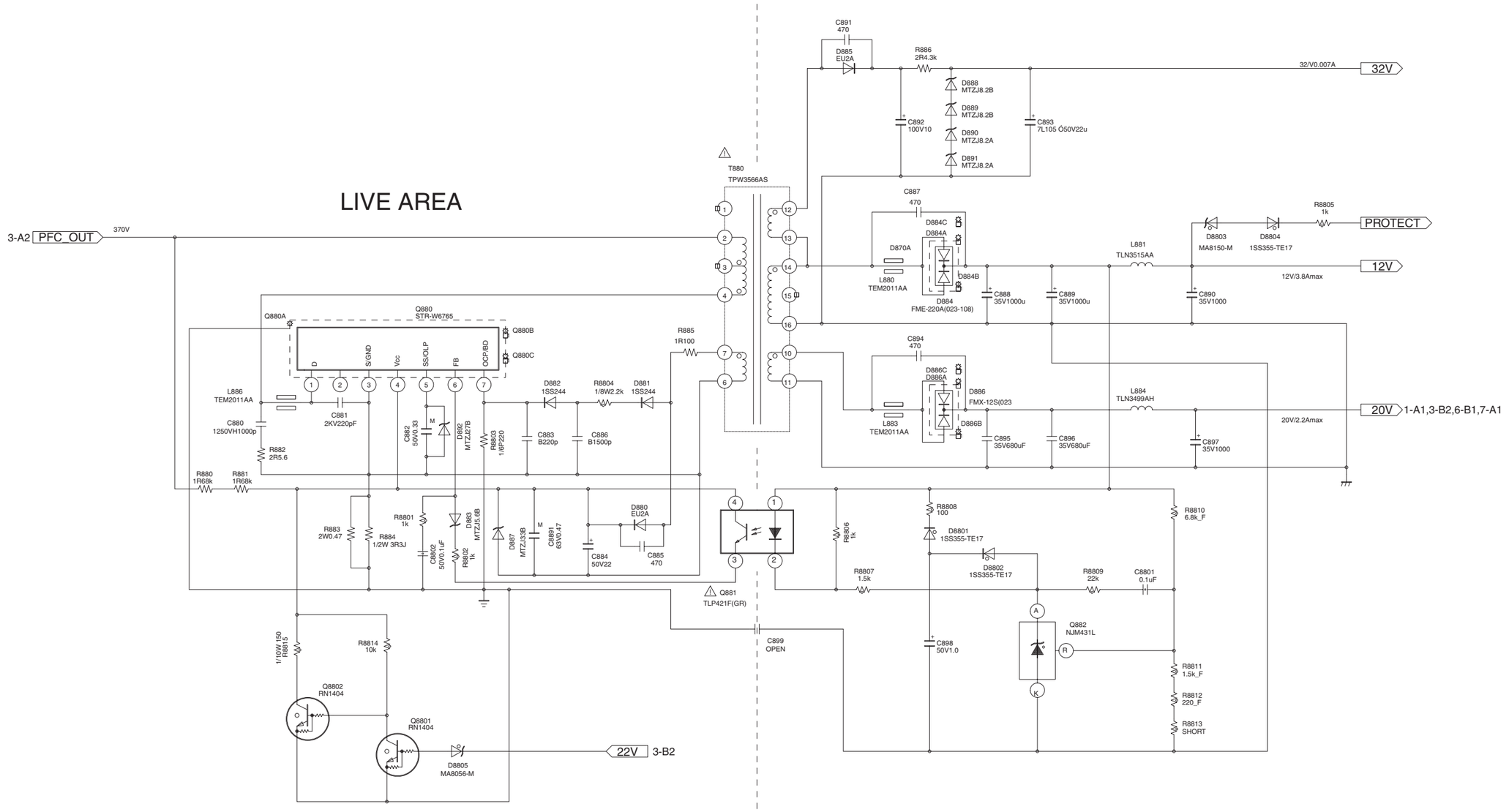
LIVE AREA

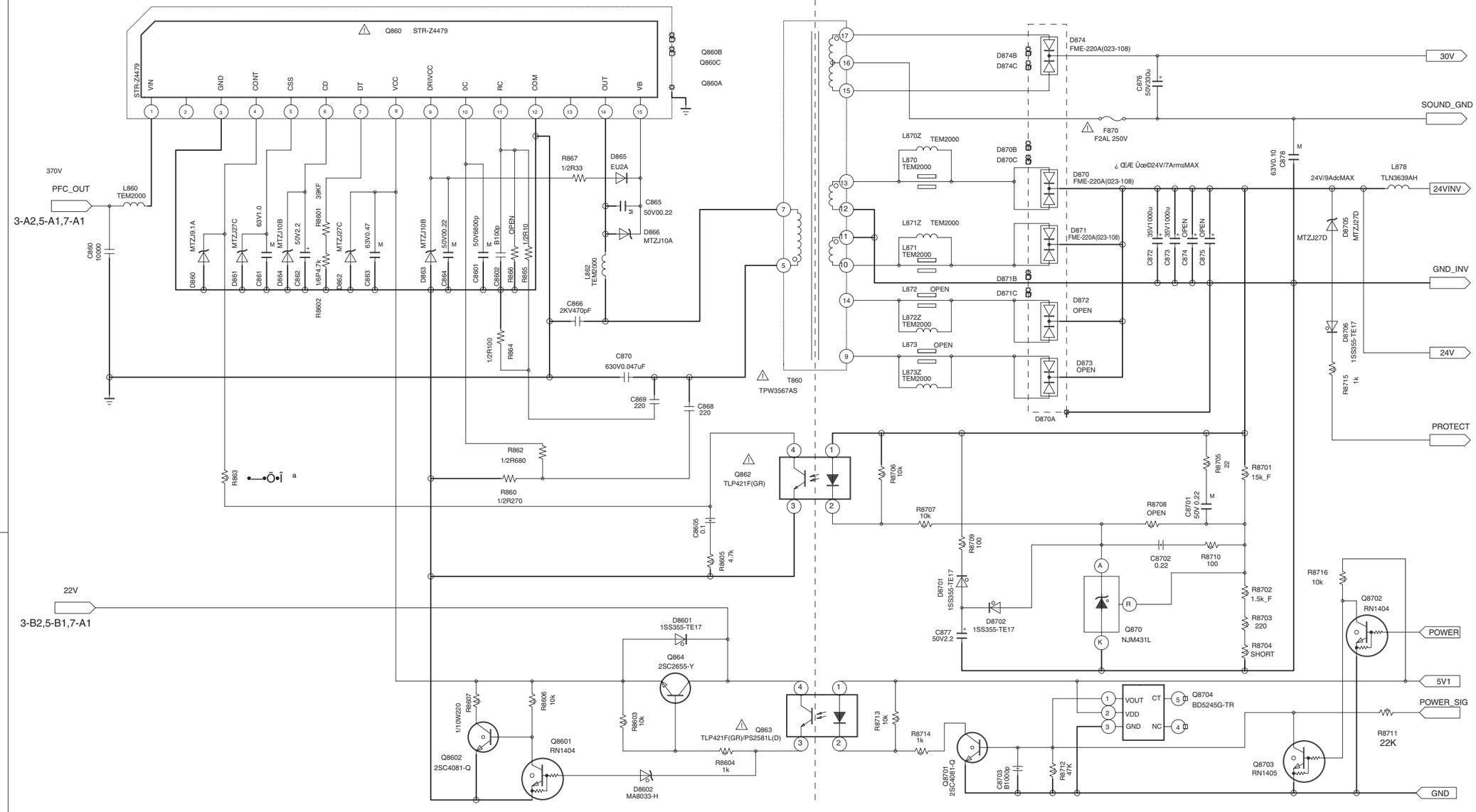
32WL58P
POWER (RELAY/RUSH PROTECTION)
SHEET-2/7


AC
LINE2
AC
NEUTRAL
D820
RBV-1506LFC
D820B
D820C
GREASE SI 1K
D820A
HEAT SINK
40mm
INR15G477K
D828
L821
TLN3641AF
GAP12
0.5mm
L820
TLN3641AF
GAP11
0.5mm
C824
450V1uF
L8211
TEM2014AH
L8212
TEM2014AH
C825
450V1uF
D823
RM3A LF014-102
T820
TPW3564AS
L825
TEM2011AA
T822
TLN3640AH
C827
2KV330pF
L824
TEM2014AH
D821
FMC-26UAR
D821B
L823
short
C826
2KV330pF
D822
FMX-G16S
D822B
D821C
GREASE SI 1K
D822C
GREASE SI 1K
C820
450V/270uF
C822
500VB 0.01
C821
450V/82uF
F820
F2AL 250V
PFC_OUT
DC370V
5-A1
GND1
R8240
47kxF
R8241
47kxF
R8242
47kxF
R8243
47kxF
R8244
47kxF
R8245
47kxF
R8246
47kxF
R8247
47kxF
R8248
47kxF
R8249
SHORT
R8215
2.2k D
R8216
2.2k D
R8218
1.5k D
L8241
OPEN
R825
OPEN
L8242
OPEN
D824
OPEN
Q820
SRX2039
Q820A
HEAT SINK
100mm
Q820C
GREASE SI 1K
Q820B
D
S
GND
ISNS
COMP
FB
Vcc
VSENSE 5.1V
R8214
SHORT
R823
R821
R820
KETSU
RK993DR15J
RK993DR15J
C8250
2KV470pF
R8210
RM73B1J471DD
C8251
500V 10pF
R826
1/2W1.2M
76004714C
D826
EU2A
R8217
2.2
R8213
2.2k
D825
MTZJ33A
B1000p
C8213
IC GND
R8212
1k D
C8210
CH 330p
C8240
OPEN
C8211
0.047
C8212
B25V0.1
C8241
50V22uF
D8272
MTZJ39D
D8271
1SS133
D8273
MTZJ22A
D8274
MTZJ22A
5-B1
22V
to STNDBY
25V
4-B1
Vcc on/off
D8231
1SS355-TE17
2SC2655-Y
Q822
D8230
MA8240-L
R8230
3.3k
22.8-23.3-23.97
Q821
TLP421F(GR)/PS2581L1
R8232
4.7k
5V1
R8231
22K
Q8231
2SC4081-Q
R8233
4.7k
PFC Vcc on/off
POWER
Q8230
RN1405
D8232
MA8056-M
2GND
LIVE AREA
32WL58P
POWER (PFC)
SHEET-3/7



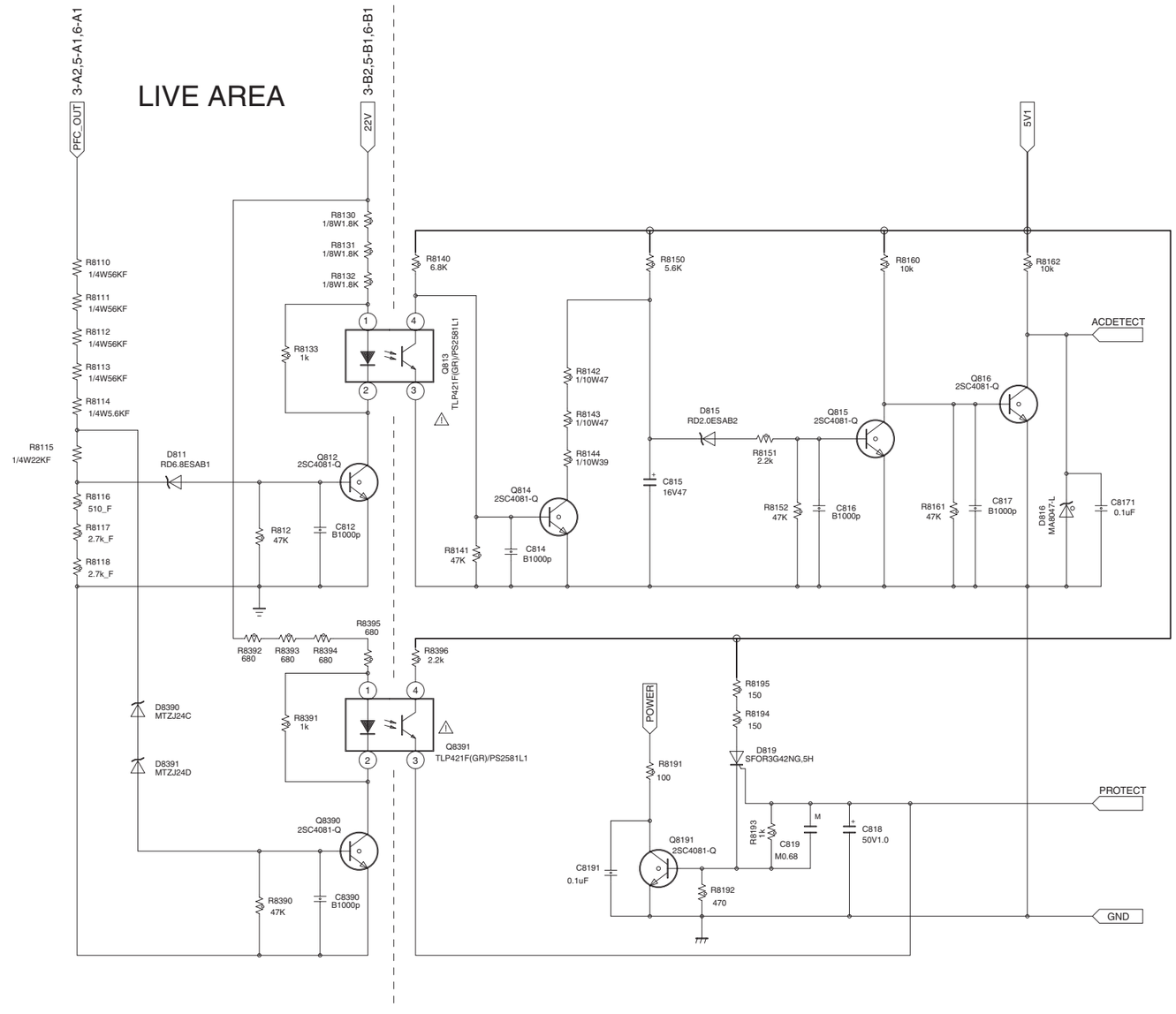
32WL58P
POWER (STANDBY POWER)
SHEET-4/7


LIVE AREA
3-A2 PFC_OUT
370V
T880
TPW3566AS
C891
470
D885
EU2A
R886
2R4.3k
32/V0.007A
32V
D888
MTZJ8.2B
D889
MTZJ8.2B
D890
MTZJ8.2A
D891
MTZJ8.2A
C892
100V10
C893
7L105 Ö50V22u
C887
470
D884C
D884A
D870A
L880
TEM2011AA
D884
FME-220A(023-108)
D884B
C888
35V1000u
C889
35V1000u
L881
TLN3515AA
R8805
1k
D8803
MA8150-M
D8804
1SS355-TE17
PROTECT
12V
12V/3.8Amax
C890
35V1000
C894
470
D886C
D886A
D886
FMX-12S(023
L883
TEM2011AA
D886B
C895
35V680uF
C896
35V680uF
L884
TLN3499AH
20V
20V/2.2Amax
C897
35V1000
1-A1,3-B2,6-B1,7-A1
Q880
STR-W6765
Q880A
Q880B
Q880C
D
S/GND
Vcc
SS/OLP
FB
OCP/BD
R885
1R100
L886
TEM2011AA
C881
2KV220pF
C880
1250VH1000p
R882
2R5.6
C882
50V0.33
D882
MTZJ27B
R8803
1/6P220
D882
1SS244
R8804
1/8W2.2k
D881
1SS244
C883
B220p
C886
B1500p
R880
1R68k
R881
1R68k
R883
2W0.47
R884
1/2W 3R3J
R8801
1k
C8802
50V0.1uF
R8802
1k
D883
MTZJ5.6B
D887
MTZJ33B
C8801
63V0.47
C884
50V22
D880
EU2A
C885
470
Q881
TLP421F(GR)
R8806
1k
R8808
100
D8801
1SS355-TE17
R8807
1.5k
D8802
1SS355-TE17
R8809
22k
C8801
0.1uF
R8810
6.8k_F
C899
OPEN
C898
50V1.0
Q882
NJM431L
R8811
1.5k_F
R8812
220_F
R8813
SHORT
1/10W 150
R8815
R8814
10k
Q8802
RN1404
Q8801
RN1404
D8805
MA8056-M
22V
3-B2
32WL58P
POWER (RCC POWER)
SHEET-5/7

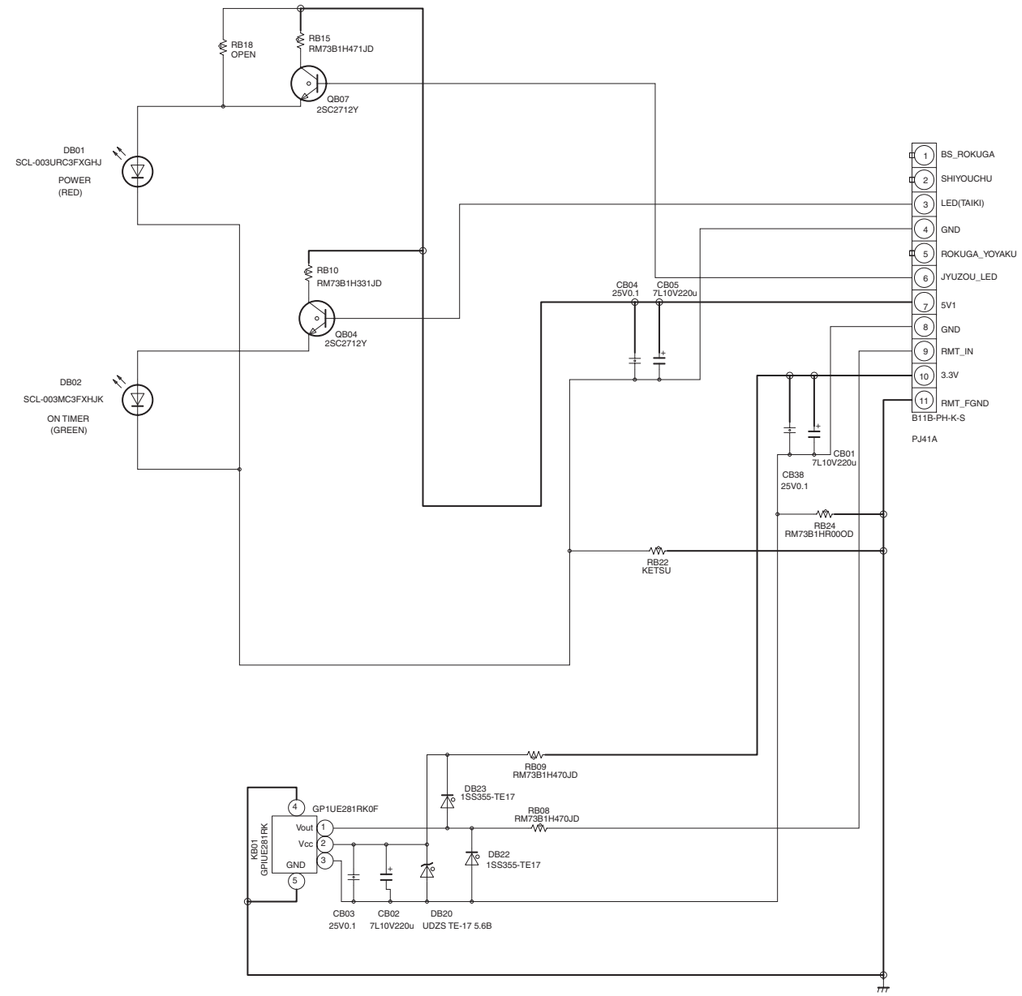


LIVE AREA

32WL58P
POWER (RESONANCE POWER)
SHEET-6/7



32WL58P
POWER (PROTECT/ AC DETECT)
SHEET-7/7



32WL58P
LED RMT

TO SIGNAL BOARD

3P

PJ21A
B3B-PH-K-S

50P-FFC

PJ22A
9637S-50ATL FFC/FPC SOCKET

50P-FFC

PJ60A
9637S-50ATL FFC/FPC SOCKET

19P-FFC

PV99B

PC/HDMI(A)

19FMN-BTK-A

5P

TO LED_HP BOARD

P602A
B5B-PH-K-S

13P

TO LOWB BOARD

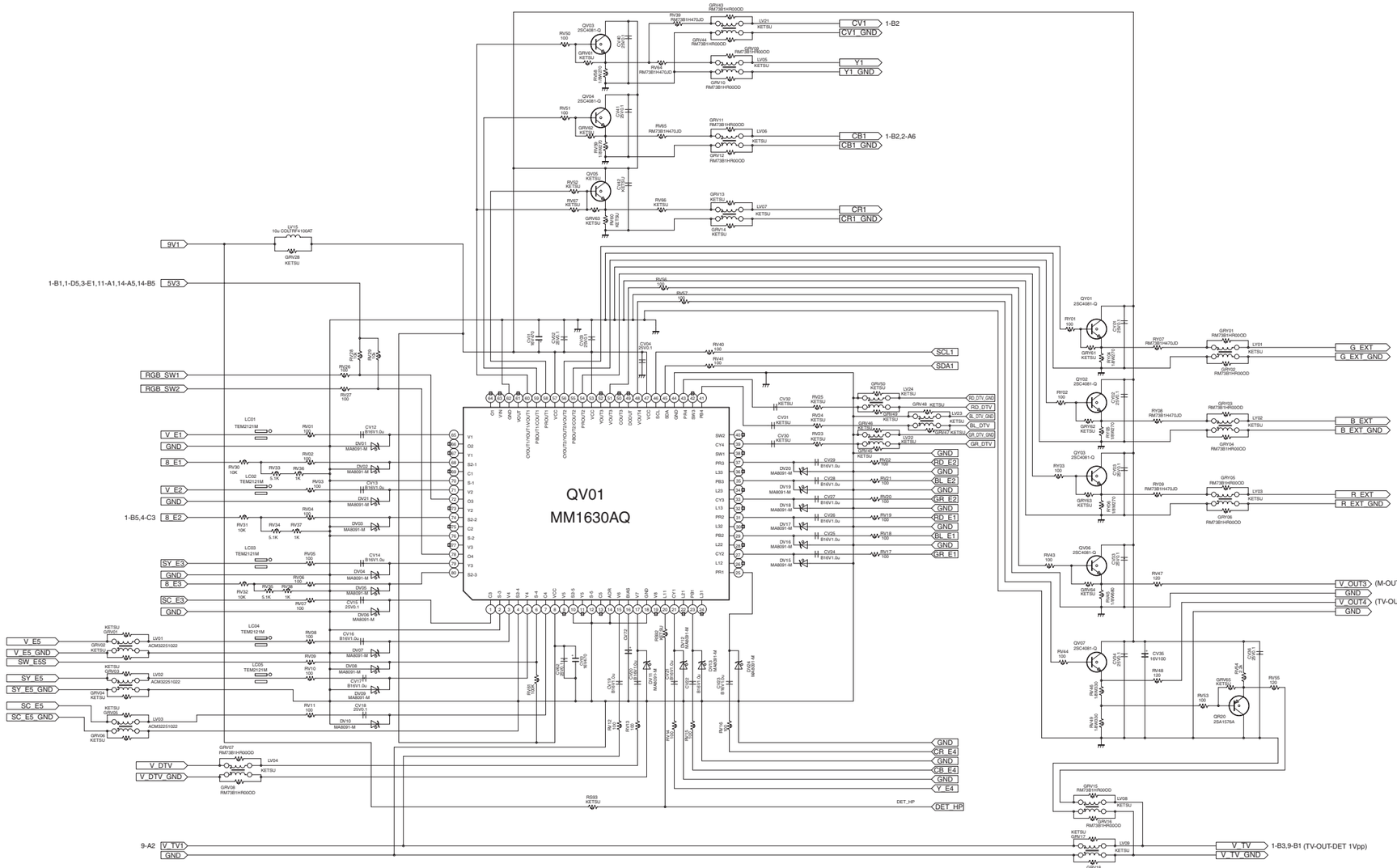
P803B
13P_2.5mm_1.5A_Beige

11P

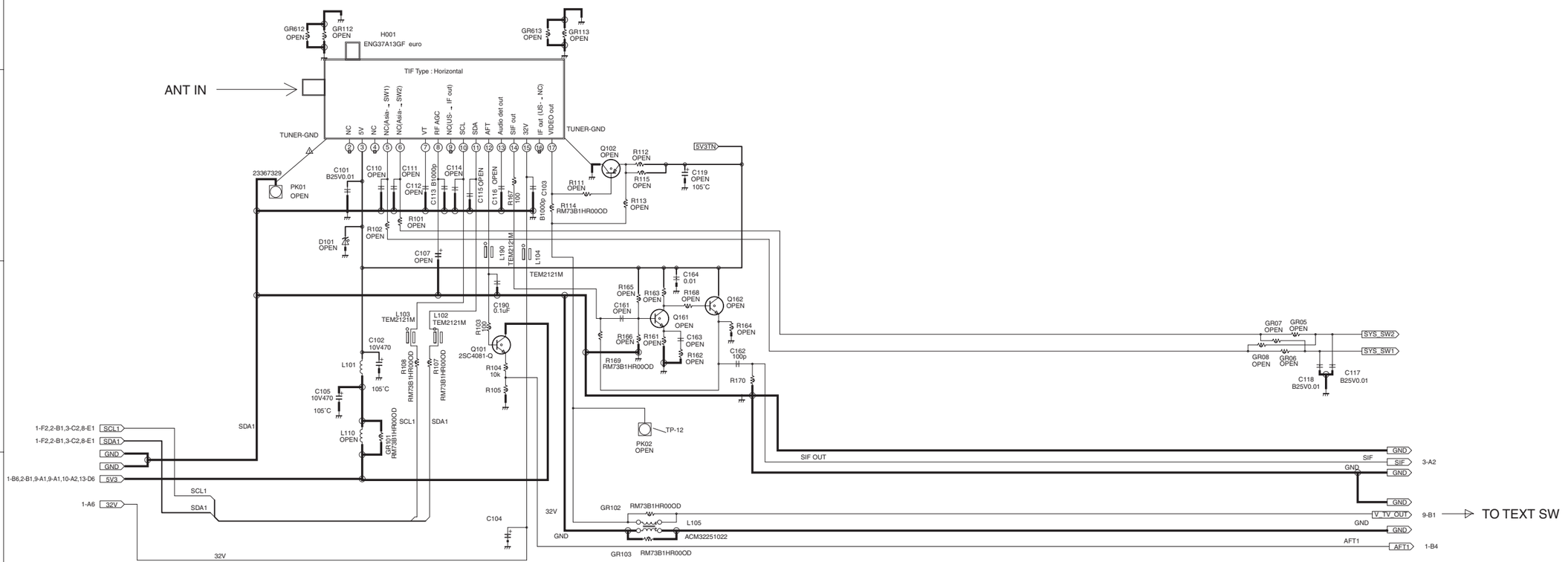
FRONT-AV(E5)

PV25A
B12B-PH-K-S

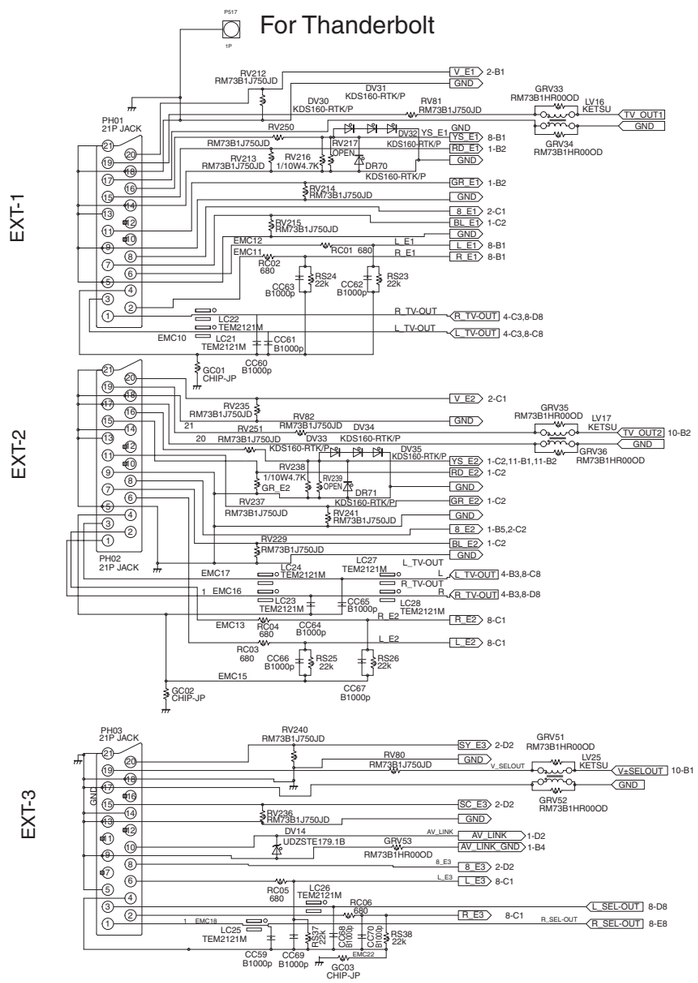
32WL58P
TN_AV (CONNECTOR)
SHEET-1/11

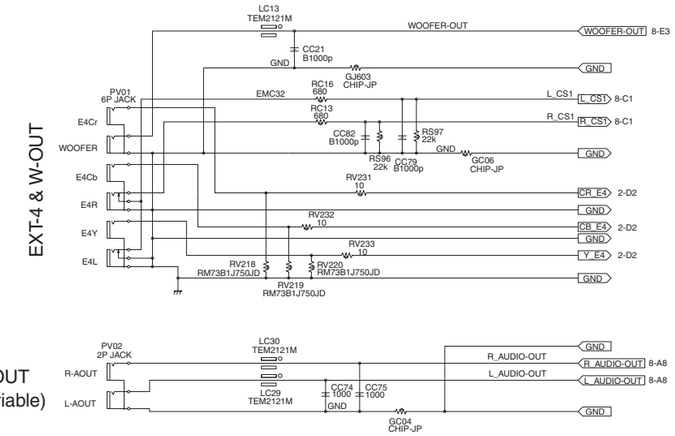


32WL58P
TN_AV (VIDEO-SW)
SHEET-2/11

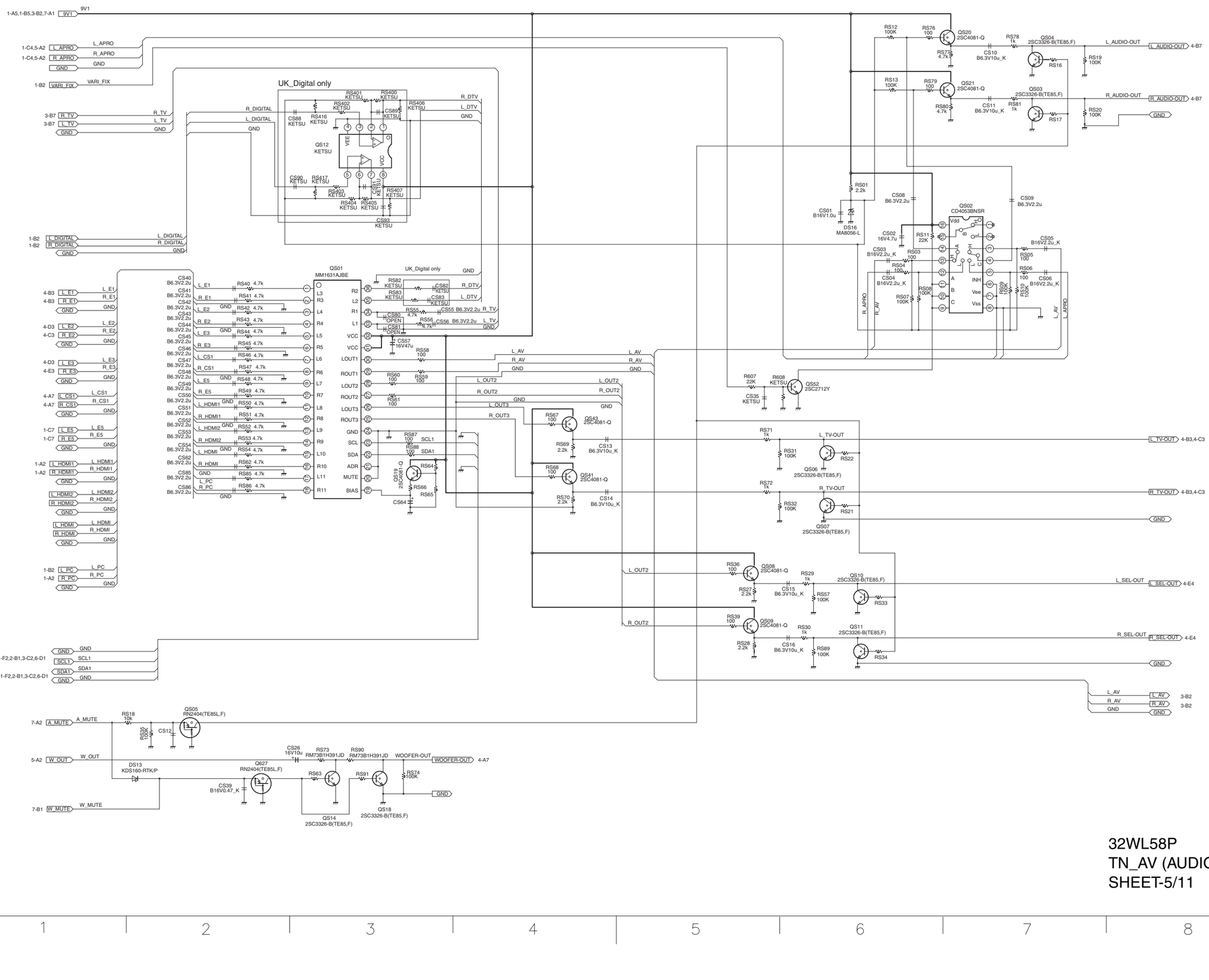


32WL58P
TN_AV (TIF)
SHEET-3/11





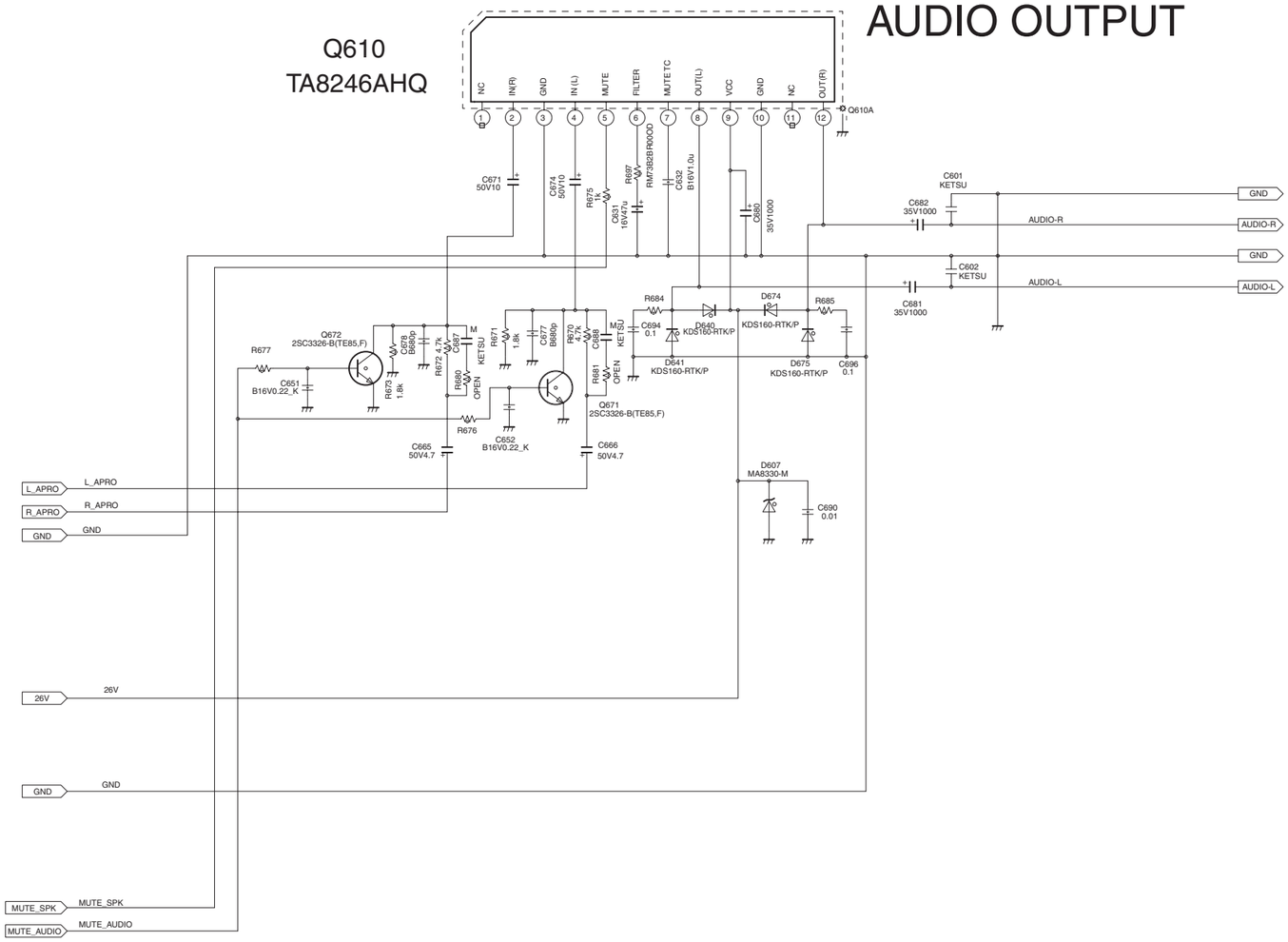
32WL58P
TN_AV (AV-TERMINAL)
SHEET-4/11



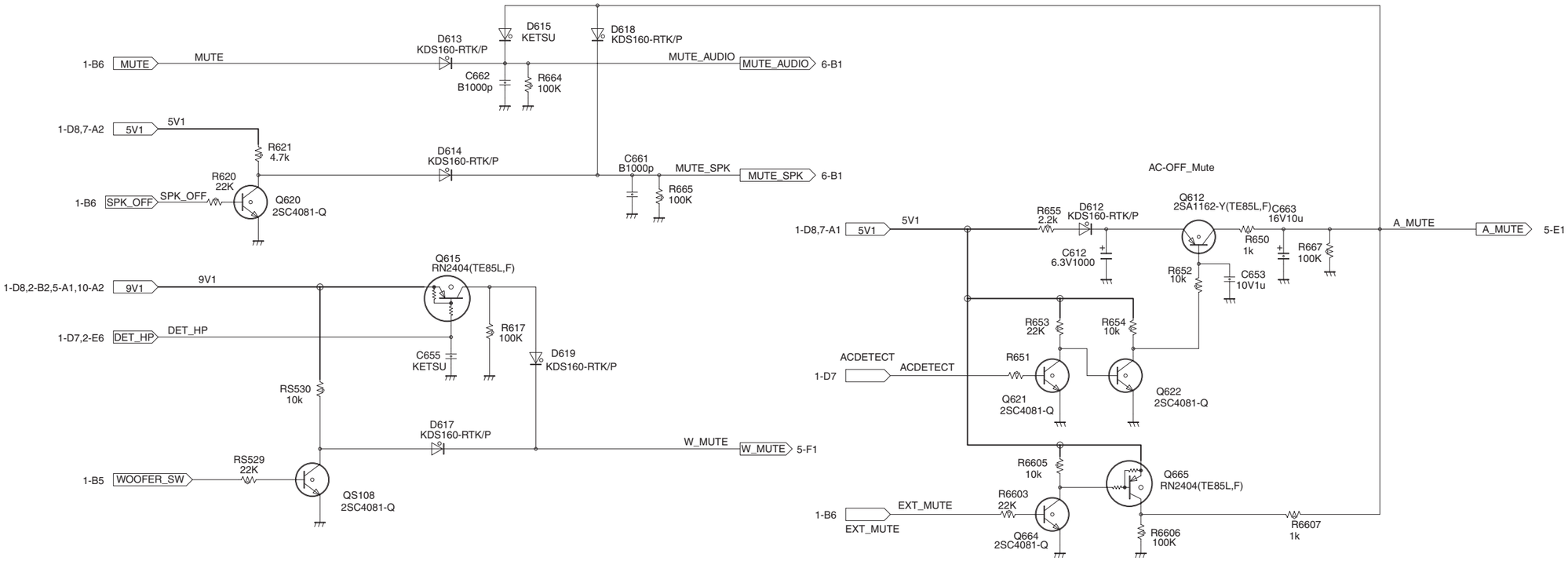
32WL58P
TN_AV (AUDIO-SW)
SHEET-5/11

AUDIO OUTPUT

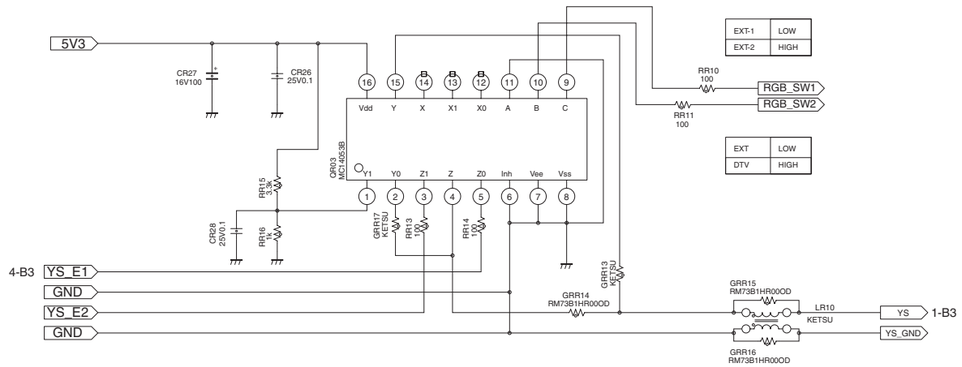
Q610
TA8246AHQ



32WL58P
TN_AV (A-OUT)
SHEET-6/11

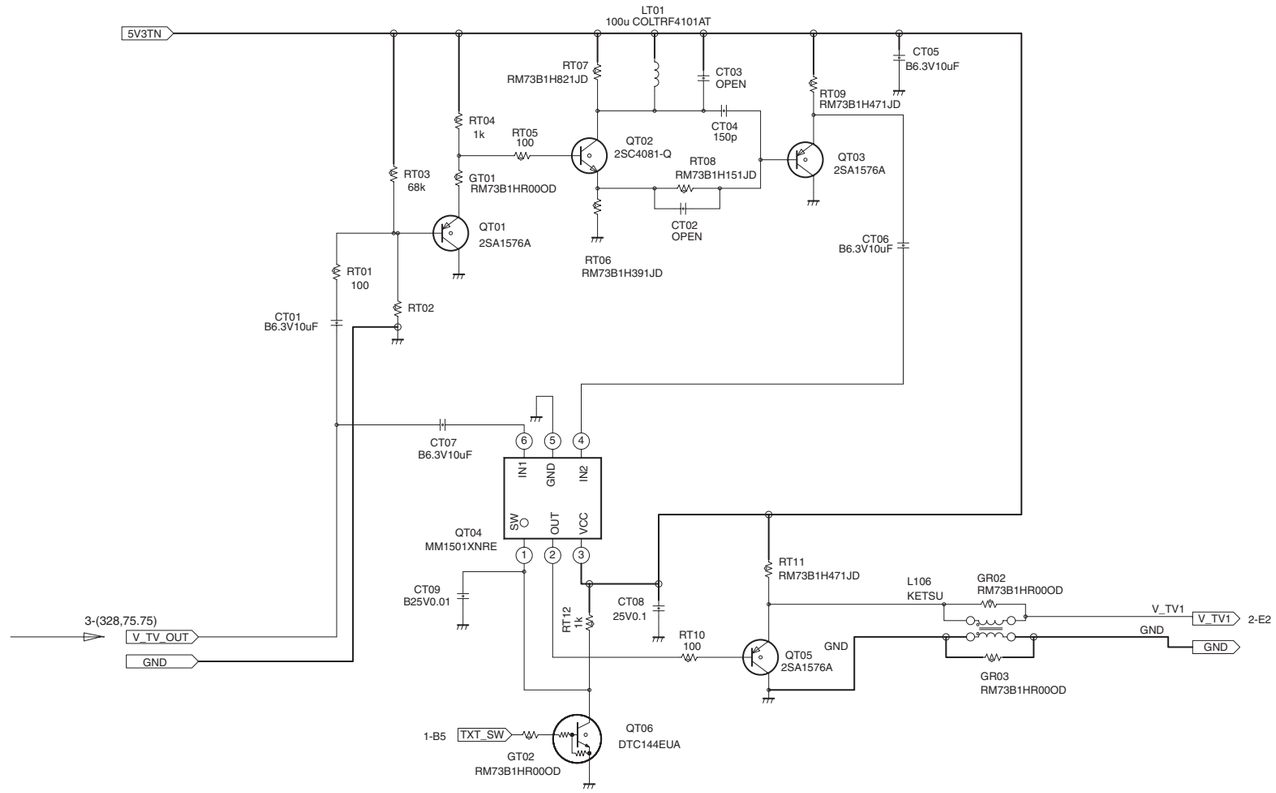


32WL58P
TN_AV (AUDIO MUTE)
SHEET-7/11

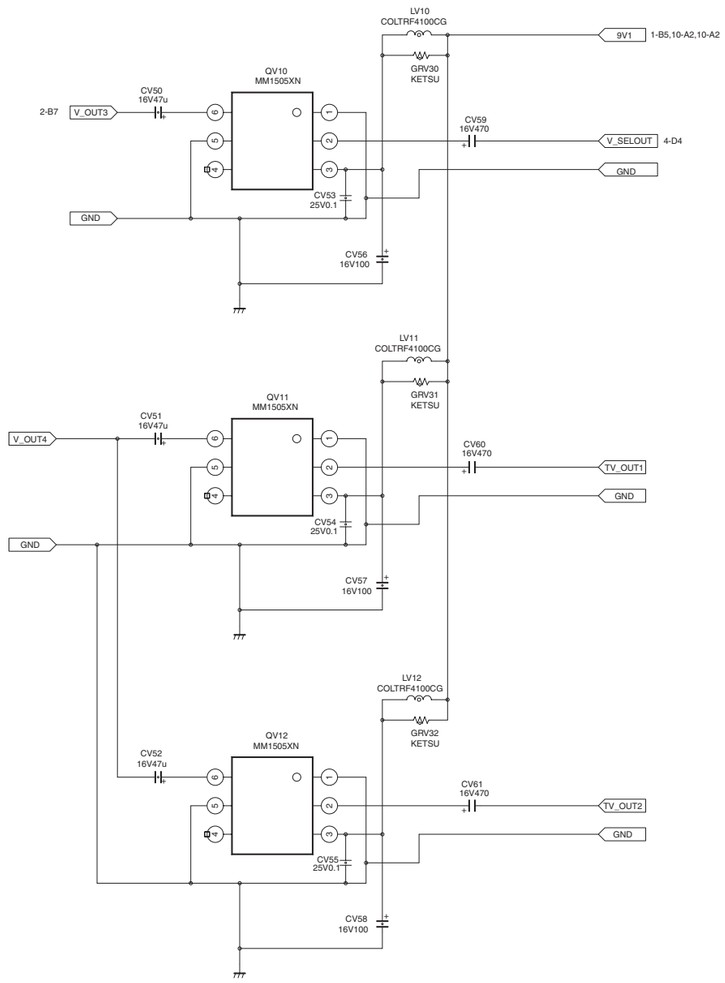


32WL58P
TN_AV (RGB_SW)
SHEET-8/11

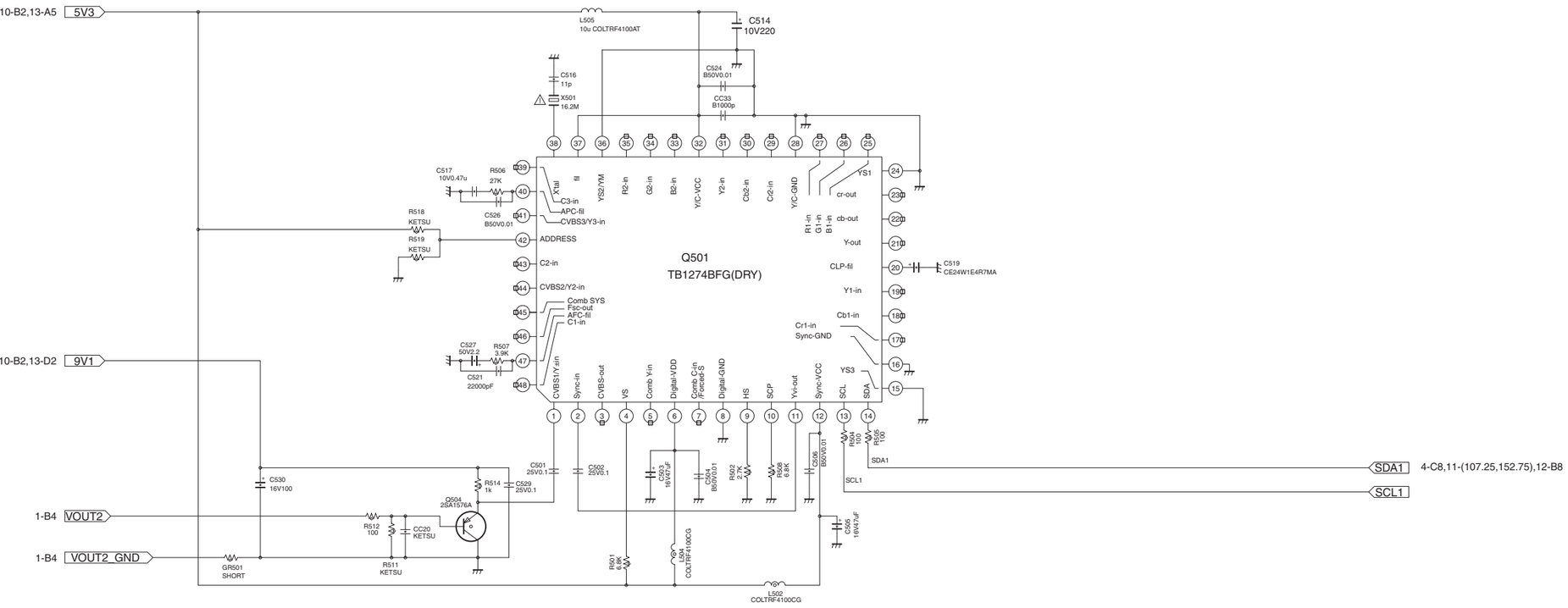
FROM TIF



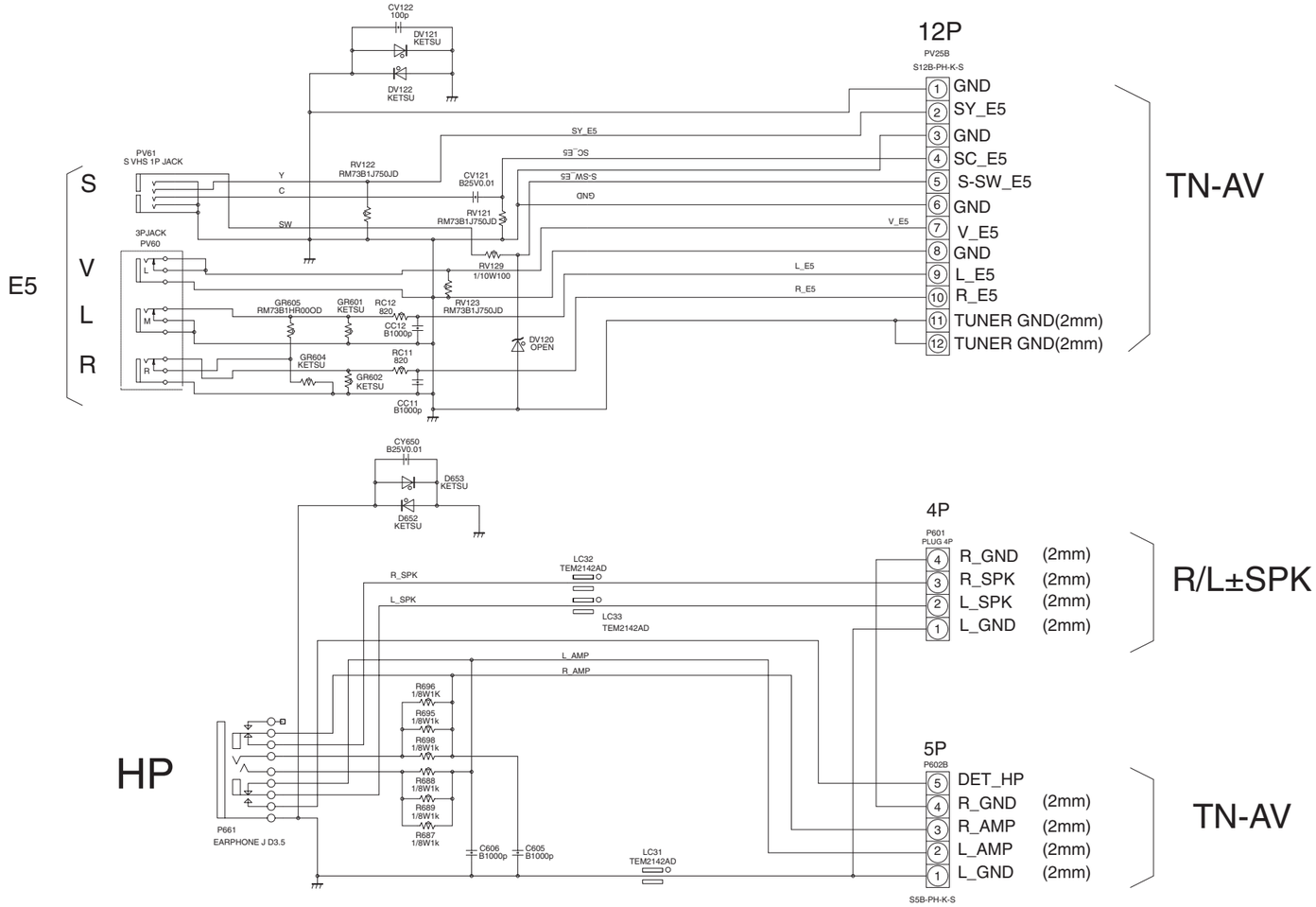
32WL58P
TN_AV (TEXT SW)
SHEET-9/11



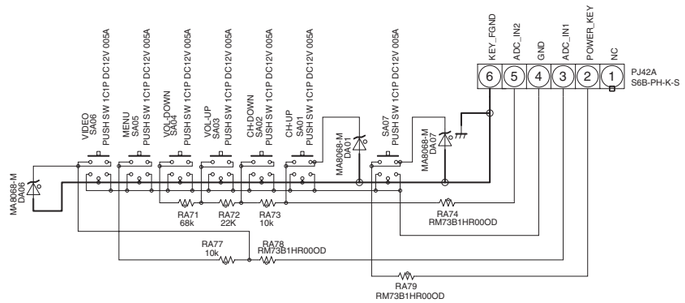
32WL58P
TN_AV (TV-OUT)
SHEET-10/11



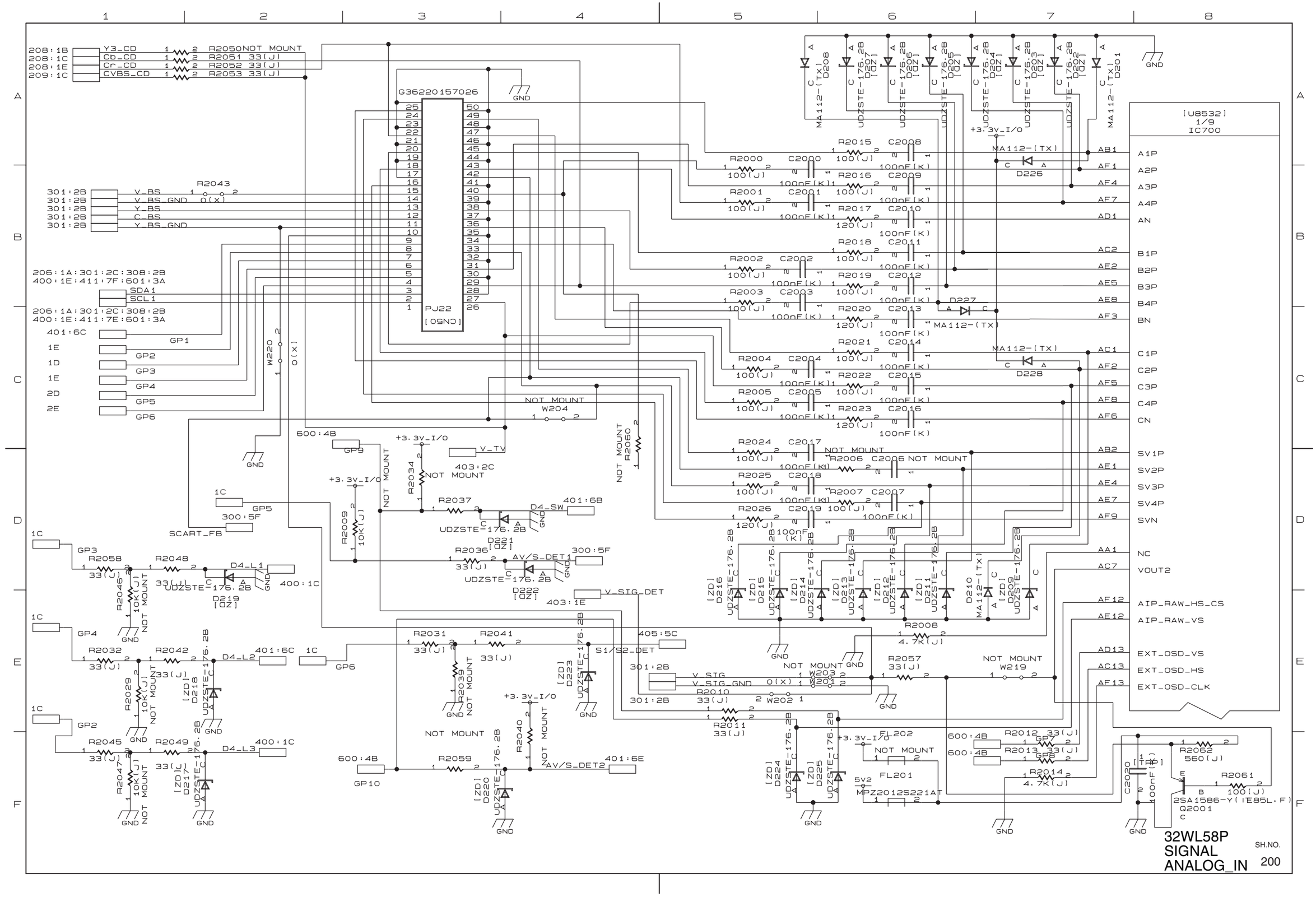
32WL58P
TN_AV (VIDEO CHROMA)
SHEET-11/11

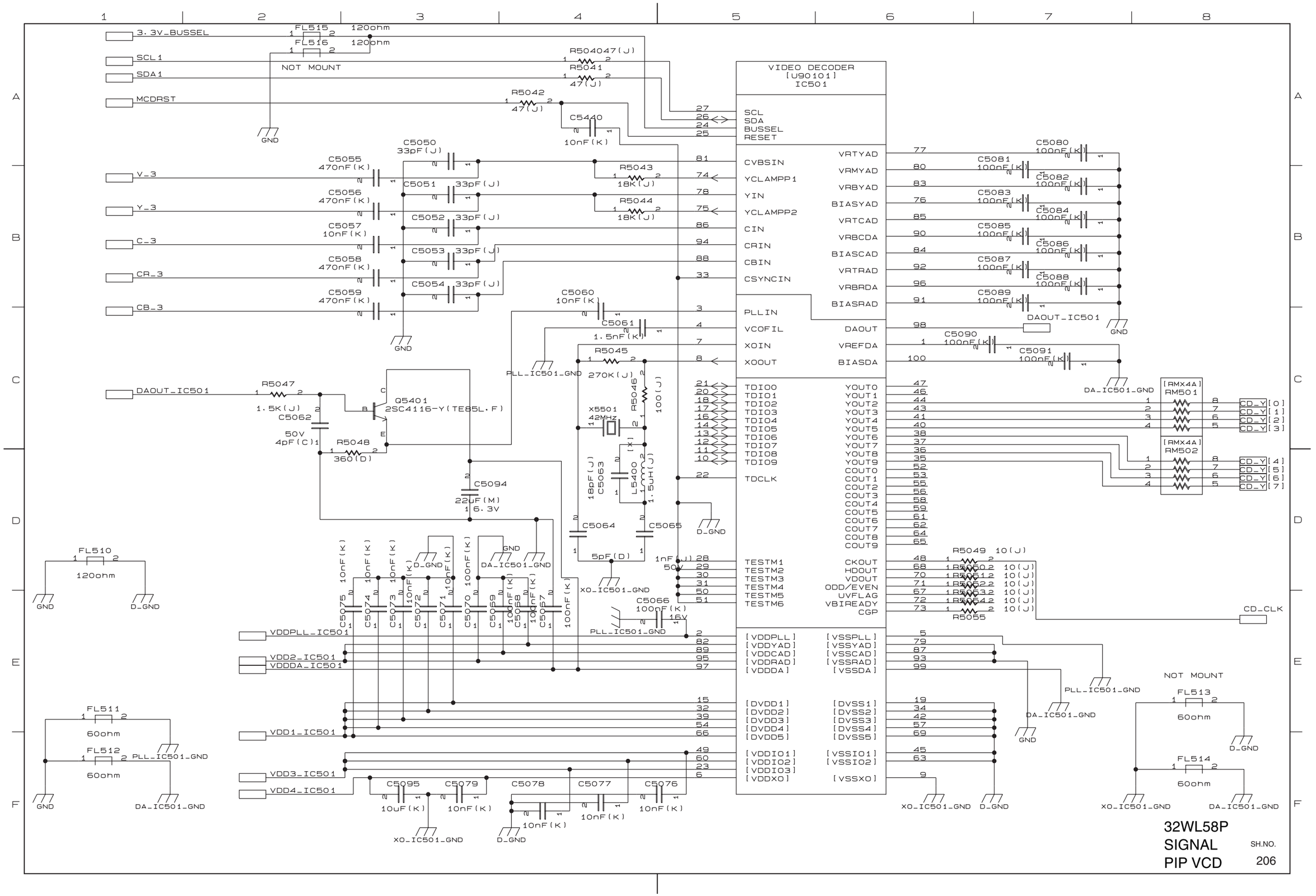


32WL58P
FRONT-AV


KEY_FGND
ADC_IN2
GND
ADC_IN1
POWER_KEY
NC
PJ42A
S6B-PH-K-S
VIDEO
SA06
MENU
SA05
VOL-DOWN
SA04
VOL-UP
SA03
CH-DOWN
SA02
CH-UP
SA01
SA07
PUSH SW 1C1P DC12V 005A
MA8068-M
DA05
MA8068-M
DA06
MA8068-M
DA07
RA71
68k
RA72
22K
RA73
10k
RA74
RM73B1HR00OD
RA77
10k
RA78
RM73B1HR00OD
RA79
RM73B1HR00OD

32WL58P
KEY


208:1B Y3_CD R2050NOT MOUNT
208:1C CB_CD R2051 33(J)
208:1E CR_CD R2052 33(J)
209:1C CVBS_CD R2053 33(J)
G36220157026
PJ22
[CN50]
GND
R2043
301:2B V_BS
301:2B V_BS_GND
301:2B Y_BS
301:2B C_BS
301:2B Y_BS_GND
206:1A:301:2C:308:2B
400:1E:411:7F:601:3A
SDA1
SCL1
206:1A:301:2C:308:2B
400:1E:411:7E:601:3A
401:6C GP1
1E GP2
1D GP3
1E GP4
2D GP5
2E GP6
W220
NOT MOUNT W204
600:4B GP9
+3.3V_I/O
V_TV
403:2C
NOT MOUNT R2034
1C GP5
300:5F SCART_FB
+3.3V_I/O
R2009 10K(J)
NOT MOUNT
R2037
D4_SW
401:6B
UDZSTE-176.2B
D221 [DZ]
R2036 33(J)
AV/S_DET1
300:5F
D222 [DZ]
V_SIG_DET
403:1E
1C GP3
R2058 33(J)
R2048 33(J)
D4_L1
R2046 10K(J)
NOT MOUNT
D219 [DZ]
400:1C
1C GP4
R2032 33(J)
R2042 33(J)
D4_L2
401:6C
R2029 10K(J)
NOT MOUNT
[ZD] D218
1C GP2
R2045 33(J)
R2049 33(J)
D4_L3
400:1C
R2047 10K(J)
NOT MOUNT
[ZD] D217
R2031 33(J)
R2041 33(J)
405:5C
S1/S2_DET
R2039 NOT MOUNT
[ZD] D223
NOT MOUNT
R2040 NOT MOUNT
600:4B GP10
R2059
AV/S_DET2
401:6E
[ZD] D220
301:2B V_SIG
V_SIG_GND
R2010 33(J)
NOT MOUNT W203
W201
W202
R2011 33(J)
R2057 33(J)
NOT MOUNT W219
R2008 4.7K(J)
[ZD] D224
[ZD] D225
FL202
NOT MOUNT
FL201
5V2
MPZ2012S221AT
600:4B GP7
600:4B GP8
R2012 33(J)
R2013 33(J)
R2014 4.7K(J)
C2020 100nF(K)
R2062 560(J)
R2061 100(J)
2SA1586-Y(IE85L.F)
Q2001
MA112-(TX) D208
UDZSTE-176.2B [DZ] D207
D206
D205
D204
D203
D202
MA112-(TX) D201
R2015 C2008 100(J) 100nF(K)
R2000 C2000 100(J) 100nF(K)
R2016 C2009 100(J) 100nF(K)
R2001 C2001 100(J) 100nF(K)
R2017 C2010 120(J) 100nF(K)
R2018 C2011 100(J) 100nF(K)
R2002 C2002 100(J) 100nF(K)
R2019 C2012 100(J) 100nF(K)
R2003 C2003 100(J) 100nF(K)
R2020 C2013 120(J) 100nF(K)
R2021 C2014 100(J) 100nF(K)
R2004 C2004 100(J) 100nF(K)
R2022 C2015 100(J) 100nF(K)
R2005 C2005 100(J) 100nF(K)
R2023 C2016 120(J) 100nF(K)
R2024 C2017 100(J) NOT MOUNT
R2006 C2006 NOT MOUNT
R2025 C2018 100(J) 100nF(K)
R2007 C2007 100(J) 100nF(K)
R2026 C2019 120(J) 100nF(K)
D226
D227
D228
[ZD] D216
[ZD] D215
[ZD] D214
[ZD] D213
[ZD] D212
[ZD] D211
D210 MA112-(TX)
[ZD] D209
[U8532] 1/9 IC700
A1P AB1
A2P AF1
A3P AF4
A4P AF7
AN AD1
B1P AC2
B2P AE2
B3P AE5
B4P AE8
BN AF3
C1P AC1
C2P AF2
C3P AF5
C4P AF8
CN AF6
SV1P AB2
SV2P AE1
SV3P AE4
SV4P AE7
SVN AF9
NC AA1
VOUT2 AC7
AIP_RAW_HS_CS AF12
AIP_RAW_VS AE12
EXT_OSD_VS AD13
EXT_OSD_HS AC13
EXT_OSD_CLK AF13
32WL58P
SIGNAL
ANALOG_IN
SH.NO.
200


VIDEO DECODER
[U90101]
IC501
3.3V_BUSSEL
SCL1
SDA1
MCDRST
FL515 120ohm
FL516 120ohm
NOT MOUNT
R504047(J)
R5041 47(J)
R5042 47(J)
C5440 10nF(K)
SCL
SDA
BUSSEL
RESET
V_3
Y_3
C_3
CR_3
CB_3
C5050 33pF(J)
C5055 470nF(K)
C5051 33pF(J)
C5056 470nF(K)
C5052 33pF(J)
C5057 10nF(K)
C5053 33pF(J)
C5058 470nF(K)
C5054 33pF(J)
C5059 470nF(K)
R5043 18K(J)
R5044 18K(J)
CVBSIN
YCLAMPP1
YIN
YCLAMPP2
CIN
CRIN
CBIN
CSYNCIN
C5060 10nF(K)
C5061 1.5nF(K)
R5045 270K(J)
PLLIN
VCOFIL
XOIN
XOOUT
PLL_IC501_GND
DAOUT_IC501
R5047 1.5K(J)
C5062 50V 4pF(C)
R5048 360(D)
Q5401 2SC4116-Y(TE85L.F)
X5501 42MHz
R5046 100(J)
C5063 18pF(J)
L5400 1.5uH(J)
C5064
C5065
5pF(D)
C5094 22uF(M) 16.3V
D_GND
XO_IC501_GND
C5066 100nF(K) 16V
PLL_IC501_GND
TDIO0
TDIO1
TDIO2
TDIO3
TDIO4
TDIO5
TDIO6
TDIO7
TDIO8
TDIO9
TDCLK
TESTM1
TESTM2
TESTM3
TESTM4
TESTM5
TESTM6
YOUT0
YOUT1
YOUT2
YOUT3
YOUT4
YOUT5
YOUT6
YOUT7
YOUT8
YOUT9
COUT0
COUT1
COUT2
COUT3
COUT4
COUT5
COUT6
COUT7
COUT8
COUT9
CKOUT
HDOUT
VDOUT
ODD/EVEN
UVFLAG
VBIREADY
CGP
VRTYAD
VRMYAD
VRBYAD
BIASYAD
VRTCAD
VRBCDA
BIASCAD
VRTRAD
VRBRDA
BIASRAD
DAOUT
VREFDA
BIASDA
C5080 100nF(K)
C5081 100nF(K)
C5082 100nF(K)
C5083 100nF(K)
C5084 100nF(K)
C5085 100nF(K)
C5086 100nF(K)
C5087 100nF(K)
C5088 100nF(K)
C5089 100nF(K)
DAOUT_IC501
C5090 100nF(K)
C5091 100nF(K)
DA_IC501_GND
[RMX4A] RM501
[RMX4A] RM502
CD_Y[0]
CD_Y[1]
CD_Y[2]
CD_Y[3]
CD_Y[4]
CD_Y[5]
CD_Y[6]
CD_Y[7]
R5049 10(J)
R5050 10(J)
R5051 10(J)
R5052 10(J)
R5053 10(J)
R5054 10(J)
R5055
CD_CLK
FL510 120ohm
FL511 60ohm
FL512 60ohm
FL513 60ohm
FL514 60ohm
C5075 10nF(K)
C5074 10nF(K)
C5073 10nF(K)
C5072 10nF(K)
C5071 10nF(K)
C5070 10nF(K)
C5069 100nF(K)
C5068 100nF(K)
C5067 100nF(K)
VDDPLL_IC501
VDD2_IC501
VDDDA_IC501
VDD1_IC501
VDD3_IC501
VDD4_IC501
[VDDPLL]
[VDDYAD]
[VDDCAD]
[VDDRAD]
[VDDDA]
[DVDD1]
[DVDD2]
[DVDD3]
[DVDD4]
[DVDD5]
[VDDIO1]
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[VDDIO3]
[VDDXO]
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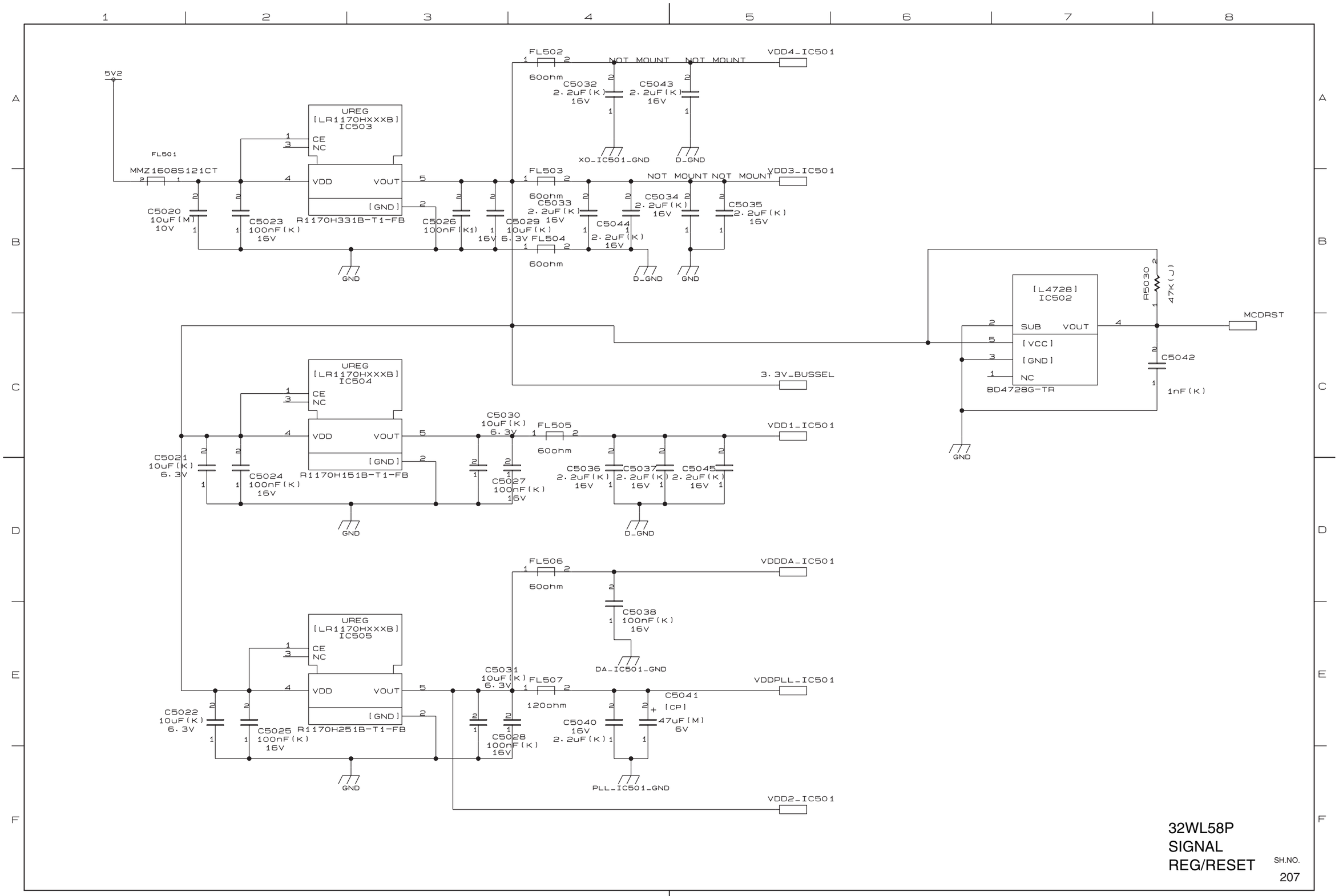
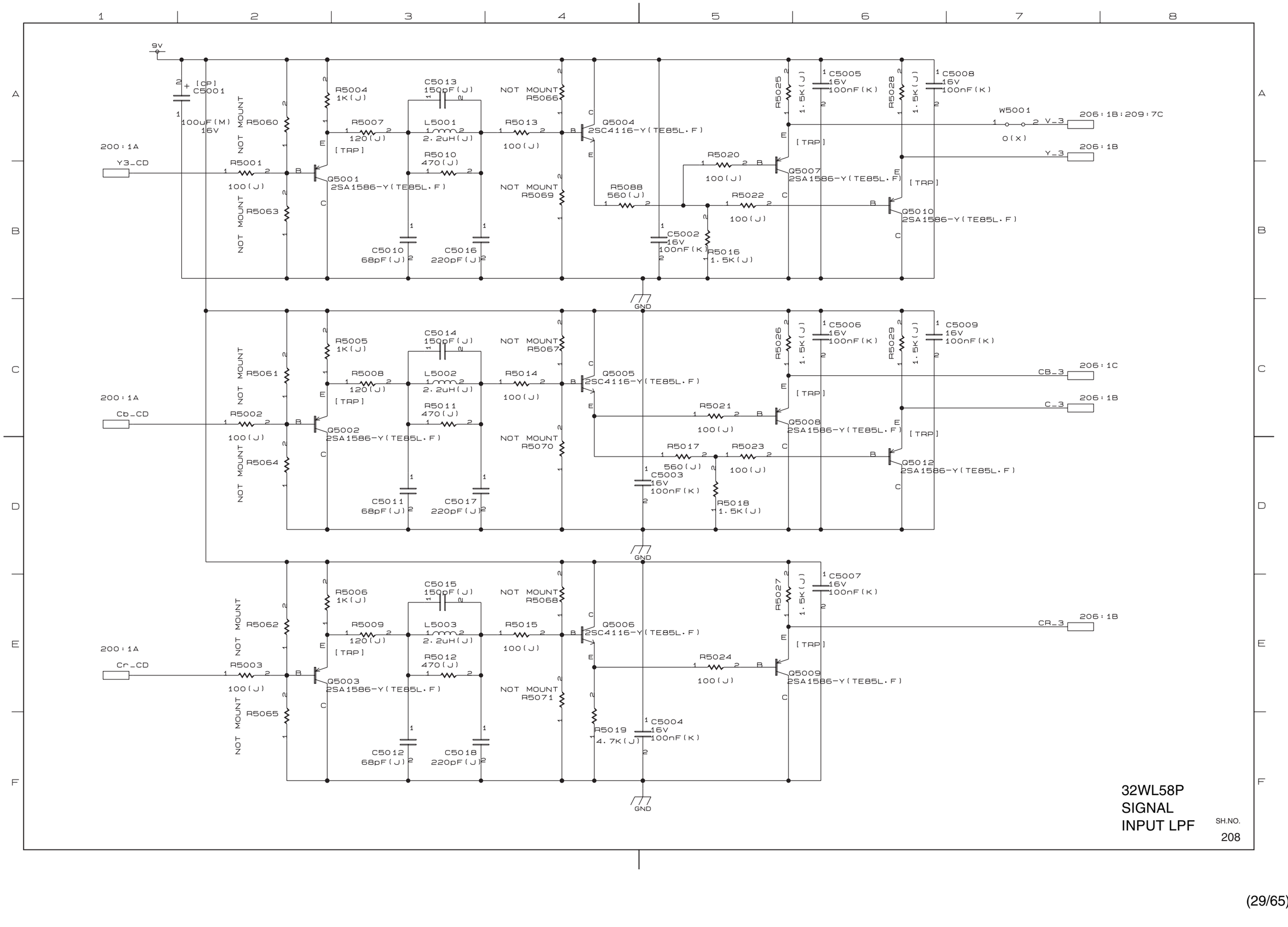
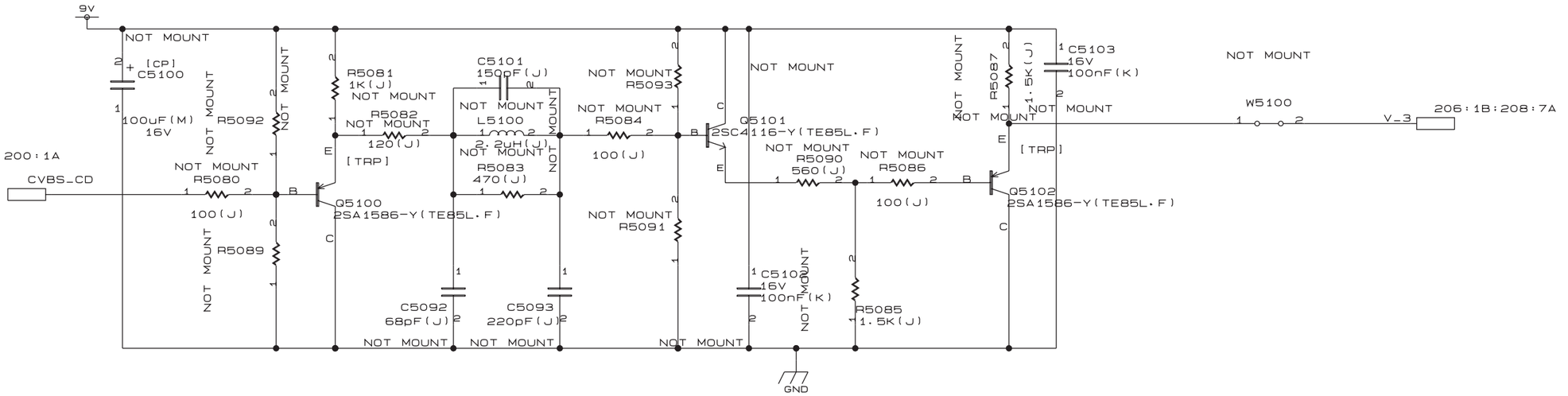
[VSSYAD]
[VSSCAD]
[VSSRAD]
[VSSDA]
[DVSS1]
[DVSS2]
[DVSS3]
[DVSS4]
[DVSS5]
[VSSIO1]
[VSSIO2]
[VSSXO]
C5095 10uF(K)
C5079 10nF(K)
C5078 10nF(K)
C5077 10nF(K)
C5076 10nF(K)
GND
D_GND
DA_IC501_GND
PLL_IC501_GND
XO_IC501_GND
32WL58P
SIGNAL
PIP VCD
SH.NO.
206



32WL58P
SIGNAL
REG/RESET

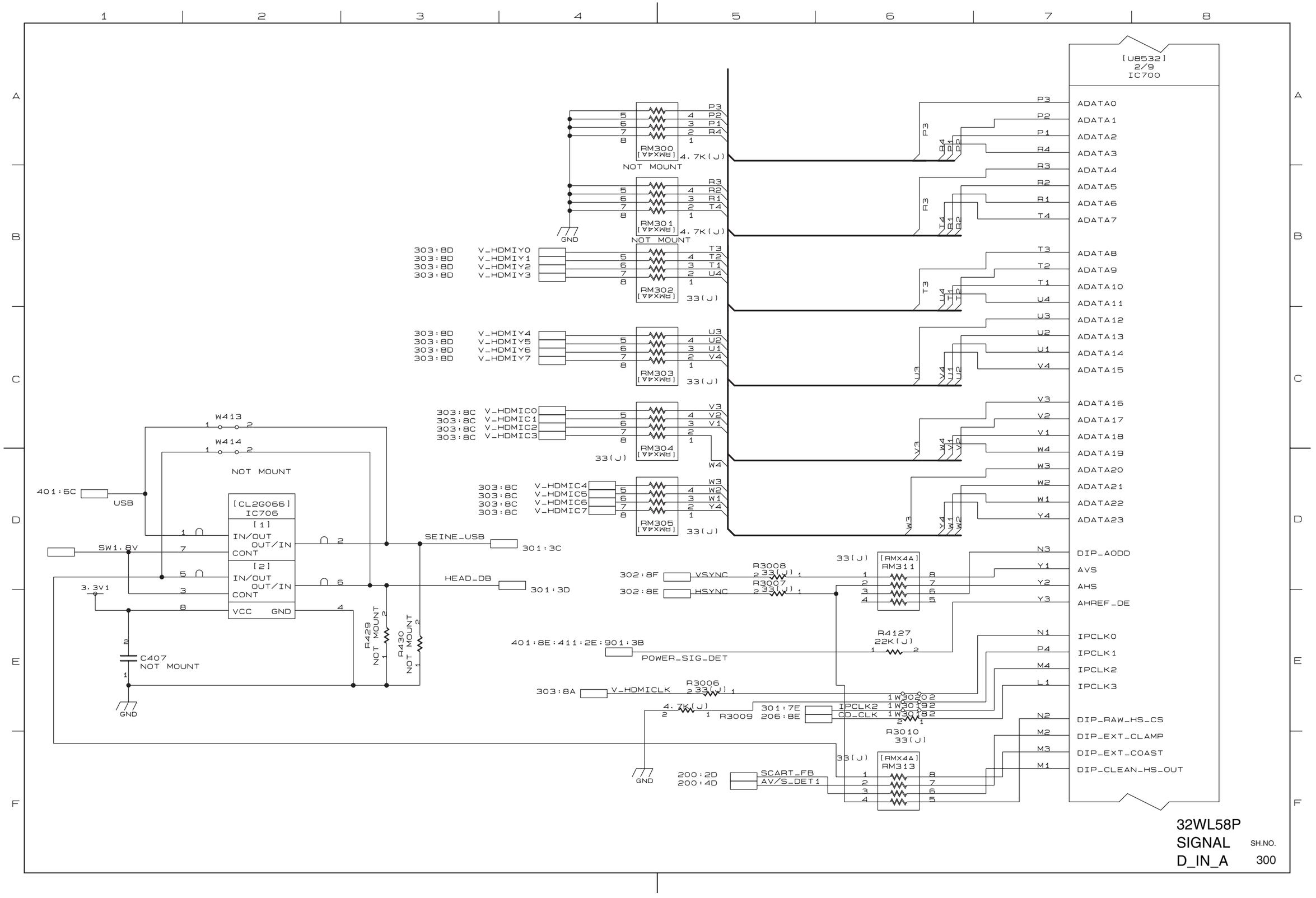
SH.NO.
207


9V
C5001
[cp]
100uF(M)
16V
R5060
NOT MOUNT
R5004
1K(J)
C5013
150pF(J)
R5007
120(J)
L5001
2.2uH(J)
[TRP]
R5001
100(J)
Y3_CD
200:1A
Q5001
2SA1586-Y(TE85L.F)
R5010
470(J)
R5063
C5010
68pF(J)
C5016
220pF(J)
NOT MOUNT
R5066
R5013
100(J)
Q5004
2SC4116-Y(TE85L.F)
NOT MOUNT
R5069
R5088
560(J)
C5002
16V
100nF(K)
R5016
1.5K(J)
R5025
1.5K(J)
C5005
16V
100nF(K)
R5028
1.5K(J)
C5008
16V
100nF(K)
W5001
206:1B:209:7C
V_3
O(X)
206:1B
Y_3
R5020
100(J)
Q5007
2SA1586-Y(TE85L.F)
R5022
100(J)
Q5010
2SA1586-Y(TE85L.F)
GND
R5005
1K(J)
C5014
150pF(J)
R5008
120(J)
L5002
2.2uH(J)
R5061
NOT MOUNT
R5001
100(J)
Cb_CD
200:1A
Q5002
2SA1586-Y(TE85L.F)
R5011
470(J)
R5064
C5011
68pF(J)
C5017
220pF(J)
NOT MOUNT
R5067
R5014
100(J)
Q5005
2SC4116-Y(TE85L.F)
NOT MOUNT
R5070
R5021
100(J)
R5017
560(J)
C5003
16V
100nF(K)
R5023
100(J)
R5018
1.5K(J)
R5026
1.5K(J)
C5006
16V
100nF(K)
R5029
1.5K(J)
C5009
16V
100nF(K)
CB_3
206:1C
C_3
206:1B
Q5008
2SA1586-Y(TE85L.F)
Q5012
2SA1586-Y(TE85L.F)
GND
R5006
1K(J)
C5015
150pF(J)
R5009
120(J)
L5003
2.2uH(J)
R5062
NOT MOUNT
R5002
100(J)
Cr_CD
200:1A
Q5003
2SA1586-Y(TE85L.F)
R5012
470(J)
R5065
C5012
68pF(J)
C5018
220pF(J)
NOT MOUNT
R5068
R5015
100(J)
Q5006
2SC4116-Y(TE85L.F)
NOT MOUNT
R5071
R5024
100(J)
R5019
4.7K(J)
C5004
16V
100nF(K)
R5027
1.5K(J)
C5007
16V
100nF(K)
CR_3
206:1B
Q5009
2SA1586-Y(TE85L.F)
32WL58P
SIGNAL
INPUT LPF
SH.NO.
208

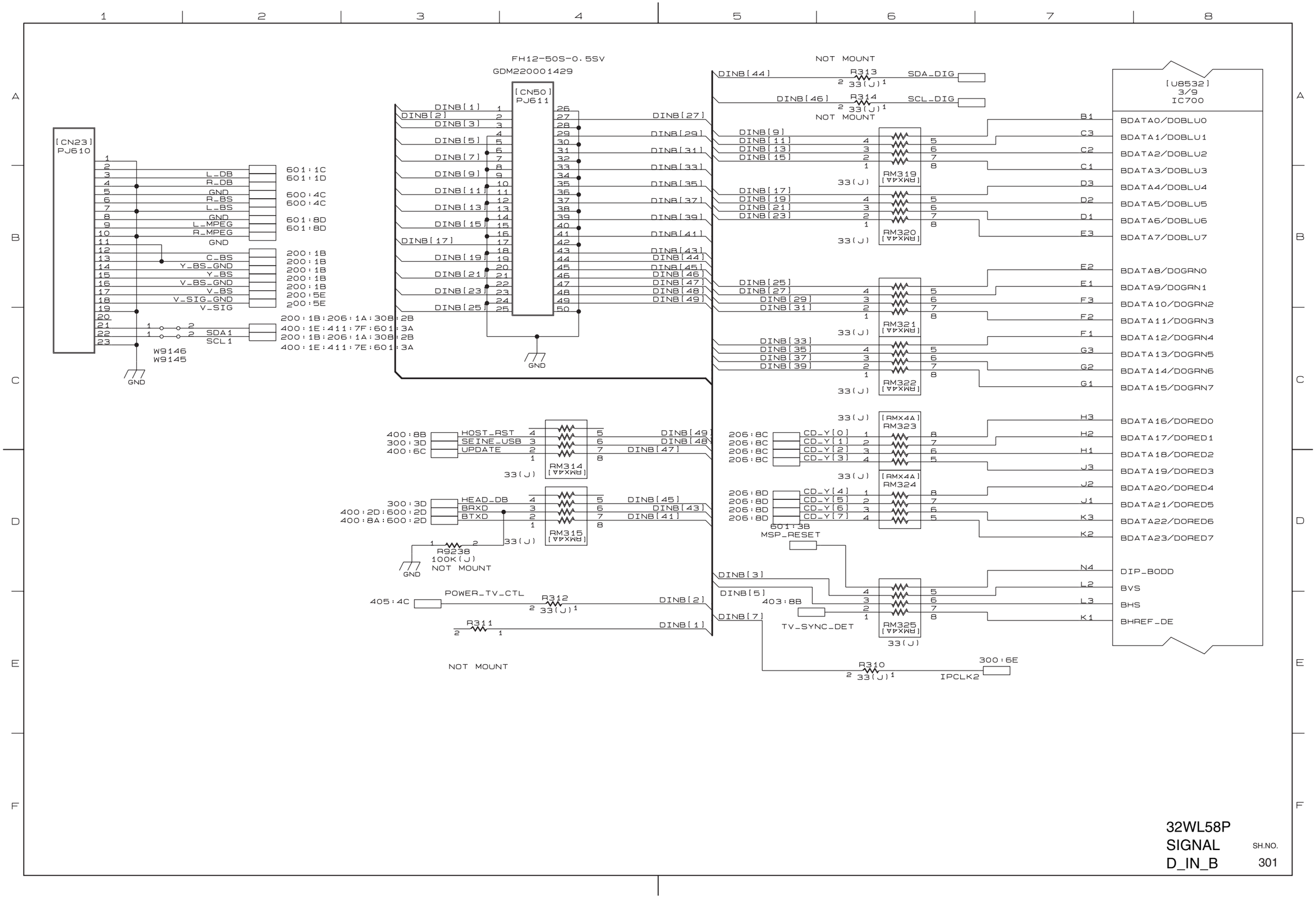


32WL58P
SIGNAL
FUNCTION

SH.NO.
209



32WL58P
SIGNAL SH.NO.
D_IN_A 300


FH12-50S-0.5SV
GDM220001429
NOT MOUNT
[CN23]
PJ610
[CN50]
PJ611
[U8532]
3/9
IC700
L_DB
R_DB
GND
R_BS
L_BS
GND
L_MPEG
R_MPEG
GND
C_BS
Y_BS_GND
Y_BS
V_BS_GND
V_BS
V_SIG_GND
V_SIG
SDA1
SCL1
W9146
W9145
601:1C
601:1D
600:4C
600:4C
601:8D
601:8D
200:1B
200:1B
200:1B
200:1B
200:1B
200:5E
200:5E
200:1B:206:1A:308:2B
400:1E:411:7F:601:3A
200:1B:206:1A:308:2B
400:1E:411:7E:601:3A
DINB[1]
DINB[2]
DINB[3]
DINB[5]
DINB[7]
DINB[9]
DINB[11]
DINB[13]
DINB[15]
DINB[17]
DINB[19]
DINB[21]
DINB[23]
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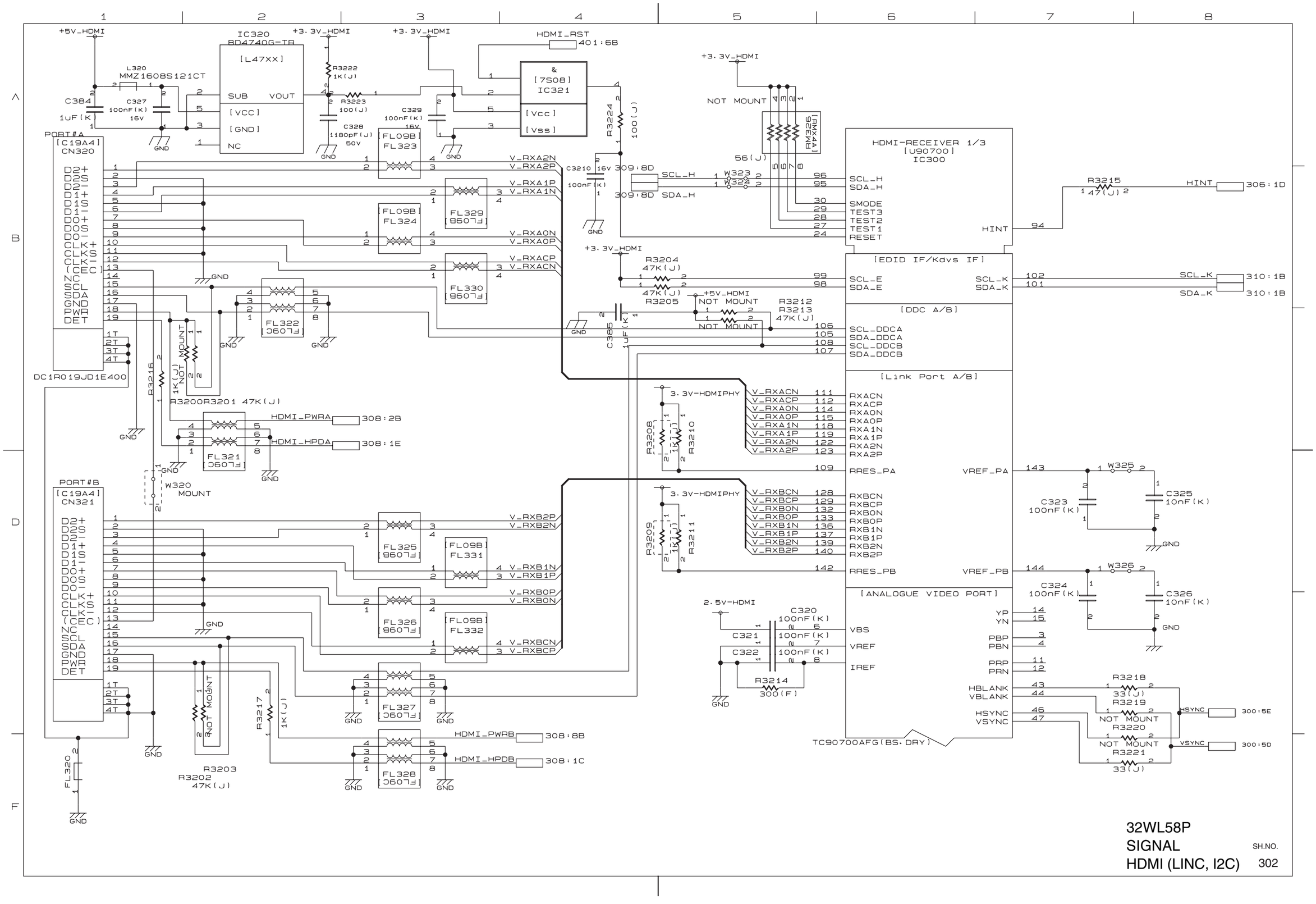
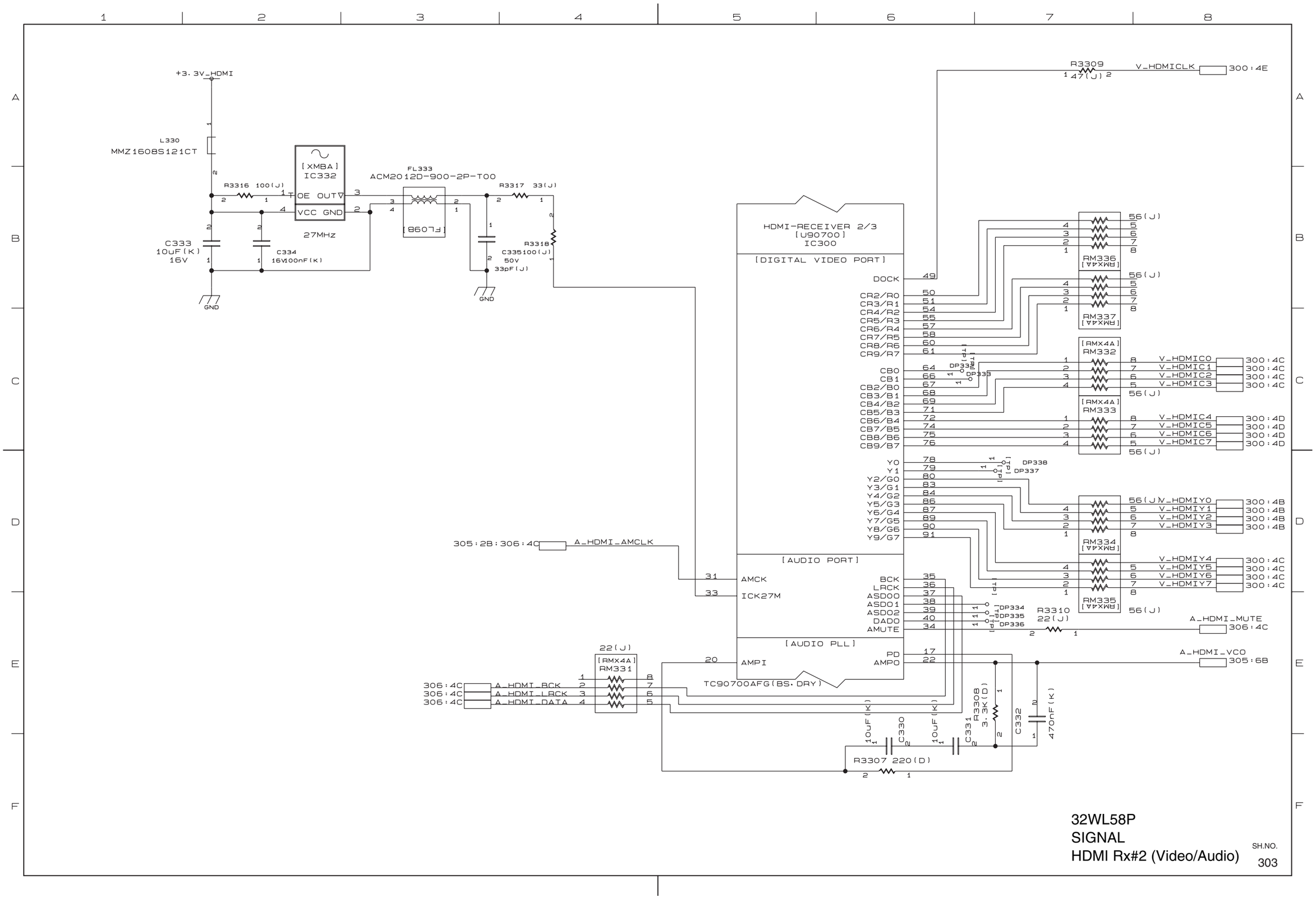
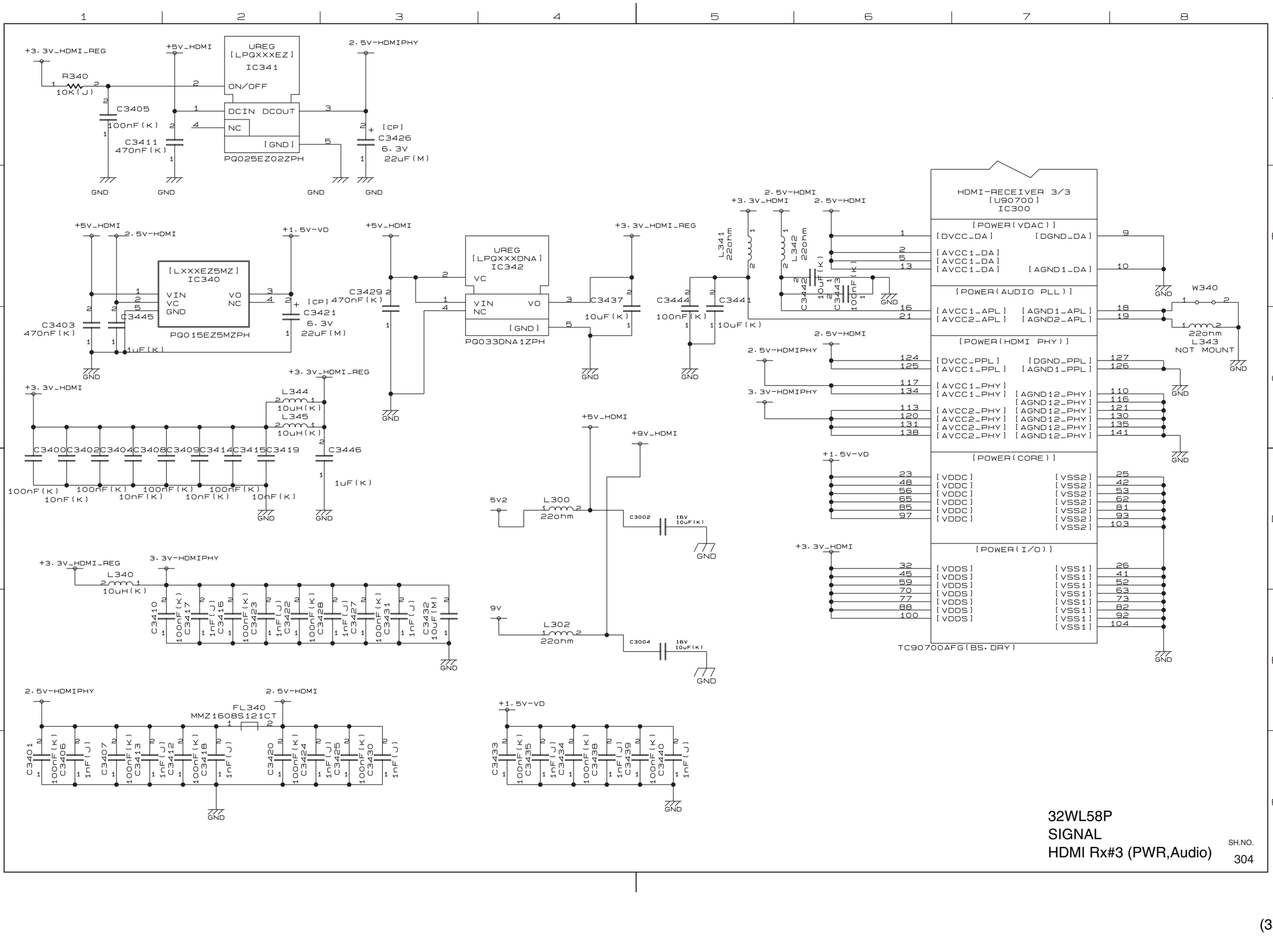
DINB[29]
DINB[31]
DINB[33]
DINB[35]
DINB[37]
DINB[39]
DINB[41]
DINB[43]
DINB[44]
DINB[45]
DINB[46]
DINB[47]
DINB[48]
DINB[49]
R313
33(J)
SDA_DIG
DINB[46]
R314
33(J)
SCL_DIG
NOT MOUNT
RM319
RM320
RM321
RM322
RM323
RM324
RM325
[RMX4A]
B1
C3
C2
C1
D3
D2
D1
E3
E2
E1
F3
F2
F1
G3
G2
G1
H3
H2
H1
J3
J2
J1
K3
K2
N4
L2
L3
K1
BDATA0/DOBLU0
BDATA1/DOBLU1
BDATA2/DOBLU2
BDATA3/DOBLU3
BDATA4/DOBLU4
BDATA5/DOBLU5
BDATA6/DOBLU6
BDATA7/DOBLU7
BDATA8/DOGRN0
BDATA9/DOGRN1
BDATA10/DOGRN2
BDATA11/DOGRN3
BDATA12/DOGRN4
BDATA13/DOGRN5
BDATA14/DOGRN6
BDATA15/DOGRN7
BDATA16/DORED0
BDATA17/DORED1
BDATA18/DORED2
BDATA19/DORED3
BDATA20/DORED4
BDATA21/DORED5
BDATA22/DORED6
BDATA23/DORED7
DIP_BODD
BVS
BHS
BHREF_DE
400:8B
300:3D
400:6C
HOST_RST
SEINE_USB
UPDATE
RM314
300:3D
400:2D:600:2D
400:8A:600:2D
HEAD_DB
BRXD
BTXD
RM315
R9238
100K(J)
NOT MOUNT
405:4C
POWER_TV_CTL
R312
33(J)
R311
NOT MOUNT
206:8C
206:8C
206:8C
206:8C
CD_Y[0]
CD_Y[1]
CD_Y[2]
CD_Y[3]
206:8D
206:8D
206:8D
206:8D
CD_Y[4]
CD_Y[5]
CD_Y[6]
CD_Y[7]
601:3B
MSP_RESET
DINB[3]
DINB[5]
DINB[7]
403:8B
TV_SYNC_DET
R310
33(J)
300:6E
IPCLK2
GND
32WL58P
SIGNAL
D_IN_B
SH.NO.
301


IC320
BD4740G-TR
[L47XX]
SUB
VOUT
[VCC]
[GND]
NC
L320
MMZ1608S121CT
C384
1uF(K)
C327
100nF(K)
16V
C328
1180pF(J)
50V
C329
100nF(K)
16V
R3222
1K(J)
R3223
100(J)
+5V_HDMI
+3.3V_HDMI
HDMI_RST
401:6B
&
[7S08]
IC321
[Vcc]
[Vss]
R3224
100(J)
C3210
100nF(K)
16V
309:8D
SCL_H
SDA_H
NOT MOUNT
RMX4A
56(J)
HDMI-RECEIVER 1/3
[U90700]
IC300
SCL_H
SDA_H
SMODE
TEST3
TEST2
TEST1
RESET
HINT
R3215
47(J)
HINT
306:1D
[EDID IF/Kdvs IF]
SCL_E
SDA_E
SCL_K
SDA_K
310:1B
R3204
47K(J)
R3205
47K(J)
R3212
R3213
47K(J)
NOT MOUNT
C385
1uF(K)
[DDC A/B]
SCL_DDCA
SDA_DDCA
SCL_DDCB
SDA_DDCB
[Link Port A/B]
RXACN
RXACP
RXAON
RXAOP
RXA1N
RXA1P
RXA2N
RXA2P
RRES_PA
RXBCN
RXBCP
RXBON
RXBOP
RXB1N
RXB1P
RXB2N
RXB2P
RRES_PB
3.3V-HDMIPHY
R3208
150(F)
R3210
R3209
R3211
V_RXACN
V_RXACP
V_RXAON
V_RXAOP
V_RXA1N
V_RXA1P
V_RXA2N
V_RXA2P
V_RXBCN
V_RXBCP
V_RXBON
V_RXBOP
V_RXB1N
V_RXB1P
V_RXB2N
V_RXB2P
VREF_PA
VREF_PB
W325
W326
C323
100nF(K)
C325
10nF(K)
C324
100nF(K)
C326
10nF(K)
[ANALOGUE VIDEO PORT]
YP
YN
PBP
PBN
PRP
PRN
HBLANK
VBLANK
HSYNC
VSYNC
R3218
33(J)
R3219
NOT MOUNT
R3220
NOT MOUNT
R3221
33(J)
HSYNC
300:5E
VSYNC
300:5D
2.5V-HDMI
C320
100nF(K)
C321
100nF(K)
C322
100nF(K)
VBS
VREF
IREF
R3214
300(F)
TC90700AFG(BS.DRY)
PORT#A
[C19A4]
CN320
PORT#B
[C19A4]
CN321
D2+
D2S
D2-
D1+
D1S
D1-
D0+
D0S
D0-
CLK+
CLKS
CLK-
(CEC)
NC
SCL
SDA
GND
PWR
DET
DC1R019JD1E400
R3216
1K(J)
NOT MOUNT
R3200R3201 47K(J)
R3217
1K(J)
R3203
R3202
47K(J)
W320
MOUNT
FL320
FL322
FL323
FL324
FL325
FL326
FL327
FL328
FL329
FL330
FL331
FL332
FL321
[FL09B]
[J607F]
HDMI_PWRA
HDMI_HPDA
HDMI_PWRB
HDMI_HPDB
308:2B
308:1E
308:8B
308:1C
GND
32WL58P
SIGNAL
HDMI (LINC, I2C)
SH.NO.
302



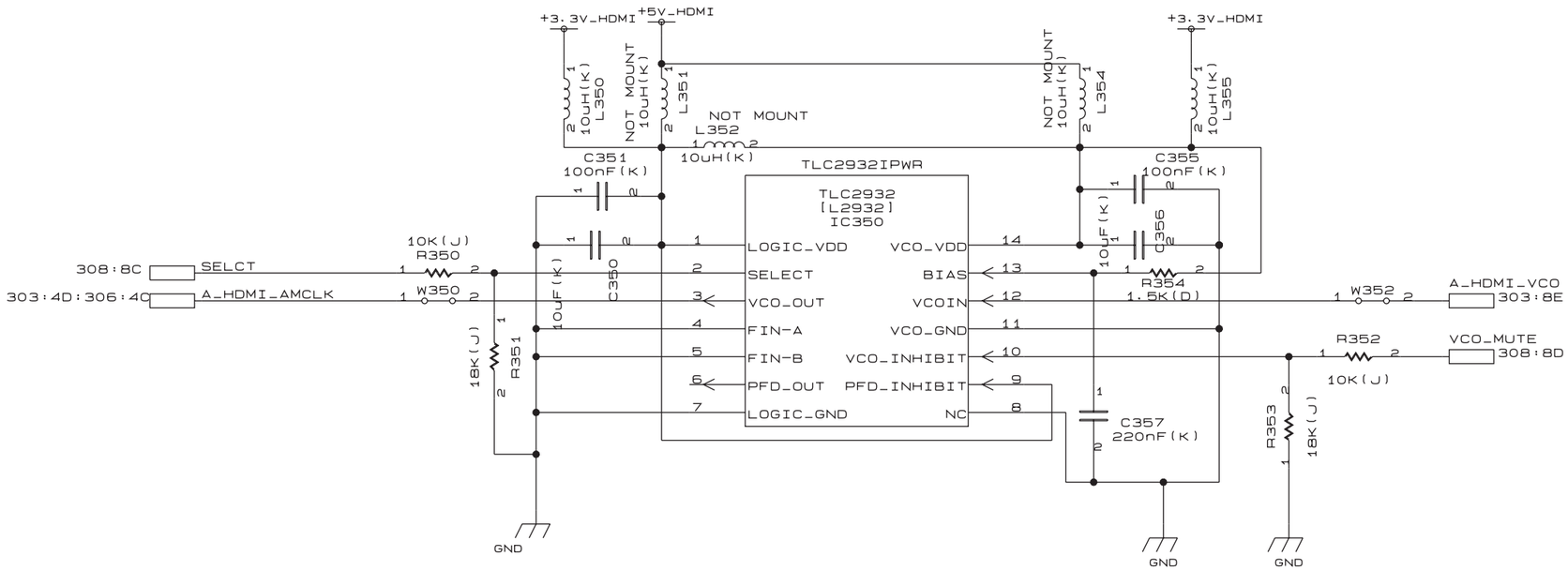
32WL58P
SIGNAL
HDMI Rx#2 (Video/Audio)

SH.NO.
303



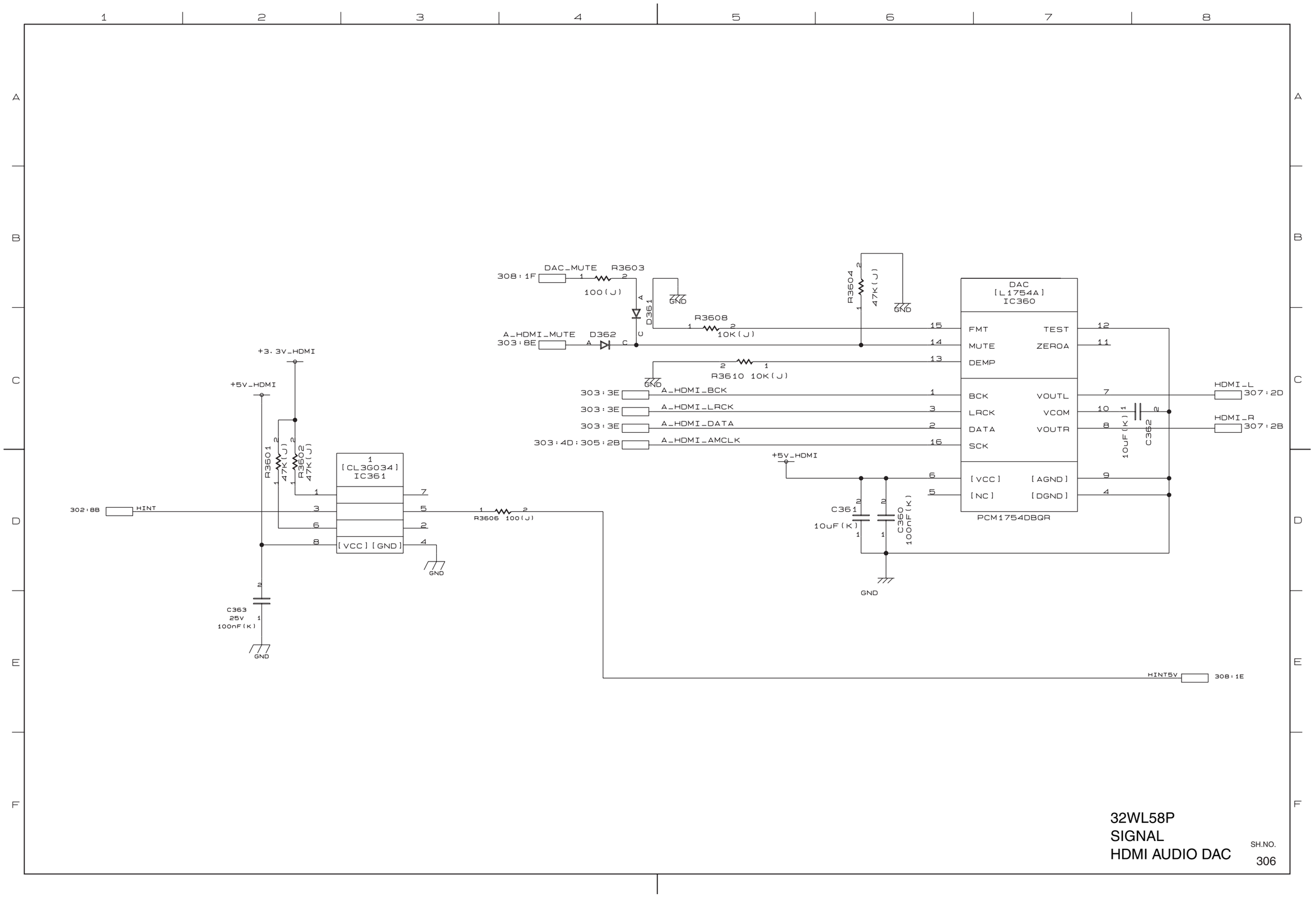
32WL58P
SIGNAL
HDMI Rx#3 (PWR,Audio)

SH.NO.
304



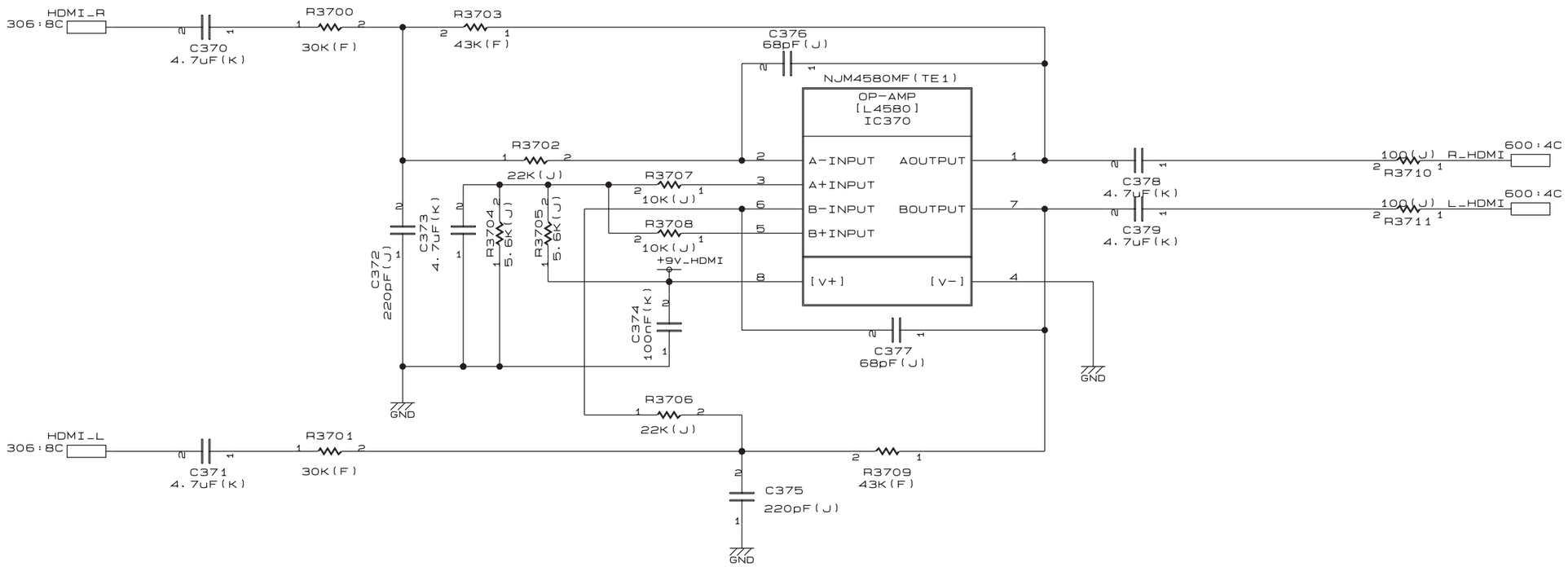
32WL58P
SIGNAL
HDMI AUDIO PLL

SH.NO.
305



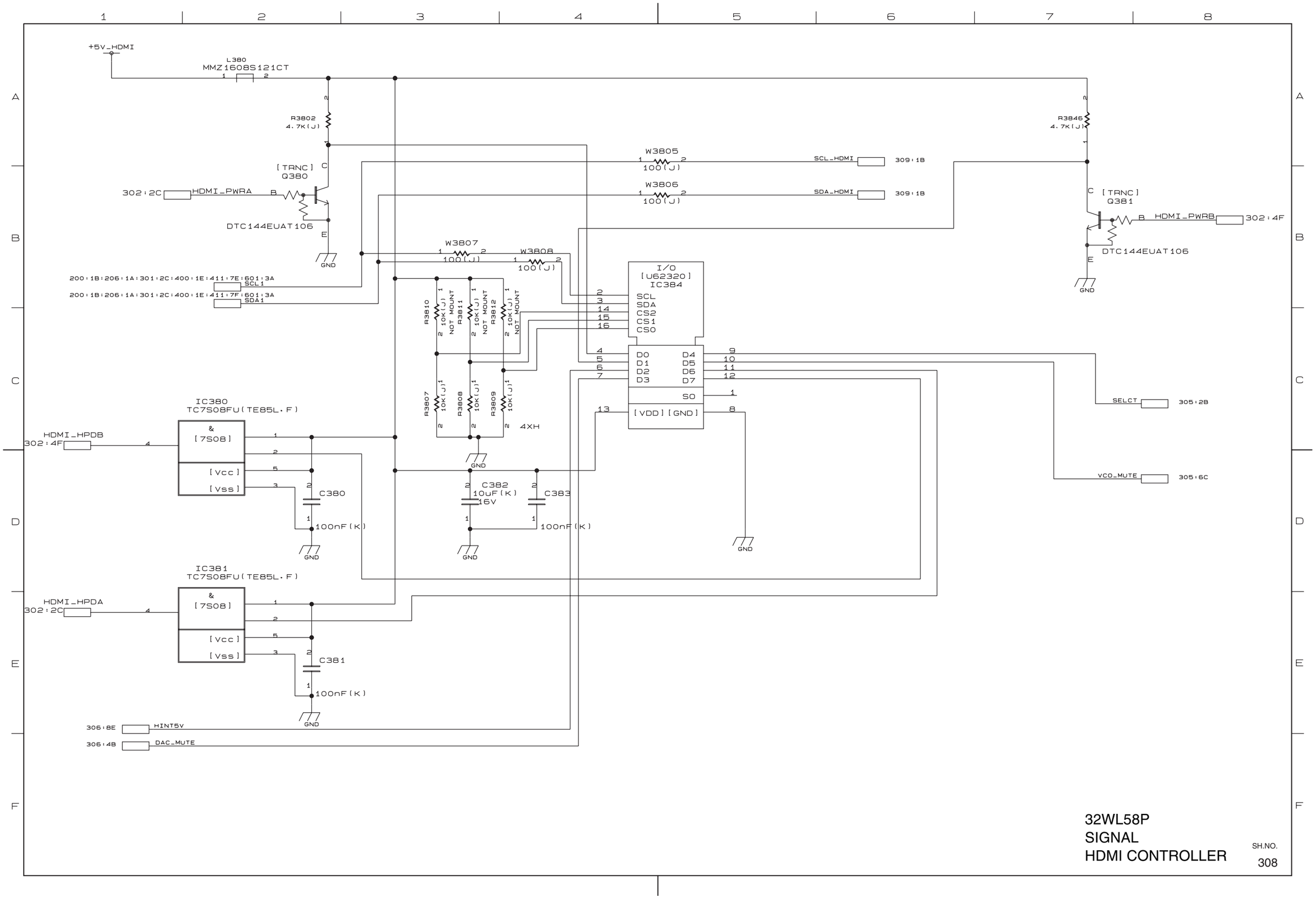
32WL58P
SIGNAL
HDMI AUDIO DAC

SH.NO.
306



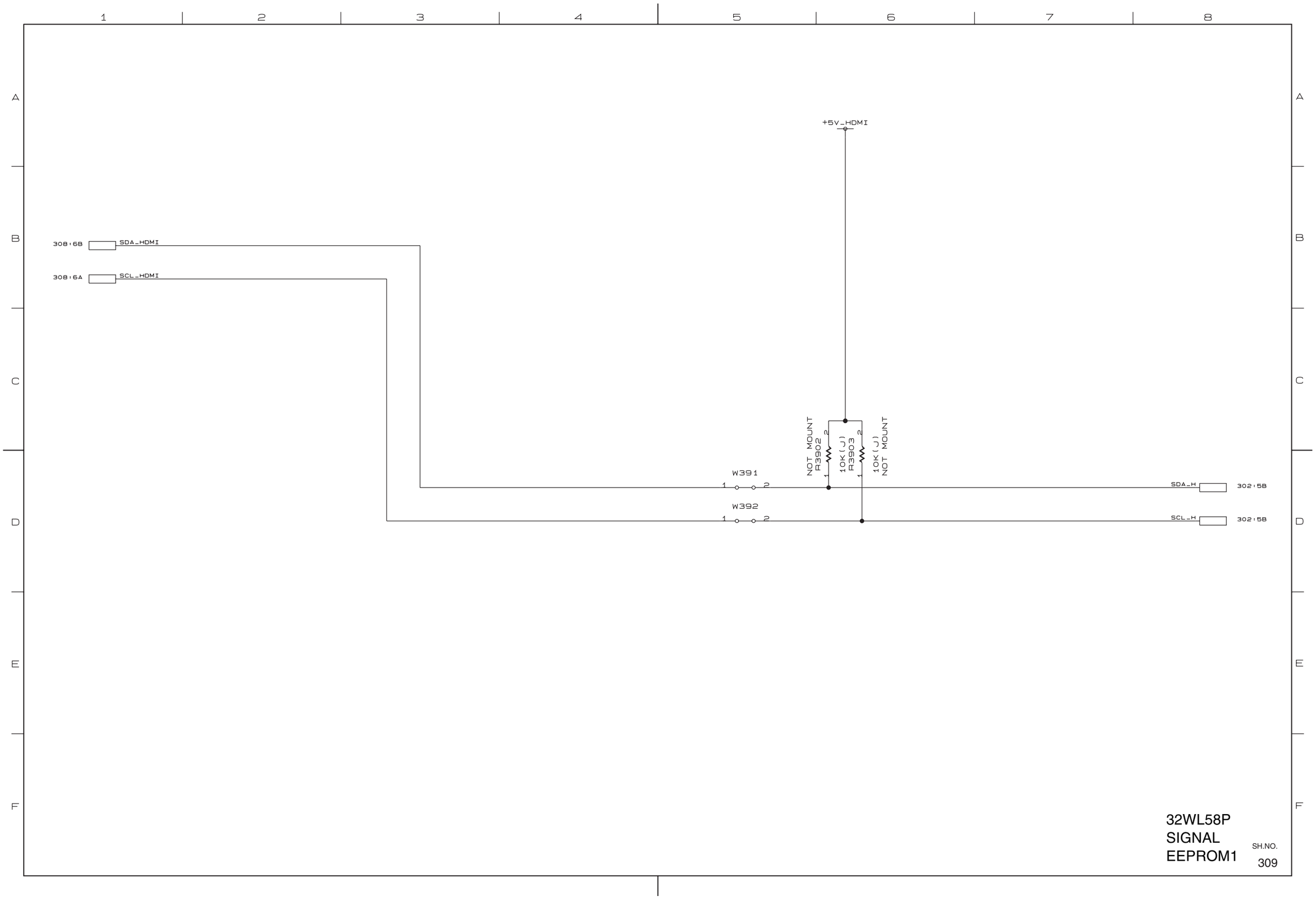
32WL58P
SIGNAL
HDMI AUDIO OUT

SH.NO.
307



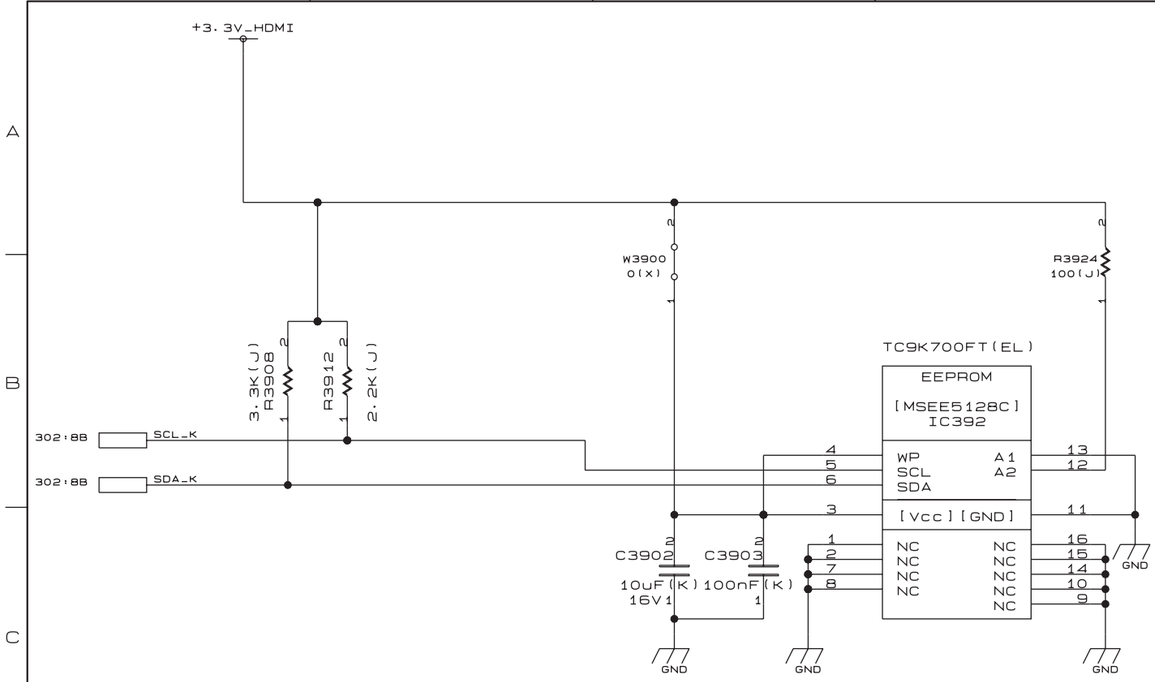
32WL58P
SIGNAL
HDMI CONTROLLER

SH.NO.
308



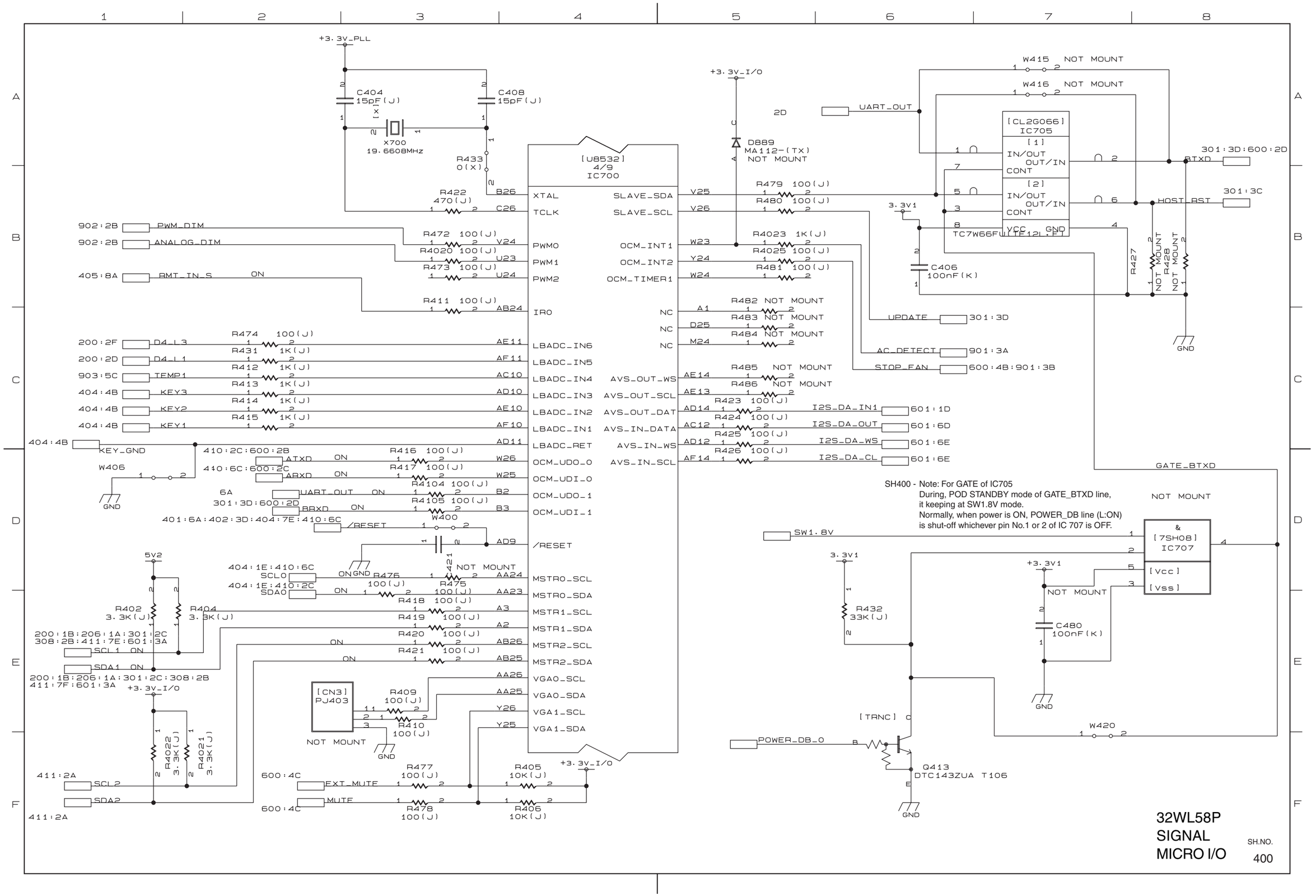
32WL58P
SIGNAL
EEPROM1

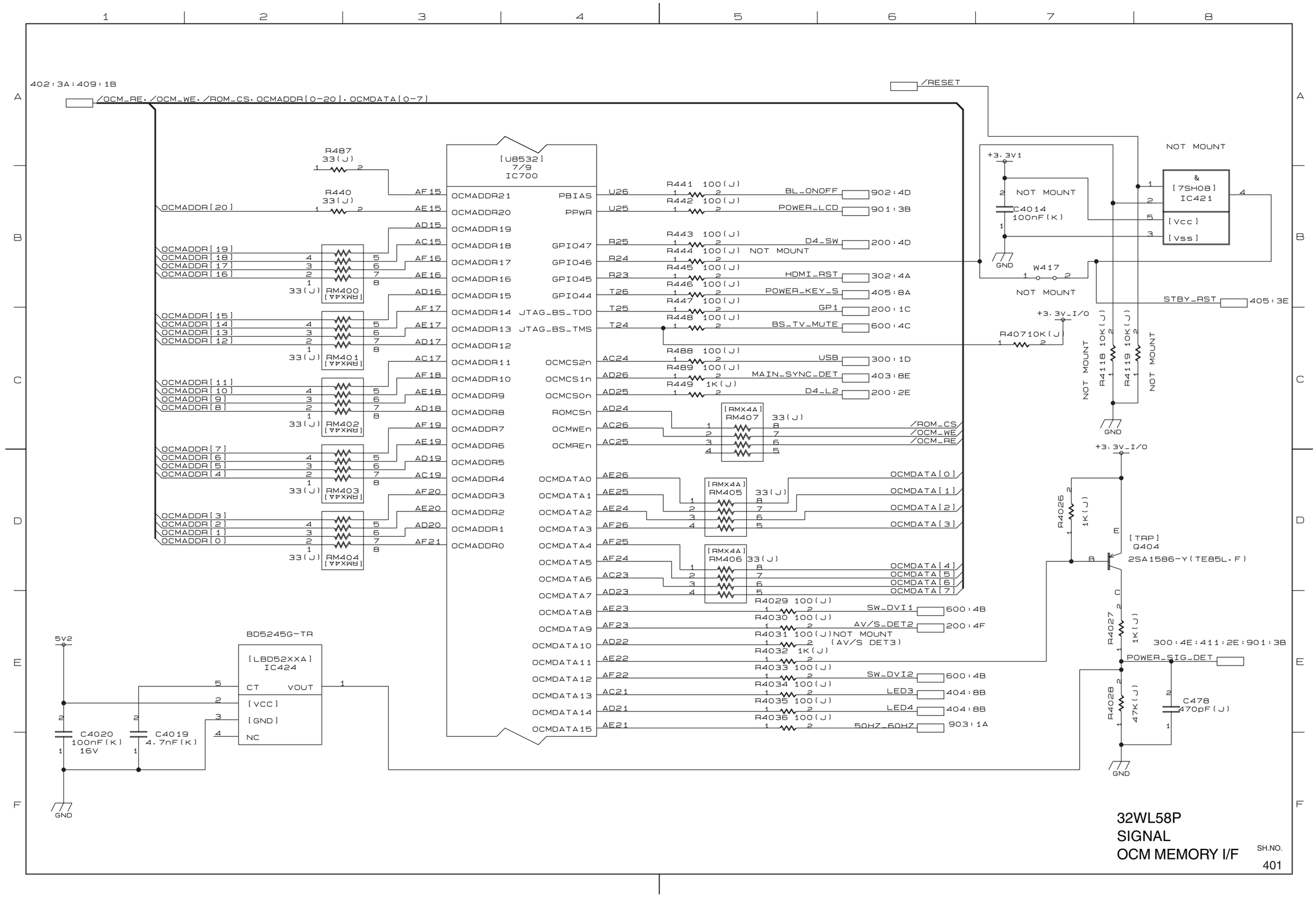
SH.NO.
309



32WL58P
SIGNAL
EEPROM2

SH.NO.
310


+3.3V_PLL
C404 15pF(J)
C408 15pF(J)
X700 19.6608MHz
R433 0(X)
[U8532] 4/9 IC700
XTAL
TCLK
R422 470(J)
B26
C26
902:2B PWM_DIM
902:2B ANALOG_DIM
405:8A RMT_IN_S
ON
R472 100(J)
R4020 100(J)
R473 100(J)
R411 100(J)
PWM0
PWM1
PWM2
IRO
V24
U23
U24
AB24
200:2F D4_L3
200:2D D4_L1
903:5C TEMP1
404:4B KEY3
404:4B KEY2
404:4B KEY1
404:4B KEY_GND
R474 100(J)
R431 1K(J)
R412 1K(J)
R413 1K(J)
R414 1K(J)
R415 1K(J)
W406
GND
LBADC_IN6
LBADC_IN5
LBADC_IN4
LBADC_IN3
LBADC_IN2
LBADC_IN1
LBADC_RET
AE11
AF11
AC10
AD10
AE10
AF10
AD11
410:2C:600:2B ATXD
410:6C:600:2C ARXD
6A UART_OUT
301:3D:600:2D BRXD
ON
R416 100(J)
R417 100(J)
R4104 100(J)
R4105 100(J)
W400
W26
W25
B2
B3
OCM_UDO_0
OCM_UDI_0
OCM_UDO_1
OCM_UDI_1
401:6A:402:3D:404:7E:410:6C /RESET
AD9
/RESET
5V2
404:1E:410:6C SCL0
404:1E:410:2C SDA0
R476 100(J)
R475 100(J)
R418 100(J)
R419 100(J)
R420 100(J)
R421 100(J)
R421 NOT MOUNT
AA24
AA23
A3
A2
AB26
AB25
MSTR0_SCL
MSTR0_SDA
MSTR1_SCL
MSTR1_SDA
MSTR2_SCL
MSTR2_SDA
R402 3.3K(J)
R404 3.3K(J)
200:1B:206:1A:301:2C 308:2B:411:7E:601:3A SCL1
SDA1
200:1B:206:1A:301:2C:308:2B 411:7F:601:3A
+3.3V_I/O
R4022 3.3K(J)
R4021 3.3K(J)
411:2A SCL2
SDA2
411:2A
[CN3] PJ403
NOT MOUNT
R409 100(J)
R410 100(J)
AA26
AA25
Y26
Y25
VGA0_SCL
VGA0_SDA
VGA1_SCL
VGA1_SDA
600:4C EXT_MUTE
600:4C MUTE
R477 100(J)
R478 100(J)
R405 10K(J)
R406 10K(J)
+3.3V_I/O
SLAVE_SDA
SLAVE_SCL
OCM_INT1
OCM_INT2
OCM_TIMER1
NC
NC
NC
AVS_OUT_WS
AVS_OUT_SCL
AVS_OUT_DAT
AVS_IN_DATA
AVS_IN_WS
AVS_IN_SCL
V25
V26
W23
Y24
W24
A1
D25
M24
AE14
AE13
AD14
AC12
AD12
AF14
R479 100(J)
R480 100(J)
R4023 1K(J)
R4025 100(J)
R481 100(J)
R482 NOT MOUNT
R483 NOT MOUNT
R484 NOT MOUNT
R485 NOT MOUNT
R486 NOT MOUNT
R423 100(J)
R424 100(J)
R425 100(J)
R426 100(J)
I2S_DA_IN1
I2S_DA_OUT
I2S_DA_WS
I2S_DA_CL
601:1D
601:6D
601:6E
601:6E
+3.3V_I/O
2D
D889 MA112-(TX) NOT MOUNT
UART_OUT
UPDATE
301:3D
AC_DETECT
901:3A
STOP_FAN
600:4B:901:3B
W415 NOT MOUNT
W416 NOT MOUNT
[CL2G066] IC705
[1] IN/OUT OUT/IN CONT
[2] IN/OUT OUT/IN CONT
VCC GND
TC7W66FU(TE12L,F)
3.3V1
C406 100nF(K)
R427
R428 NOT MOUNT
R429 NOT MOUNT
301:3D:600:2D
RTXD
HOST_RST
301:3C
GND
GATE_BTXD
SH400 - Note: For GATE of IC705
During, POD STANDBY mode of GATE_BTXD line,
it keeping at SW1.8V mode.
Normally, when power is ON, POWER_DB line (L:ON)
is shut-off whichever pin No.1 or 2 of IC 707 is OFF.
NOT MOUNT
SW1.8V
&
[7SH08] IC707
[Vcc]
[Vss]
3.3V1
R432 33K(J)
+3.3V1
NOT MOUNT
C480 100nF(K)
GND
[TRNC]
POWER_DB_O
Q413 DTC143ZUA T106
W420
GND
32WL58P
SIGNAL
MICRO I/O
SH.NO.
400


402:3A:409:1B
/OCM_RE. /OCM_WE. /ROM_CS. OCMADDR[0-20]. OCMDATA[0-7]
/RESET
[U8532]
7/9
IC700
R487 33(J)
R440 33(J)
OCMADDR[20]
OCMADDR[19]
OCMADDR[18]
OCMADDR[17]
OCMADDR[16]
OCMADDR[15]
OCMADDR[14]
OCMADDR[13]
OCMADDR[12]
OCMADDR[11]
OCMADDR[10]
OCMADDR[9]
OCMADDR[8]
OCMADDR[7]
OCMADDR[6]
OCMADDR[5]
OCMADDR[4]
OCMADDR[3]
OCMADDR[2]
OCMADDR[1]
OCMADDR[0]
RM400 [RMX4A] 33(J)
RM401 [RMX4A] 33(J)
RM402 [RMX4A] 33(J)
RM403 [RMX4A] 33(J)
RM404 [RMX4A] 33(J)
AF15 AE15 AD15 AC15 AF16 AE16 AD16 AF17 AE17 AD17 AC17 AF18 AE18 AD18 AF19 AE19 AD19 AC19 AF20 AE20 AD20 AF21
OCMADDR21 OCMADDR20 OCMADDR19 OCMADDR18 OCMADDR17 OCMADDR16 OCMADDR15 OCMADDR14 OCMADDR13 OCMADDR12 OCMADDR11 OCMADDR10 OCMADDR9 OCMADDR8 OCMADDR7 OCMADDR6 OCMADDR5 OCMADDR4 OCMADDR3 OCMADDR2 OCMADDR1 OCMADDR0
PBIAS PPWR GPIO47 GPIO46 GPIO45 GPIO44 JTAG_BS_TDO JTAG_BS_TMS OCMCS2n OCMCS1n OCMCS0n ROMCSn OCMWEn OCMREn
OCMDATA0 OCMDATA1 OCMDATA2 OCMDATA3 OCMDATA4 OCMDATA5 OCMDATA6 OCMDATA7 OCMDATA8 OCMDATA9 OCMDATA10 OCMDATA11 OCMDATA12 OCMDATA13 OCMDATA14 OCMDATA15
U26 U25 R25 R24 R23 T26 T25 T24 AC24 AD26 AD25 AD24 AC26 AC25 AE26 AE25 AE24 AF26 AF25 AF24 AC23 AD23 AE23 AF23 AD22 AE22 AF22 AC21 AD21 AE21
R441 100(J) BL_ONOFF 902:4D
R442 100(J) POWER_LCD 901:3B
R443 100(J) D4_SW 200:4D
R444 100(J) NOT MOUNT
R445 100(J) HDMI_RST 302:4A
R446 100(J) POWER_KEY_S 405:8A
R447 100(J) GP1 200:1C
R448 100(J) BS_TV_MUTE 600:4C
R488 100(J) USB 300:1D
R489 100(J) MAIN_SYNC_DET 403:8E
R449 1K(J) D4_L2 200:2E
RM407 [RMX4A] 33(J) /ROM_CS /OCM_WE /OCM_RE
RM405 [RMX4A] 33(J) OCMDATA[0] OCMDATA[1] OCMDATA[2] OCMDATA[3]
RM406 [RMX4A] 33(J) OCMDATA[4] OCMDATA[5] OCMDATA[6] OCMDATA[7]
R4029 100(J) SW_DVI1 600:4B
R4030 100(J) AV/S_DET2 200:4F
R4031 100(J) NOT MOUNT (AV/S DET3)
R4032 1K(J)
R4033 100(J) SW_DVI2 600:4B
R4034 100(J) LED3 404:8B
R4035 100(J) LED4 404:8B
R4036 100(J) 50HZ_60HZ 903:1A
+3.3V1
NOT MOUNT
C4014 100nF(K)
GND
[7SH08] IC421
[Vcc]
[Vss]
W417
STBY_RST 405:3E
+3.3V_I/O
R4071 0K(J)
R4118 10K(J) NOT MOUNT
R4119 10K(J) NOT MOUNT
R4026 1K(J)
[TRP] Q404 2SA1586-Y(TE85L.F)
R4027 1K(J)
300:4E:411:2E:901:3B
POWER_SIG_DET
R4028 47K(J)
C478 470pF(J)
5V2
BD5245G-TR
[LBD52XXA] IC424
CT VOUT [VCC] [GND] NC
C4020 100nF(K) 16V
C4019 4.7nF(K)
32WL58P
SIGNAL
OCM MEMORY I/F
SH.NO.
401

401:1A:409:1B

/OCM_RE. /OCM_WE. /ROM_CS. OCMADDR[0-20]. OCMDATA[0-7]

MX26LV160TTC-55G

[MFLV160C]
IC404

OCMADDR[1] 25 — A0
OCMADDR[2] 24 — A1
OCMADDR[3] 23 — A2
OCMADDR[4] 22 — A3
OCMADDR[5] 21 — A4
OCMADDR[6] 20 — A5
OCMADDR[7] 19 — A6
OCMADDR[8] 18 — A7
OCMADDR[9] 8 — A8
OCMADDR[10] 7 — A9
OCMADDR[11] 6 — A10
OCMADDR[12] 5 — A11
OCMADDR[13] 4 — A12
OCMADDR[14] 3 — A13
OCMADDR[15] 2 — A14
OCMADDR[16] 1 — A15
OCMADDR[17] 48 — A16
OCMADDR[18] 17 — A17
OCMADDR[19] 16 — A18
OCMADDR[20] 9 — A19

/ROM_CS 26 — CEN
/OCM_RE 28 — OEN
/OCM_WE 11 — WEN
12 — RPN
47 — BYTEN

RY/BYN — 15
NC — 10
NC — 13
NC — 14

D0 29 — OCMDATA[0]
D1 31 — OCMDATA[1]
D2 33 — OCMDATA[2]
D3 35 — OCMDATA[3]
D4 38 — OCMDATA[4]
D5 40 — OCMDATA[5]
D6 42 — OCMDATA[6]
D7 44 — OCMDATA[7]
D8 30
D9 32
D10 34
D11 36
D12 39
D13 41
D14 43
D15/A-1 45 — OCMADDR[0]

[Vcc] 37
[GND] 27
[GND] 46

GND

400:3D:401:6A:404:7E:410:6C /RESET 1 2 W411 MOUNT

R4086 1 2 2.2K(J)

C464 NOT MOUNT

+3.3V_I/O

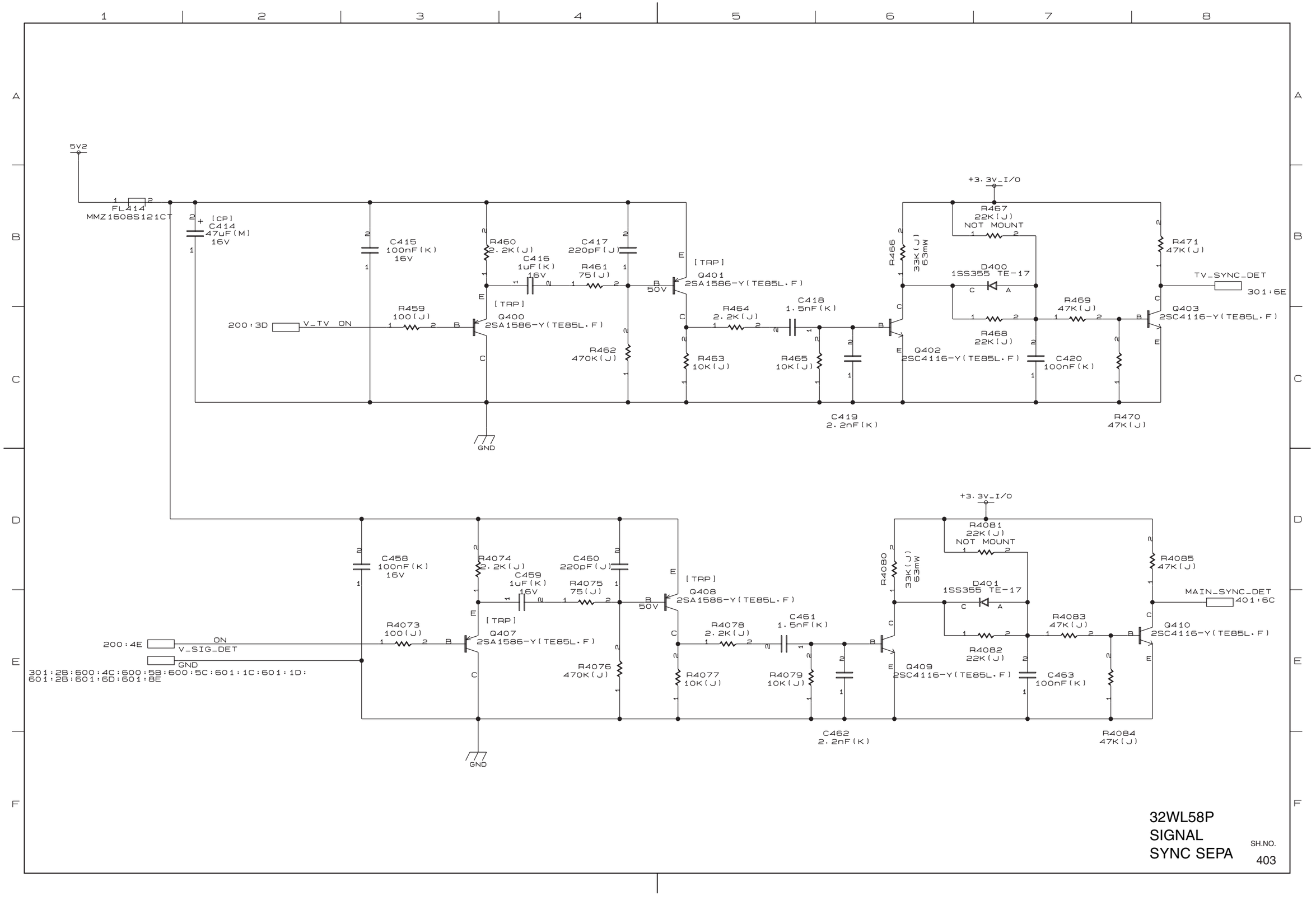
C410 10uF(K) 6.3V

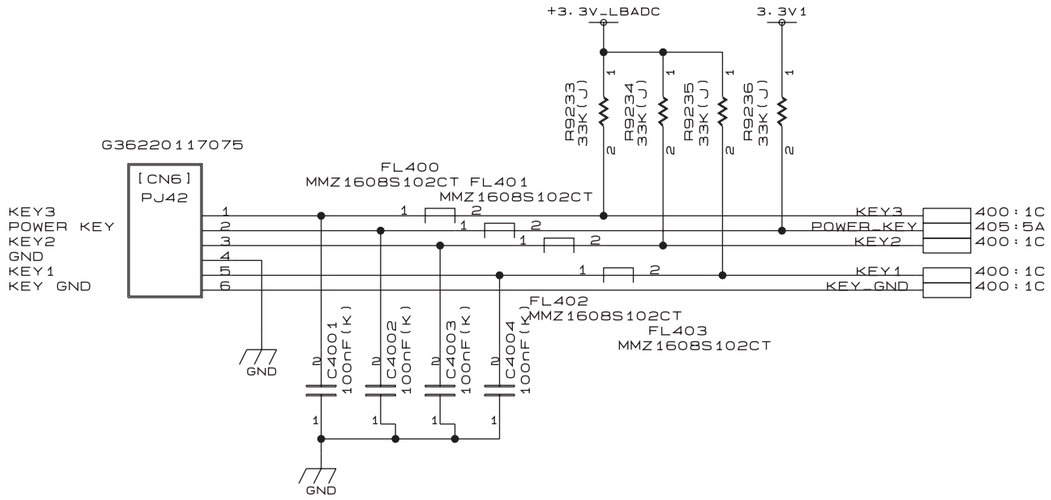
C411 100nF(K) 16V

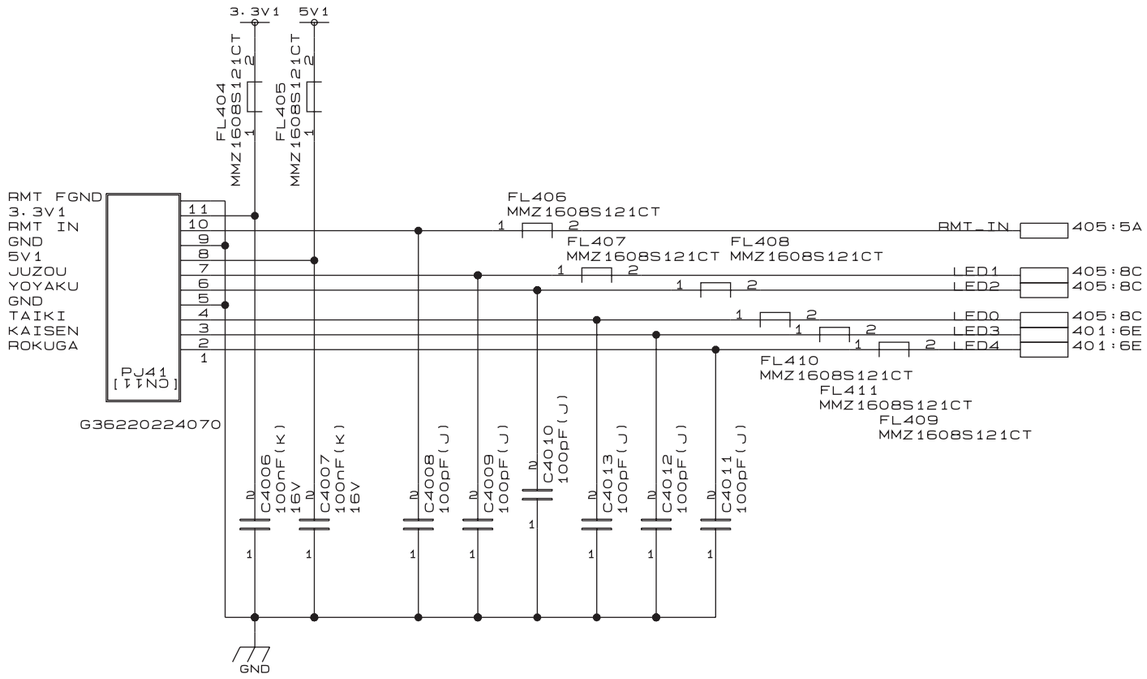
GND

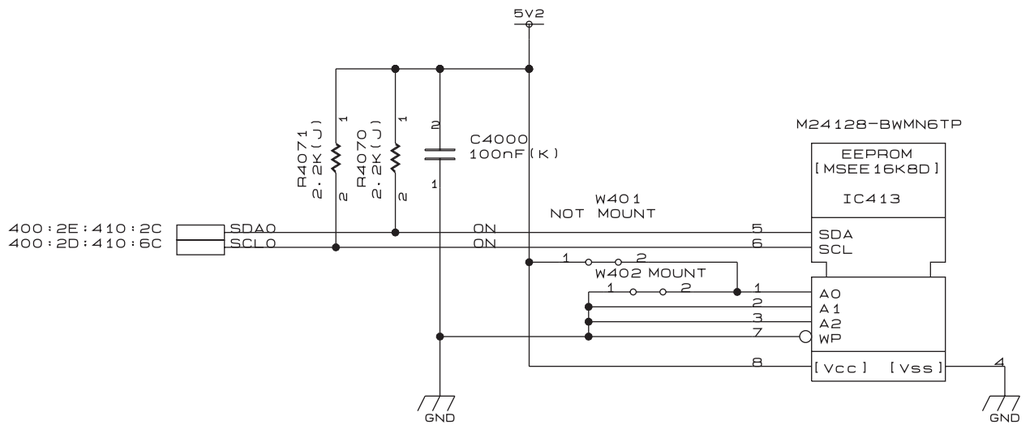
32WL58P
SIGNAL
FLASH MEMORY

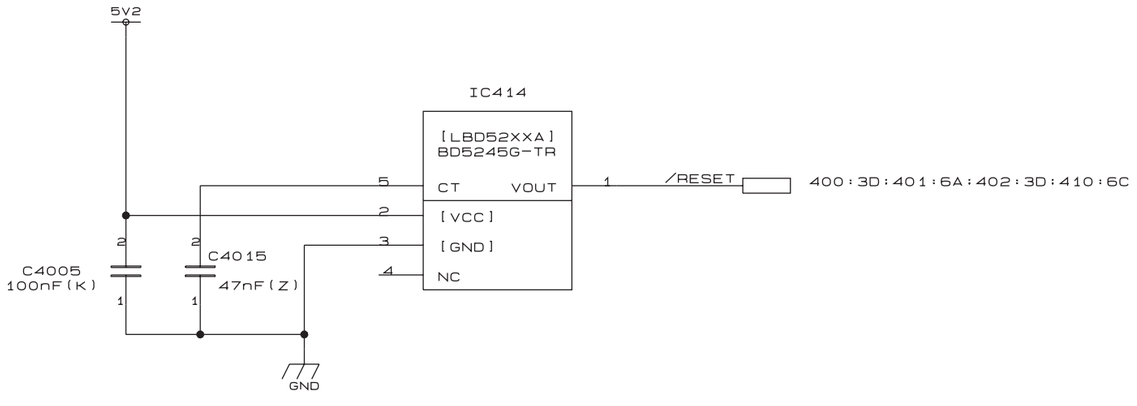
SH.NO.
402


5V2
FL414
MMZ1608S121CT
[CP]
C414
47uF(M)
16V
C415
100nF(K)
16V
R460
2.2K(J)
C416
1uF(K)
16V
C417
220pF(J)
R461
75(J)
[TRP]
Q401
2SA1586-Y(TE85L.F)
50V
R459
100(J)
200:3D
V_TV ON
[TRP]
Q400
2SA1586-Y(TE85L.F)
R462
470K(J)
R464
2.2K(J)
C418
1.5nF(K)
R463
10K(J)
R465
10K(J)
C419
2.2nF(K)
GND
+3.3V_I/O
R467
22K(J)
NOT MOUNT
R466
33K(J)
63mW
D400
1SS355 TE-17
R471
47K(J)
TV_SYNC_DET
301:6E
R469
47K(J)
Q403
2SC4116-Y(TE85L.F)
R468
22K(J)
Q402
2SC4116-Y(TE85L.F)
C420
100nF(K)
R470
47K(J)
C458
100nF(K)
16V
R4074
2.2K(J)
C459
1uF(K)
16V
C460
220pF(J)
R4075
75(J)
[TRP]
Q408
2SA1586-Y(TE85L.F)
50V
R4073
100(J)
200:4E
ON
V_SIG_DET
GND
301:2B:600:4C:600:5B:600:5C:601:1C:601:1D:
601:2B:601:6D:601:8E
[TRP]
Q407
2SA1586-Y(TE85L.F)
R4076
470K(J)
R4078
2.2K(J)
C461
1.5nF(K)
R4077
10K(J)
R4079
10K(J)
C462
2.2nF(K)
GND
+3.3V_I/O
R4081
22K(J)
NOT MOUNT
R4080
33K(J)
63mW
D401
1SS355 TE-17
R4085
47K(J)
MAIN_SYNC_DET
401:6C
R4083
47K(J)
Q410
2SC4116-Y(TE85L.F)
R4082
22K(J)
Q409
2SC4116-Y(TE85L.F)
C463
100nF(K)
R4084
47K(J)
32WL58P
SIGNAL
SYNC SEPA
SH.NO.
403



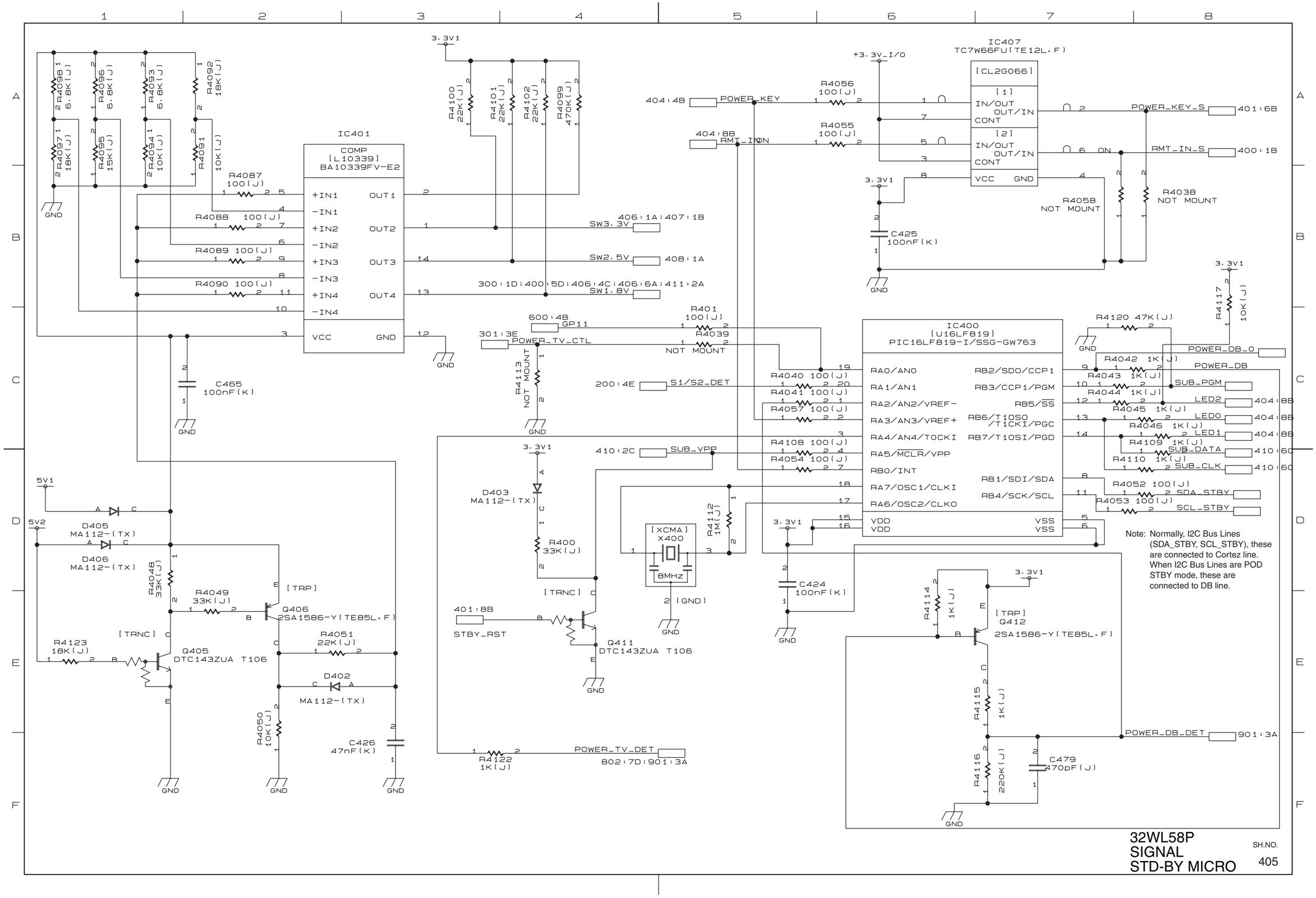






32WL58P
SIGNAL
E2P OTHER

SH.NO.
404


IC401
COMP
[L10339]
BA10339FV-E2
+IN1
-IN1
+IN2
-IN2
+IN3
-IN3
+IN4
-IN4
OUT1
OUT2
OUT3
OUT4
VCC
GND
IC407
TC7W66FU(TE12L.F)
[CL2G066]
[1]
[2]
IN/OUT
OUT/IN
CONT
IC400
[U16LF819]
PIC16LF819-I/SSG-GW763
RA0/AN0
RA1/AN1
RA2/AN2/VREF-
RA3/AN3/VREF+
RA4/AN4/TOCKI
RA5/MCLR/VPP
RB0/INT
RA7/OSC1/CLKI
RA6/OSC2/CLKO
RB2/SDO/CCP1
RB3/CCP1/PGM
RB5/SS
RB6/T1OSO/T1CKI/PGC
RB7/T1OSI/PGD
RB1/SDI/SDA
RB4/SCK/SCL
VDD
VSS
[XCMA]
X400
8MHz
[TRP]
[TRNC]
Q405
DTC143ZUA T106
Q406
2SA1586-Y(TE85L.F)
Q411
DTC143ZUA T106
Q412
2SA1586-Y(TE85L.F)
D402
D403
D405
D406
MA112-(TX)
3.3V1
5V1
5V2
GND
R4087 100(J)
R4088 100(J)
R4089 100(J)
R4090 100(J)
R4091 10K(J)
R4092 18K(J)
R4093 6.8K(J)
R4094 10K(J)
R4095 15K(J)
R4096 6.8K(J)
R4097 18K(J)
R4098 6.8K(J)
R4099 470K(J)
R4100 22K(J)
R4101 22K(J)
R4102 22K(J)
R4108 100(J)
R4054 100(J)
R4055 100(J)
R4056 100(J)
R4058 NOT MOUNT
R4038 NOT MOUNT
R4039 NOT MOUNT
R4040 100(J)
R4041 100(J)
R4057 100(J)
R401 100(J)
R4113 NOT MOUNT
R4112 1M(J)
R400 33K(J)
R4048 33K(J)
R4049 33K(J)
R4051 22K(J)
R4050 10K(J)
R4123 18K(J)
R4122 1K(J)
R4114 1K(J)
R4115 1K(J)
R4116 220K(J)
R4117 10K(J)
R4120 47K(J)
R4042 1K(J)
R4043 1K(J)
R4044 1K(J)
R4045 1K(J)
R4046 1K(J)
R4109 1K(J)
R4110 1K(J)
R4052 100(J)
R4053 100(J)
C425 100nF(K)
C424 100nF(K)
C465 100nF(K)
C426 47nF(K)
C479 470pF(J)
404:4B
POWER_KEY
404:8B
RMT_IN
POWER_KEY_S
401:6B
RMT_IN_S
400:1B
406:1A:407:1B
SW3.3V
SW2.5V
408:1A
300:1D:400:5D:406:4C:406:6A:411:2A
SW1.8V
600:4B
GP11
301:3E
POWER_TV_CTL
200:4E
S1/S2_DET
410:2C
SUB_VPP
401:8B
STBY_RST
POWER_TV_DET
802:7D:901:3A
POWER_DB_DET
901:3A
POWER_DB_O
POWER_DB
SUB_PGM
LED2
LED0
LED1
SUB_DATA
SUB_CLK
SDA_STBY
SCL_STBY
404:8B
410:6C
+3.3V_I/O
ON
(GND)
Note: Normally, I2C Bus Lines (SDA_STBY, SCL_STBY), these are connected to Cortez line. When I2C Bus Lines are POD STBY mode, these are connected to DB line.
32WL58P
SIGNAL
STD-BY MICRO
SH.NO.
405

405:4B:407:1B

SW3.3V
R4059
100(J)
C471
47pF(J)
GND
IC402
PQ070XH02ZPH
UREG
[LPQ070XH]
3.3V
VC
VADJ
VIN
VO
[GND]
5V2
C428
1uF(K)
GND
+3.3V
[CP]
C427
47uF(M)
GND
FL423
MPZ2012S221AT
+3.3V_I/O
C451
100nF(K)
16V
GND
R4061
1.5K(F)
R4062
150(F)
R4060
1K(F)

300:1D:400:5D:405:4B:6A:411:2A
SW1.8V
R4063
100(J)
C473
100pF(J)
GND
PQ018EH02ZPH
UREG
[LPQXXXEH]
IC403
1.8V
VC
NC
VIN
VO
[GND]
+3.3V
C429
1uF(K)
GND
[CP]
C453
47uF(M)
GND

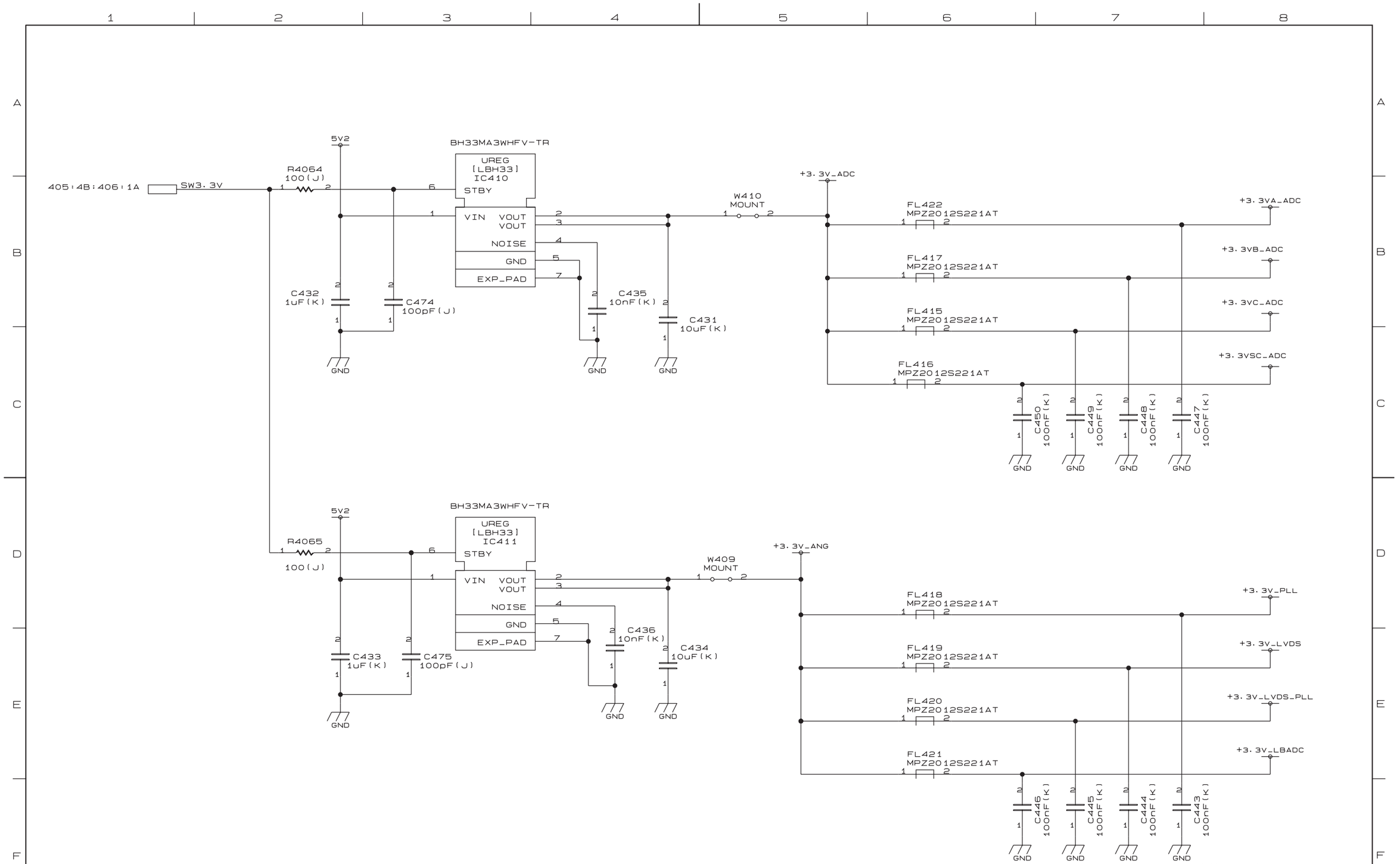
300:1D:400:5D:405:4B:4C:411:2A
5V2
SW1.8V
R4072
100(J)
BH18LB1WG-TR
UREG
[LBHXX]
IC412
STBY
NC
VIN
VOUT
[GND]
C452
100nF(K)
C472
100pF(J)
GND
+1.8V_ADC
FL427
MPZ1608S221AT
C454
1uF(K)
GND

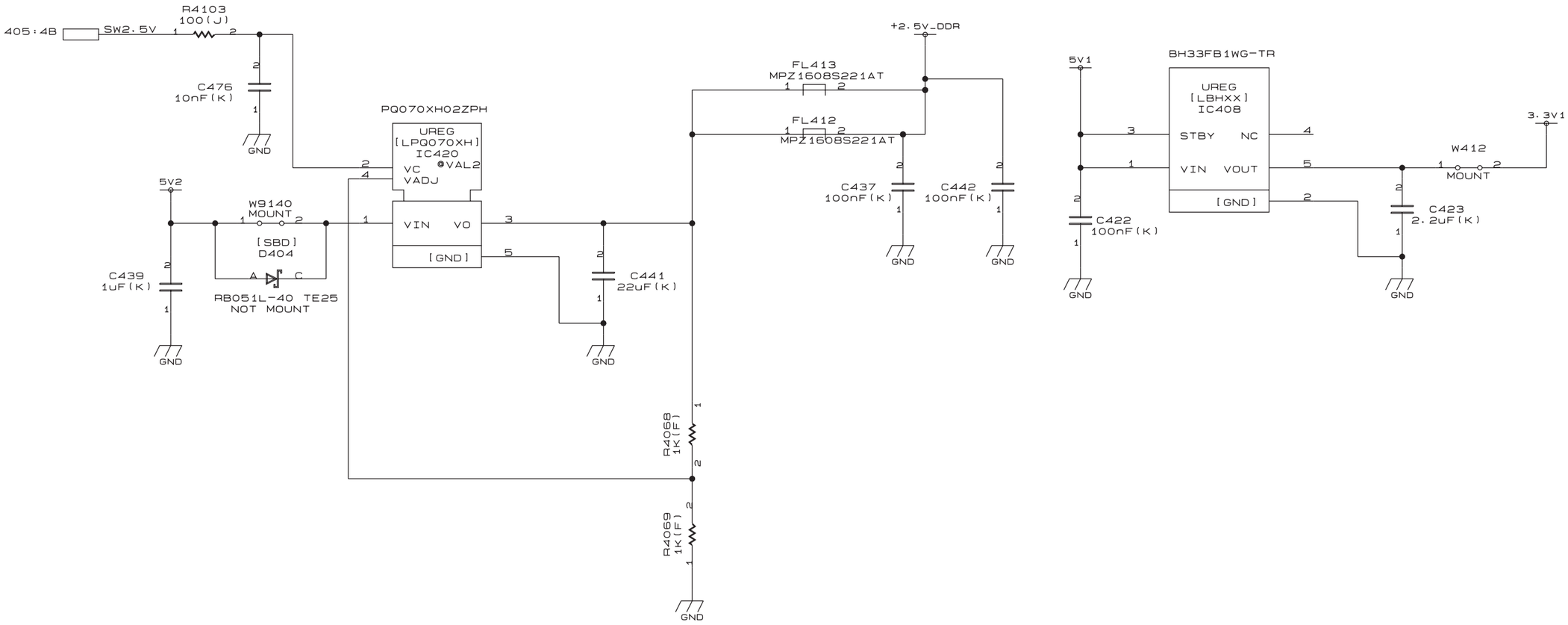
NOT MOUNT
FL428
MPZ1608S221AT

+1.8V_DLL
FL425
MPZ1608S221AT
+1.8V_PLL
FL426
MPZ1608S221AT
+1.8V_CORE
FL424
MPZ1608S221AT
C430
100nF(K)
C456
100nF(K)
C455
100nF(K)
GND

32WL58P
SIGNAL
CORTEZ REG 1

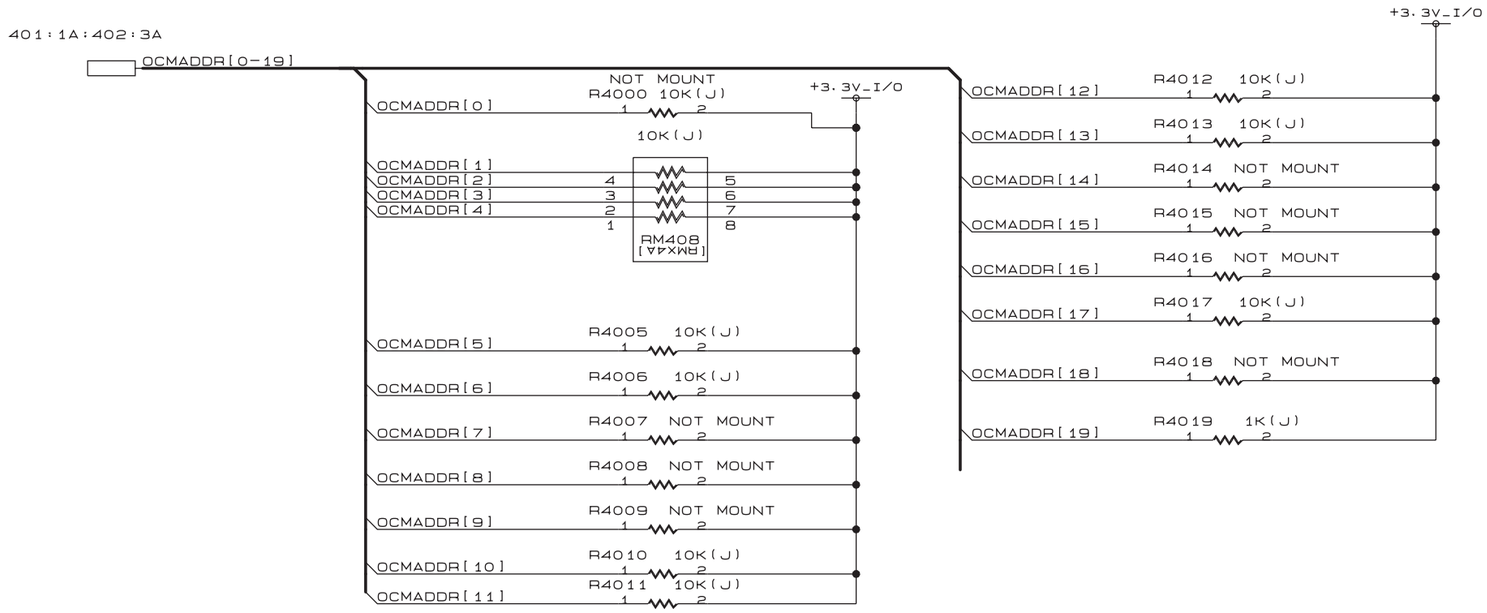
SH.NO.
406


1
2
3
4
5
6
7
8
A
B
C
D
E
F
5V2
BH33MA3WHFV-TR
UREG
[LBH33]
IC410
STBY
VIN
VOUT
VOUT
NOISE
GND
EXP_PAD
405:4B:406:1A
SW3.3V
R4064
100(J)
C432
1uF(K)
C474
100pF(J)
C435
10nF(K)
C431
10uF(K)
W410
MOUNT
+3.3V_ADC
FL422
MPZ2012S221AT
FL417
MPZ2012S221AT
FL415
MPZ2012S221AT
FL416
MPZ2012S221AT
+3.3VA_ADC
+3.3VB_ADC
+3.3VC_ADC
+3.3VSC_ADC
C450
100nF(K)
C449
100nF(K)
C448
100nF(K)
C447
100nF(K)
GND
R4065
100(J)
C433
1uF(K)
C475
100pF(J)
C436
10nF(K)
C434
10uF(K)
W409
MOUNT
+3.3V_ANG
FL418
MPZ2012S221AT
FL419
MPZ2012S221AT
FL420
MPZ2012S221AT
FL421
MPZ2012S221AT
+3.3V_PLL
+3.3V_LVDS
+3.3V_LVDS_PLL
+3.3V_LBADC
C446
100nF(K)
C445
100nF(K)
C444
100nF(K)
C443
100nF(K)
32WL58P
SIGNAL
CORTEZ REG 2
SH.NO.
407



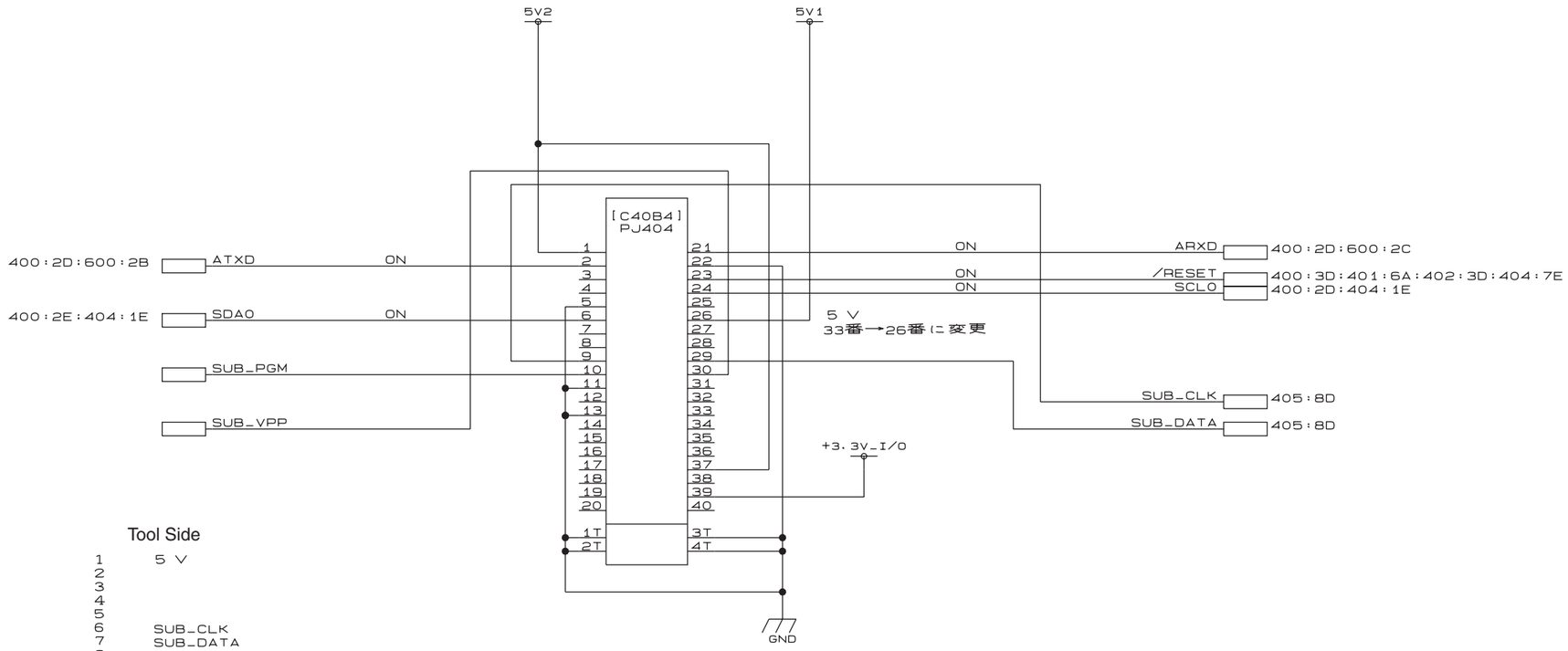
32WL58P
SIGNAL
CORTEZ REG 3

SH.NO.
408



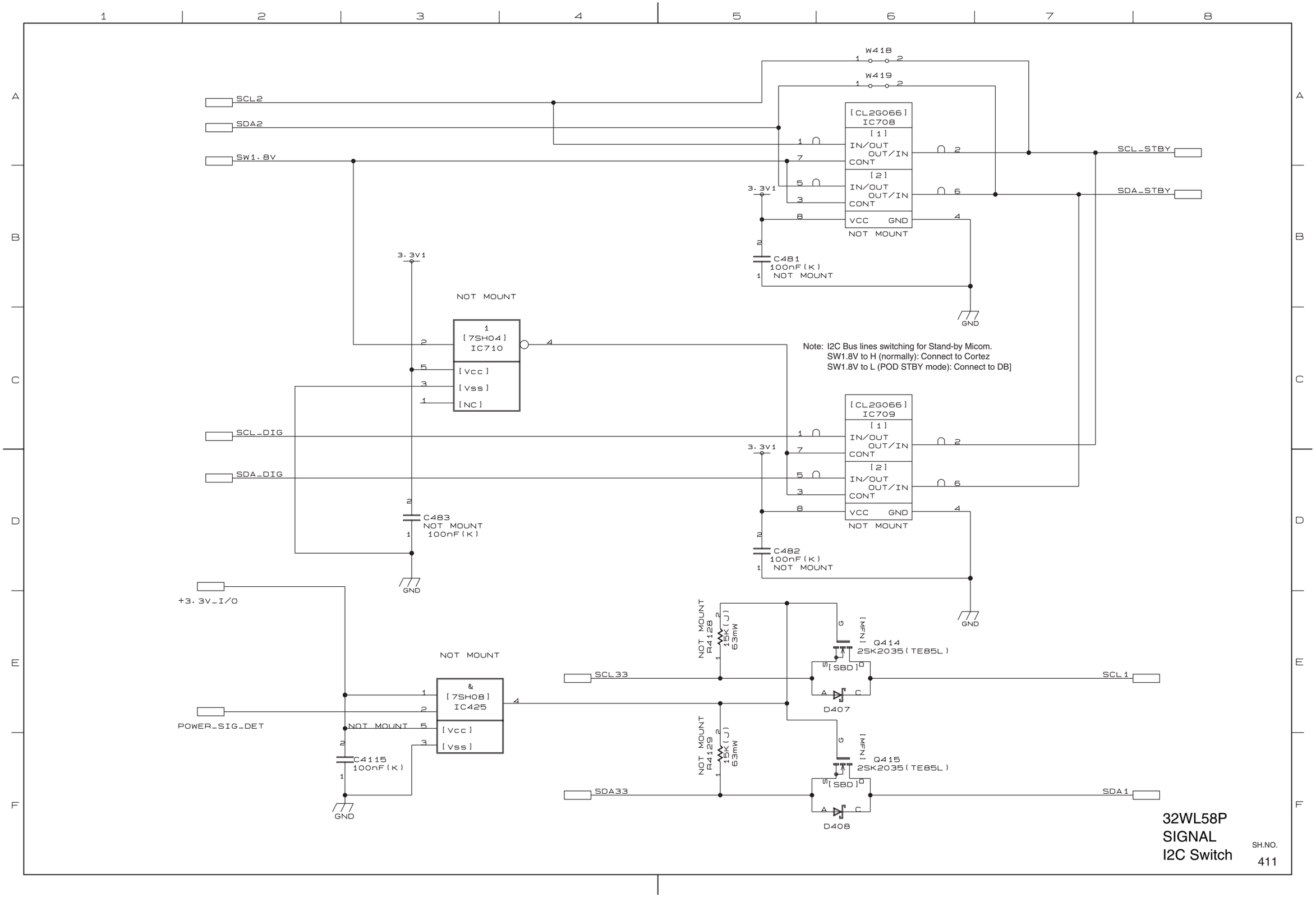
32WL58P
SIGNAL
BOOT CONFIG

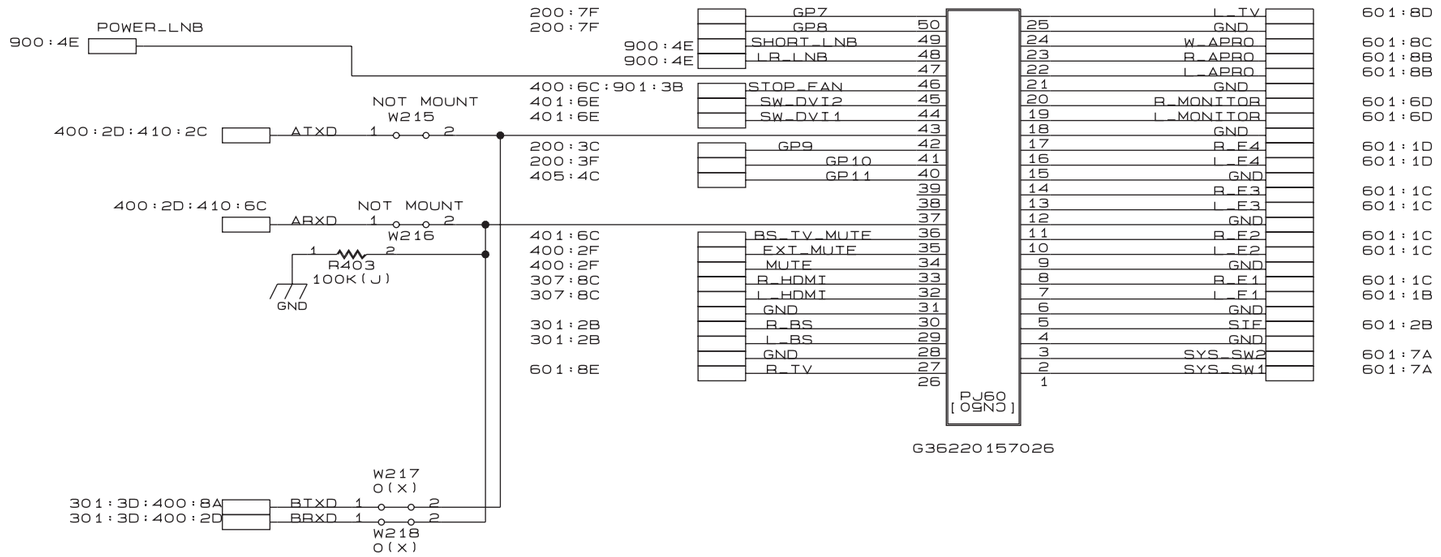
SH.NO.
409



32WL58P
SIGNAL
SERVICE CONNECTOR

SH.NO.
410

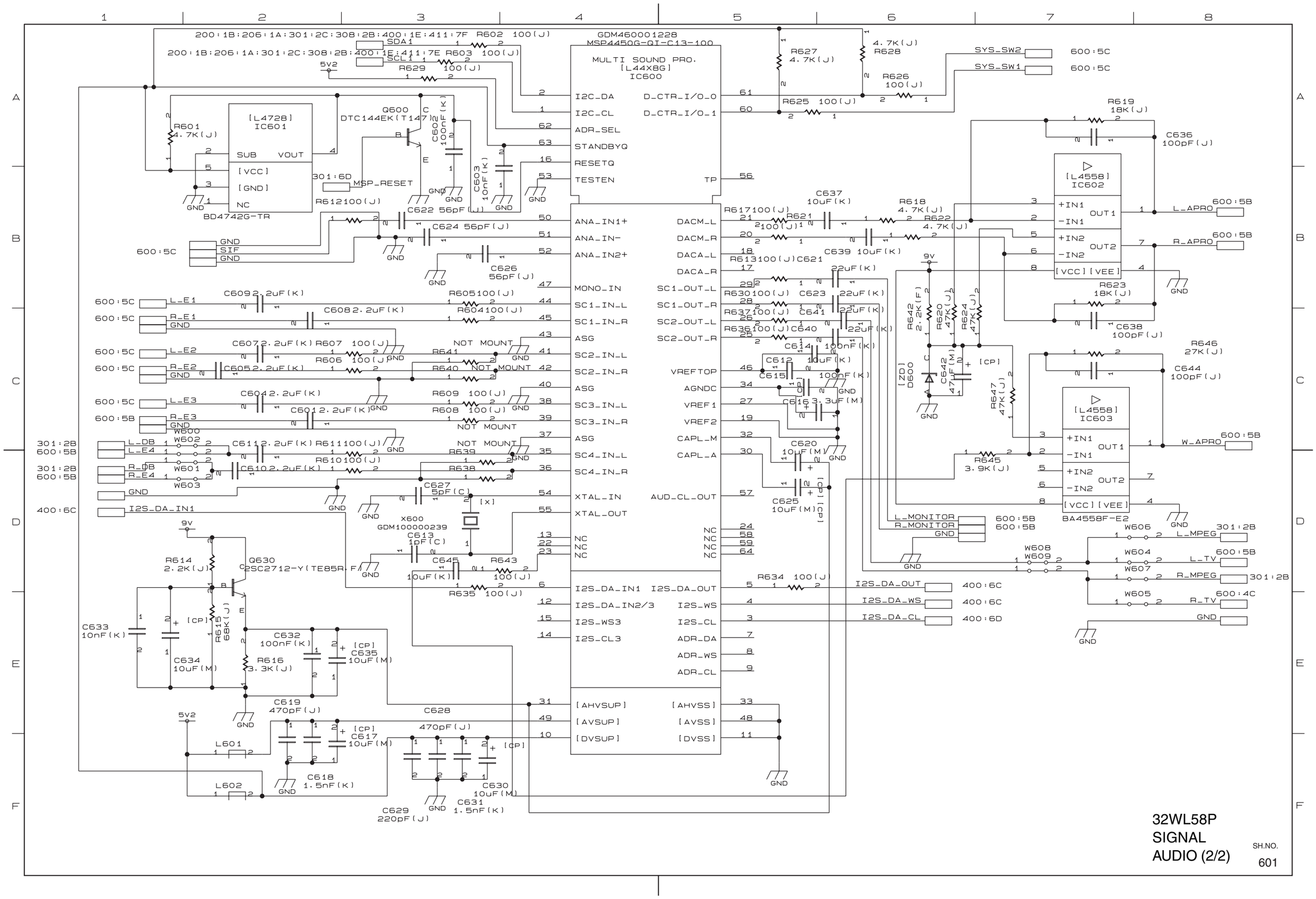

W418
W419
SCL2
SDA2
SW1.8V
[CL2G066]
IC708
[1]
IN/OUT
OUT/IN
CONT
[2]
IN/OUT
OUT/IN
CONT
VCC GND
NOT MOUNT
3.3V1
C481
100nF(K)
NOT MOUNT
SCL_STBY
SDA_STBY
GND
3.3V1
NOT MOUNT
1
[7SH04]
IC710
[Vcc]
[Vss]
[NC]
Note: I2C Bus lines switching for Stand-by Micom.
SW1.8V to H (normally): Connect to Cortez
SW1.8V to L (POD STBY mode): Connect to DB]
SCL_DIG
SDA_DIG
[CL2G066]
IC709
C483
NOT MOUNT
100nF(K)
C482
100nF(K)
NOT MOUNT
GND
+3.3V_I/O
NOT MOUNT
&
[7SH08]
IC425
POWER_SIG_DET
NOT MOUNT
C4115
100nF(K)
NOT MOUNT
R4128
15K(J)
63mW
NOT MOUNT
R4129
15K(J)
63mW
Q414
2SK2035(TE85L)
[N-MF]
[SBD]
D407
Q415
2SK2035(TE85L)
[N-MF]
[SBD]
D408
SCL33
SDA33
SCL1
SDA1
32WL58P
SIGNAL
I2C Switch
SH.NO.
411



G36220157026

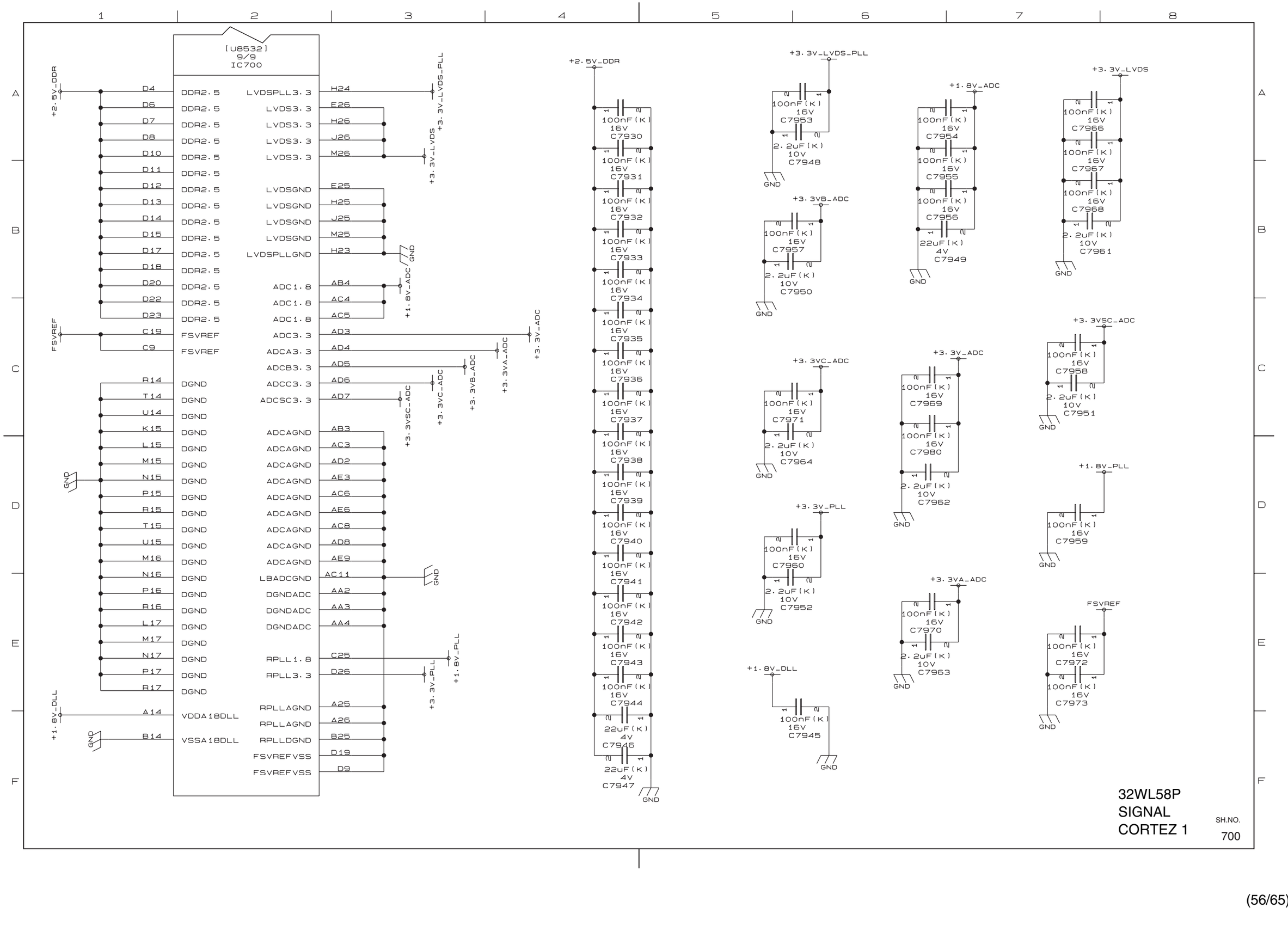
32WL58P
SIGNAL
AUDIO (1/2)

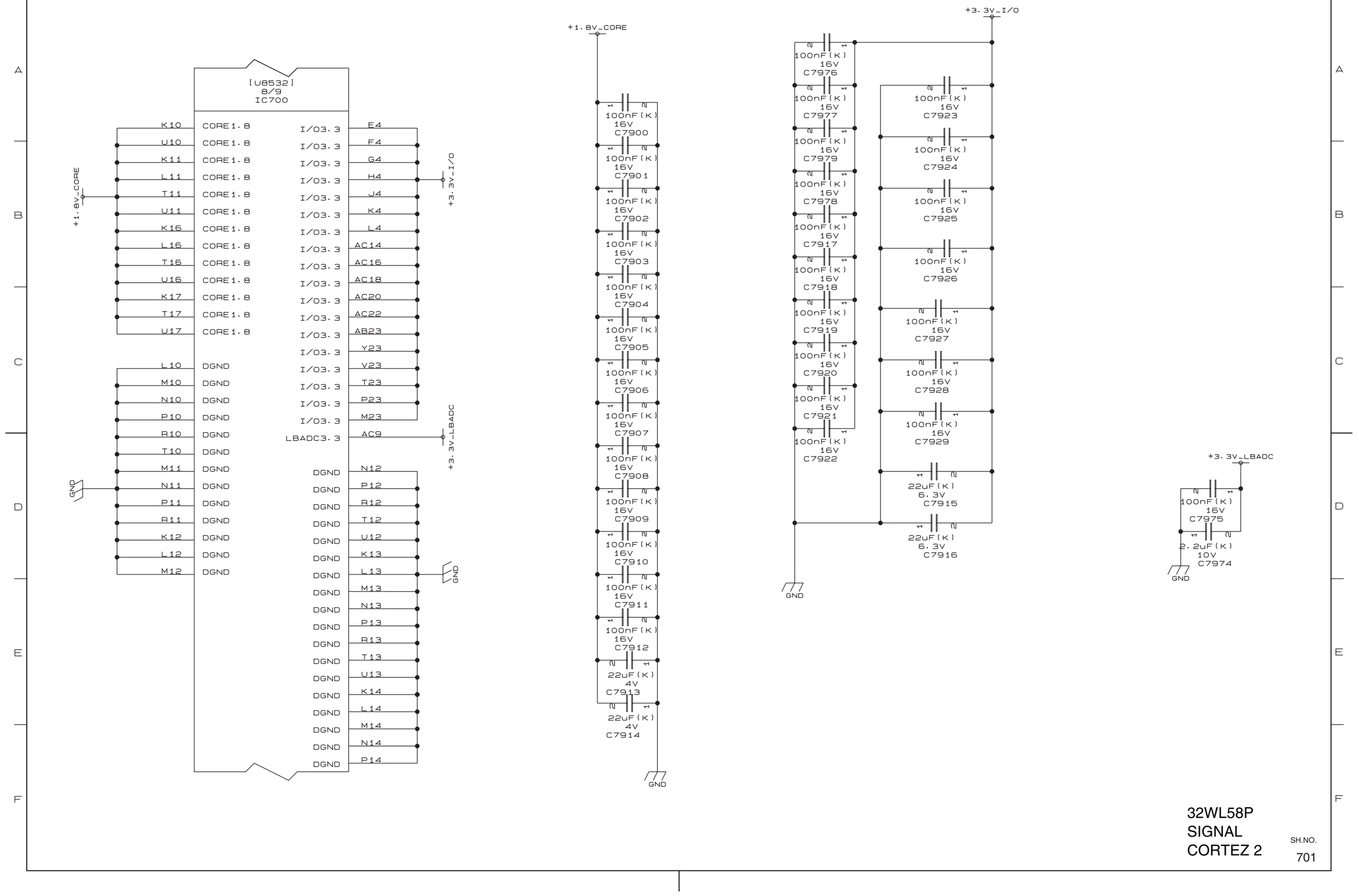
SH.NO.
600



32WL58P
SIGNAL
AUDIO (2/2)

SH.NO.
601


[U8532]
9/9
IC700
+2.5V_DDR
D4
D6
D7
D8
D10
D11
D12
D13
D14
D15
D17
D18
D20
D22
D23
FSVREF
C19
C9
R14
T14
U14
K15
L15
M15
N15
P15
R15
T15
U15
M16
N16
P16
R16
L17
M17
N17
P17
R17
A14
B14
+1.8V_DLL
GND
DDR2.5
FSVREF
DGND
VDDA18DLL
VSSA18DLL
LVDSPLL3.3
LVDS3.3
LVDSGND
LVDSPLLGND
ADC1.8
ADC3.3
ADCA3.3
ADCB3.3
ADCC3.3
ADCSC3.3
ADCAGND
LBADCGND
DGNDADC
RPLL1.8
RPLL3.3
RPLLAGND
RPLLDGND
FSVREFVSS
H24
E26
H26
J26
M26
E25
H25
J25
M25
H23
AB4
AC4
AC5
AD3
AD4
AD5
AD6
AD7
AB3
AC3
AD2
AE3
AC6
AE6
AC8
AD8
AE9
AC11
AA2
AA3
AA4
C25
D26
A25
A26
B25
D19
D9
+3.3V_LVDS_PLL
+3.3V_LVDS
+1.8V_ADC
+3.3V_ADC
+3.3VA_ADC
+3.3VB_ADC
+3.3VC_ADC
+3.3VSC_ADC
+1.8V_PLL
+3.3V_PLL
100nF(K) 16V C7930
100nF(K) 16V C7931
100nF(K) 16V C7932
100nF(K) 16V C7933
100nF(K) 16V C7934
100nF(K) 16V C7935
100nF(K) 16V C7936
100nF(K) 16V C7937
100nF(K) 16V C7938
100nF(K) 16V C7939
100nF(K) 16V C7940
100nF(K) 16V C7941
100nF(K) 16V C7942
100nF(K) 16V C7943
100nF(K) 16V C7944
22uF(K) 4V C7946
22uF(K) 4V C7947
100nF(K) 16V C7953
2.2uF(K) 10V C7948
100nF(K) 16V C7957
2.2uF(K) 10V C7950
100nF(K) 16V C7971
2.2uF(K) 10V C7964
100nF(K) 16V C7960
2.2uF(K) 10V C7952
100nF(K) 16V C7945
100nF(K) 16V C7954
100nF(K) 16V C7955
100nF(K) 16V C7956
22uF(K) 4V C7949
100nF(K) 16V C7969
100nF(K) 16V C7980
2.2uF(K) 10V C7962
100nF(K) 16V C7970
2.2uF(K) 10V C7963
100nF(K) 16V C7966
100nF(K) 16V C7967
100nF(K) 16V C7968
2.2uF(K) 10V C7961
100nF(K) 16V C7958
2.2uF(K) 10V C7951
100nF(K) 16V C7959
100nF(K) 16V C7972
100nF(K) 16V C7973
32WL58P
SIGNAL
CORTEZ 1
SH.NO.
700


[U8532]
8/9
IC700
K10 CORE1.8
U10 CORE1.8
K11 CORE1.8
L11 CORE1.8
T11 CORE1.8
U11 CORE1.8
K16 CORE1.8
L16 CORE1.8
T16 CORE1.8
U16 CORE1.8
K17 CORE1.8
T17 CORE1.8
U17 CORE1.8
+1.8V_CORE
L10 DGND
M10 DGND
N10 DGND
P10 DGND
R10 DGND
T10 DGND
M11 DGND
N11 DGND
P11 DGND
R11 DGND
K12 DGND
L12 DGND
M12 DGND
GND
I/O3.3 E4
I/O3.3 F4
I/O3.3 G4
I/O3.3 H4
I/O3.3 J4
I/O3.3 K4
I/O3.3 L4
I/O3.3 AC14
I/O3.3 AC16
I/O3.3 AC18
I/O3.3 AC20
I/O3.3 AC22
I/O3.3 AB23
I/O3.3 Y23
I/O3.3 V23
I/O3.3 T23
I/O3.3 P23
I/O3.3 M23
LBADC3.3 AC9
+3.3V_I/O
+3.3V_LBADC
DGND N12
DGND P12
DGND R12
DGND T12
DGND U12
DGND K13
DGND L13
DGND M13
DGND N13
DGND P13
DGND R13
DGND T13
DGND U13
DGND K14
DGND L14
DGND M14
DGND N14
DGND P14
GND
+1.8V_CORE
100nF(K) 16V C7900
100nF(K) 16V C7901
100nF(K) 16V C7902
100nF(K) 16V C7903
100nF(K) 16V C7904
100nF(K) 16V C7905
100nF(K) 16V C7906
100nF(K) 16V C7907
100nF(K) 16V C7908
100nF(K) 16V C7909
100nF(K) 16V C7910
100nF(K) 16V C7911
100nF(K) 16V C7912
22uF(K) 4V C7913
22uF(K) 4V C7914
GND
+3.3V_I/O
100nF(K) 16V C7976
100nF(K) 16V C7977
100nF(K) 16V C7979
100nF(K) 16V C7978
100nF(K) 16V C7917
100nF(K) 16V C7918
100nF(K) 16V C7919
100nF(K) 16V C7920
100nF(K) 16V C7921
100nF(K) 16V C7922
100nF(K) 16V C7923
100nF(K) 16V C7924
100nF(K) 16V C7925
100nF(K) 16V C7926
100nF(K) 16V C7927
100nF(K) 16V C7928
100nF(K) 16V C7929
22uF(K) 6.3V C7915
22uF(K) 6.3V C7916
GND
+3.3V_LBADC
100nF(K) 16V C7975
2.2uF(K) 10V C7974
GND
32WL58P
SIGNAL
CORTEZ 2
SH.NO.
701

/FSCAS. /FSCS[0-1]. /FSRAS. /FSWE. FSADDR[0-12]. FSBKSEL[0-1]. FSCKE. FSCLK+. FSCLK-. FSDATA[0-31]. FSDQM[0-3]. FSDQS[0-3]

[U8532]
5/9
IC700

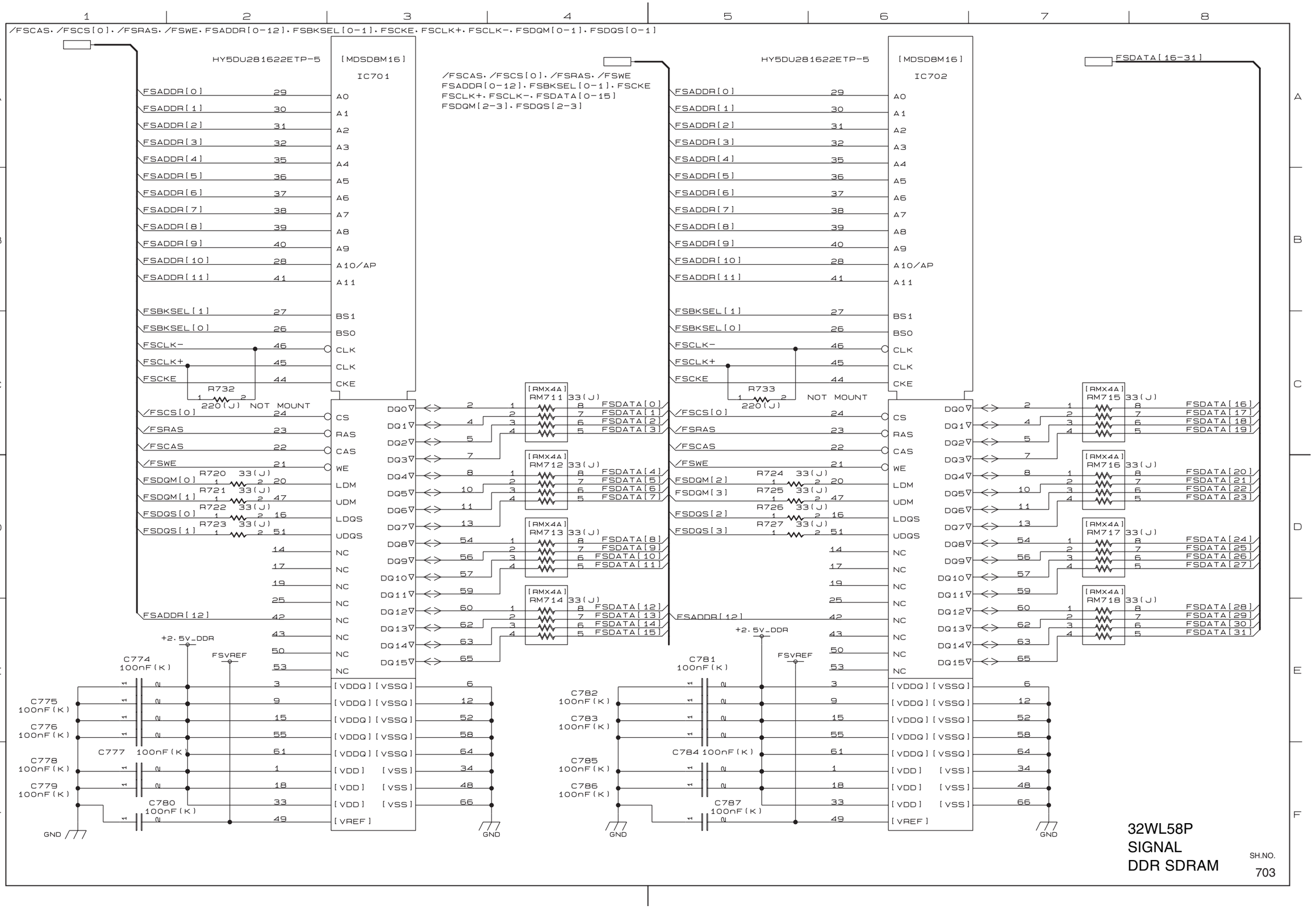
B4	FSDATA0	FSBKSEL1	C20
A4	FSDATA1	FSBKSEL0	C21
B5	FSDATA2	FSCKE	C4
A5	FSDATA3	FSRAS	C24
B7	FSDATA4	FSCAS	D24
A7	FSDATA5	FSWE	C23
B8	FSDATA6		
A8	FSDATA7	FSDQM3	B22
B9	FSDATA8	FSDQM2	B17
A9	FSDATA9	FSDQM1	A11
B10	FSDATA10	FSDQM0	A6
A10	FSDATA11	FSDQS3	A22
B12	FSDATA12	FSDQS2	A17
A12	FSDATA13	FSDQS1	B11
B13	FSDATA14	FSDQS0	B6
A13	FSDATA15	FSCS1	C22
B15	FSDATA16	FSCS0	D21
A15	FSDATA17	FSCLKn	C5
B16	FSDATA18	FSCLKp	D5
A16	FSDATA19		
B18	FSDATA20	FSADDR12	C11
A18	FSDATA21	FSADDR11	C14
B19	FSDATA22	FSADDR10	D16
A19	FSDATA23	FSADDR9	C12
B20	FSDATA24	FSADDR8	C6
A20	FSDATA25	FSADDR7	C7
B21	FSDATA26	FSADDR6	C8
A21	FSDATA27	FSADDR5	C10
B23	FSDATA28	FSADDR4	C13
A23	FSDATA29	FSADDR3	C15
B24	FSDATA30	FSADDR2	C16
A24	FSDATA31	FSADDR1	C17
		FSADDR0	C18

RM700 [RMX4A] 33(J): FSDATA[15] FSDATA[14] FSDATA[13] FSDATA[12]
RM701 [RMX4A] 33(J): FSDATA[11] FSDATA[10] FSDATA[9] FSDATA[8]
RM702 [RMX4A] 33(J): FSDATA[7] FSDATA[6] FSDATA[5] FSDATA[4]
RM703 [RMX4A] 33(J): FSDATA[3] FSDATA[2] FSDATA[1] FSDATA[0]
RM704 [RMX4A] 33(J): FSDATA[31] FSDATA[30] FSDATA[29] FSDATA[28]
RM705 [RMX4A] 33(J): FSDATA[27] FSDATA[26] FSDATA[25] FSDATA[24]
RM706 [RMX4A] 33(J): FSDATA[23] FSDATA[22] FSDATA[21] FSDATA[20]
RM737 [RMX4A] 33(J): FSDATA[19] FSDATA[18] FSDATA[17] FSDATA[16]

R736 33(J): FSBKSEL[1]
R735 33(J): FSBKSEL[0]
R734 33(J): FSCKE
RM707 [RMx4A] 33(J): /FSRAS /FSCAS /FSWE
R740 33(J): FSDQM[2]
R739 33(J): FSDQM[3]
R738 33(J): FSDQM[0]
R737 33(J): FSDQM[1]
R744 33(J): FSDQS[2]
R743 33(J): FSDQS[3]
R742 33(J): FSDQS[0]
R741 33(J): FSDQS[1]
R716 33(J): /FSCS[1]
R717 33(J): /FSCS[0]
R718 33(J): FSCLK-
R719 33(J): FSCLK+
RM708 [RMx4A] 33(J): FSADDR[11] FSADDR[4] FSADDR[9] FSADDR[12]
R707 33(J): FSADDR[10]
RM709 [RMX4A] 33(J): FSADDR[8] FSADDR[7] FSADDR[6] FSADDR[5]
RM710 [RMX4A] 33(J): FSADDR[3] FSADDR[2] FSADDR[1] FSADDR[0]

32WL58P
SIGNAL
DDR I/F

SH.NO.
702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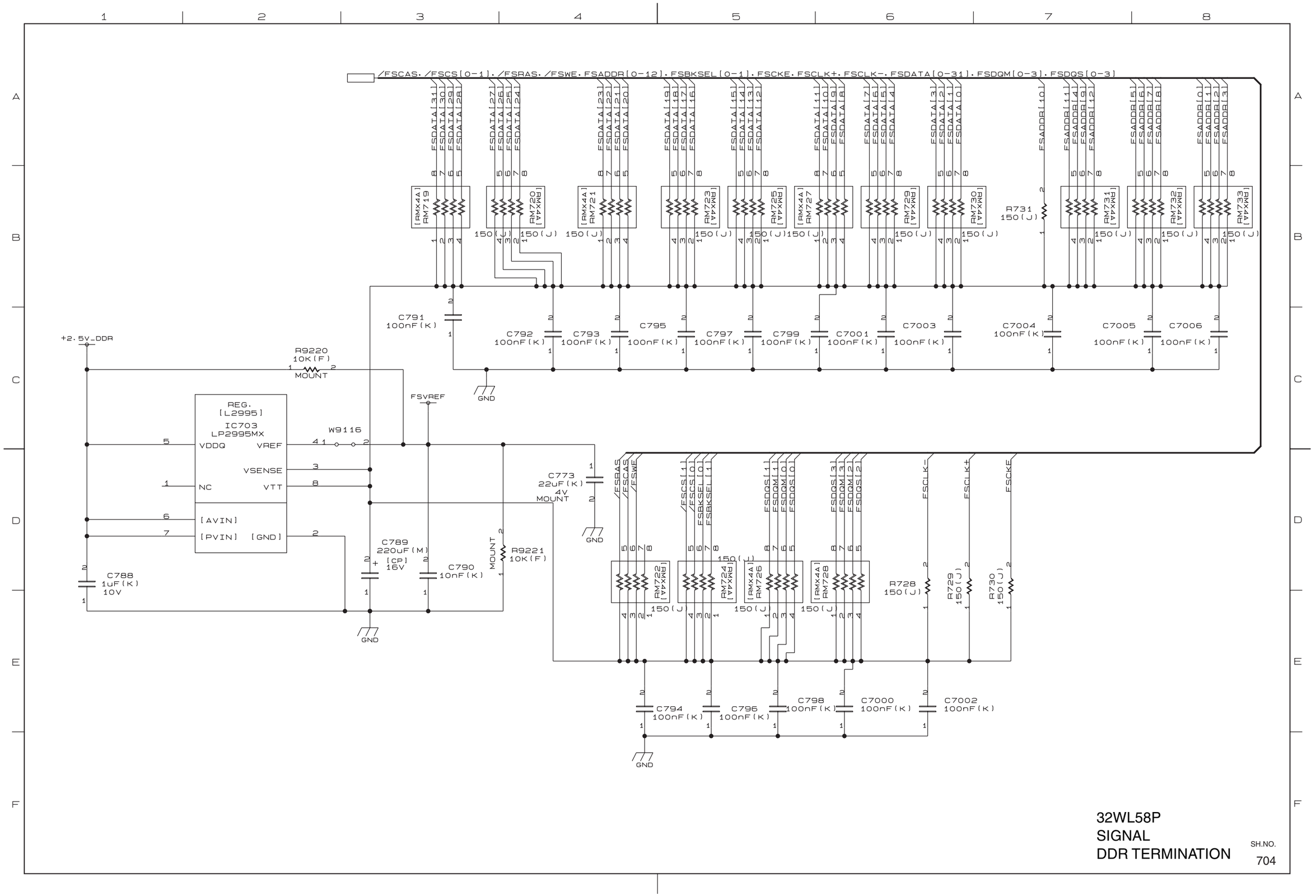
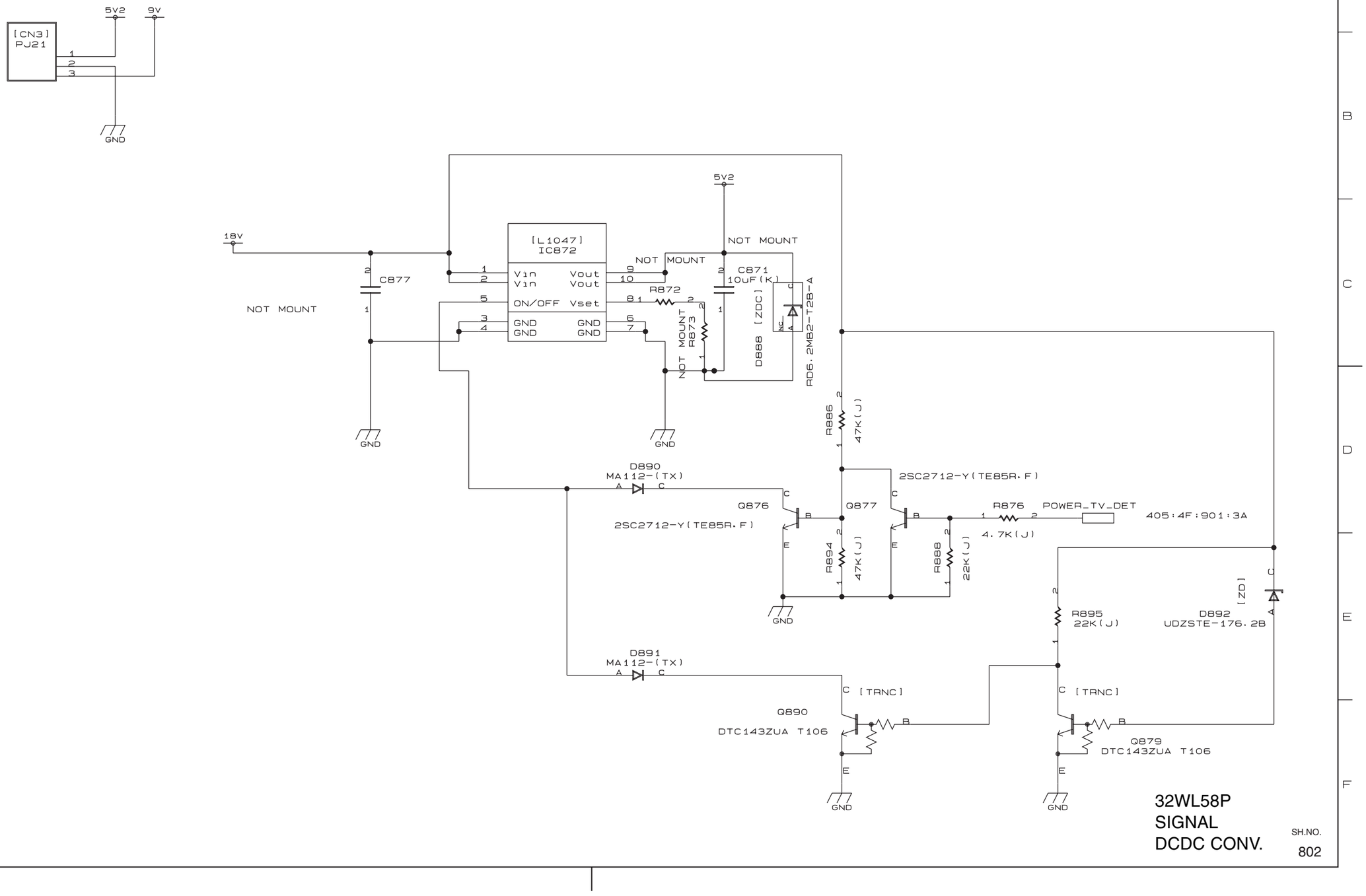
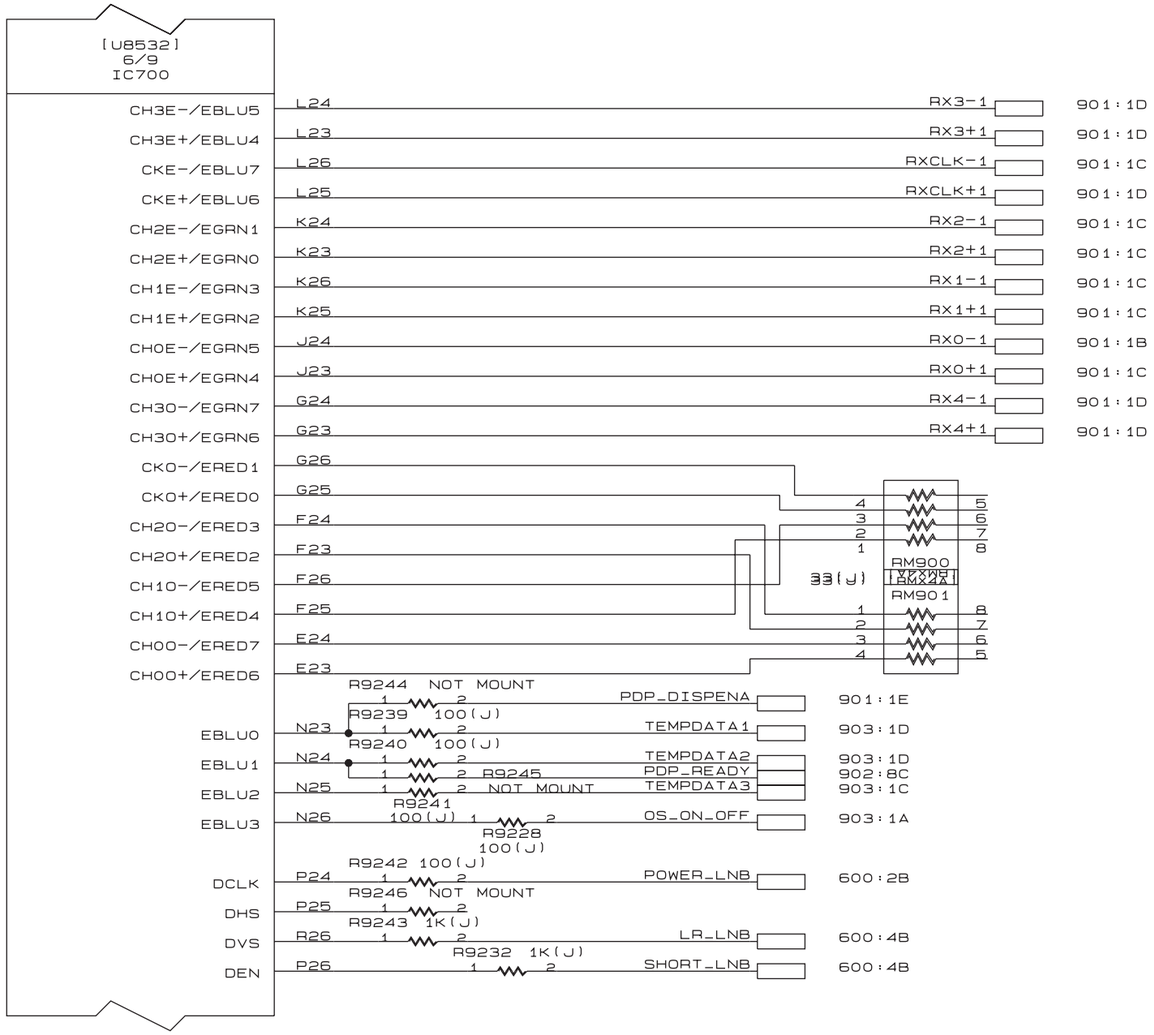
HY5DU281622ETP-5
[MDSD8M16]
IC701
/FSCAS. /FSCS[0]. /FSRAS. /FSWE
FSADDR[0-12]. FSBKSEL[0-1]. FSCKE
FSCLK+. FSCLK-. FSDATA[0-15]
FSDQM[2-3]. FSDQS[2-3]
HY5DU281622ETP-5
[MDSD8M16]
IC702
FSDATA[16-31]
FSADDR[0] FSADDR[1] FSADDR[2] FSADDR[3] FSADDR[4] FSADDR[5] FSADDR[6] FSADDR[7] FSADDR[8] FSADDR[9] FSADDR[10] FSADDR[11]
A0 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10/AP A11
FSBKSEL[1] FSBKSEL[0]
BS1 BS0
FSCLK- FSCLK+ FSCKE
CLK CLK CKE
R732 220(J) NOT MOUNT
R733 220(J) NOT MOUNT
/FSCS[0] /FSRAS /FSCAS /FSWE
CS RAS CAS WE
R720 33(J) FSDQM[0]
R721 33(J) FSDQM[1]
R722 33(J) FSDQS[0]
R723 33(J) FSDQS[1]
R724 33(J) FSDQM[2]
R725 33(J) FSDQM[3]
R726 33(J) FSDQS[2]
R727 33(J) FSDQS[3]
LDM UDM LDQS UDQS
NC
FSADDR[12]
DQ0 DQ1 DQ2 DQ3 DQ4 DQ5 DQ6 DQ7 DQ8 DQ9 DQ10 DQ11 DQ12 DQ13 DQ14 DQ15
[RMX4A] RM711 33(J) FSDATA[0] FSDATA[1] FSDATA[2] FSDATA[3]
[RMX4A] RM712 33(J) FSDATA[4] FSDATA[5] FSDATA[6] FSDATA[7]
[RMX4A] RM713 33(J) FSDATA[8] FSDATA[9] FSDATA[10] FSDATA[11]
[RMX4A] RM714 33(J) FSDATA[12] FSDATA[13] FSDATA[14] FSDATA[15]
[RMX4A] RM715 33(J) FSDATA[16] FSDATA[17] FSDATA[18] FSDATA[19]
[RMX4A] RM716 33(J) FSDATA[20] FSDATA[21] FSDATA[22] FSDATA[23]
[RMX4A] RM717 33(J) FSDATA[24] FSDATA[25] FSDATA[26] FSDATA[27]
[RMX4A] RM718 33(J) FSDATA[28] FSDATA[29] FSDATA[30] FSDATA[31]
[VDDQ] [VSSQ] [VDD] [VSS] [VREF]
+2.5V_DDR
FSVREF
C774 100nF(K)
C775 100nF(K)
C776 100nF(K)
C777 100nF(K)
C778 100nF(K)
C779 100nF(K)
C780 100nF(K)
C781 100nF(K)
C782 100nF(K)
C783 100nF(K)
C784 100nF(K)
C785 100nF(K)
C786 100nF(K)
C787 100nF(K)
GND
32WL58P
SIGNAL
DDR SDRAM
SH.NO.
703

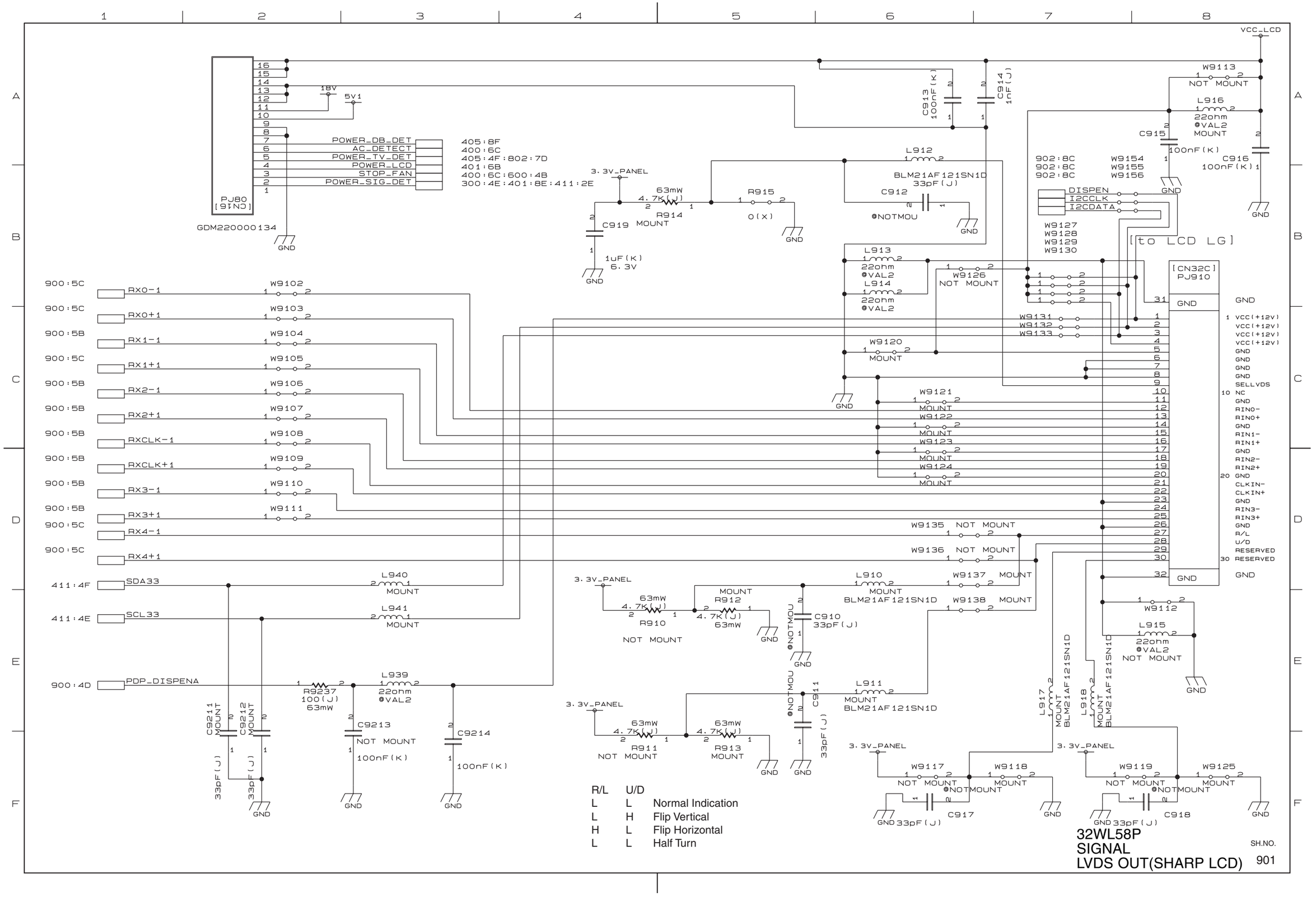

/FSCAS. /FSCS[0-1]. /FSRAS. /FSWE. FSADDR[0-12]. FSBKSEL[0-1]. FSCKE. FSCLK+. FSCLK-. FSDATA[0-31]. FSDQM[0-3]. FSDQS[0-3]
[RMX4A] RM719
[RMX4A] RM720
[RMX4A] RM721
[RMX4A] RM723
[RMX4A] RM725
[RMX4A] RM727
[RMX4A] RM729
[RMX4A] RM730
[RMX4A] RM731
[RMX4A] RM732
[RMX4A] RM733
150(J)
R731 150(J)
C791 100nF(K)
C792 100nF(K)
C793 100nF(K)
C795 100nF(K)
C797 100nF(K)
C799 100nF(K)
C7001 100nF(K)
C7003 100nF(K)
C7004 100nF(K)
C7005 100nF(K)
C7006 100nF(K)
+2.5V_DDR
R9220 10K(F) MOUNT
GND
FSVREF
REG. [L2995] IC703 LP2995MX
VDDQ
VREF
VSENSE
NC
VTT
[AVIN]
[PVIN]
[GND]
W9116
C788 1uF(K) 10V
C789 220uF(M) [CP] 16V
C790 10nF(K)
R9221 10K(F) MOUNT
C773 22uF(K) 4V MOUNT
/FSBAS
/FSCAS
/FSWE
/FSCS[1]
/FSCS[0]
FSBKSEL[0]
FSBKSEL[1]
FSDQS[1]
FSDQM[1]
FSDQM[0]
FSDQS[0]
FSDQS[3]
FSDQM[3]
FSDQM[2]
FSDQS[2]
FSCLK-
FSCLK+
FSCKE
[RMX4A] RM722
[RMX4A] RM724
[RMX4A] RM726
[RMX4A] RM728
R728 150(J)
R729 150(J)
R730 150(J)
C794 100nF(K)
C796 100nF(K)
C798 100nF(K)
C7000 100nF(K)
C7002 100nF(K)
32WL58P
SIGNAL
DDR TERMINATION
SH.NO.
704


[CN3]
PJ21
1
2
3
5V2
9V
GND
18V
C877
NOT MOUNT
[L1047]
IC872
Vin
Vin
ON/OFF
GND
GND
Vout
Vout
Vset
GND
GND
R872
NOT MOUNT
R873
NOT MOUNT
C871
10uF(K)
D888 [ZDC]
RD6.2MB2-T2B-A
R886
47K(J)
D890
MA112-(TX)
D891
MA112-(TX)
Q876
2SC2712-Y(TE85R.F)
Q877
2SC2712-Y(TE85R.F)
R894
47K(J)
R888
22K(J)
R876
4.7K(J)
POWER_TV_DET
405:4F:901:3A
R895
22K(J)
D892
UDZSTE-176.2B
[ZD]
[TRNC]
Q890
DTC143ZUA T106
Q879
DTC143ZUA T106
32WL58P
SIGNAL
DCDC CONV.
SH.NO.
802

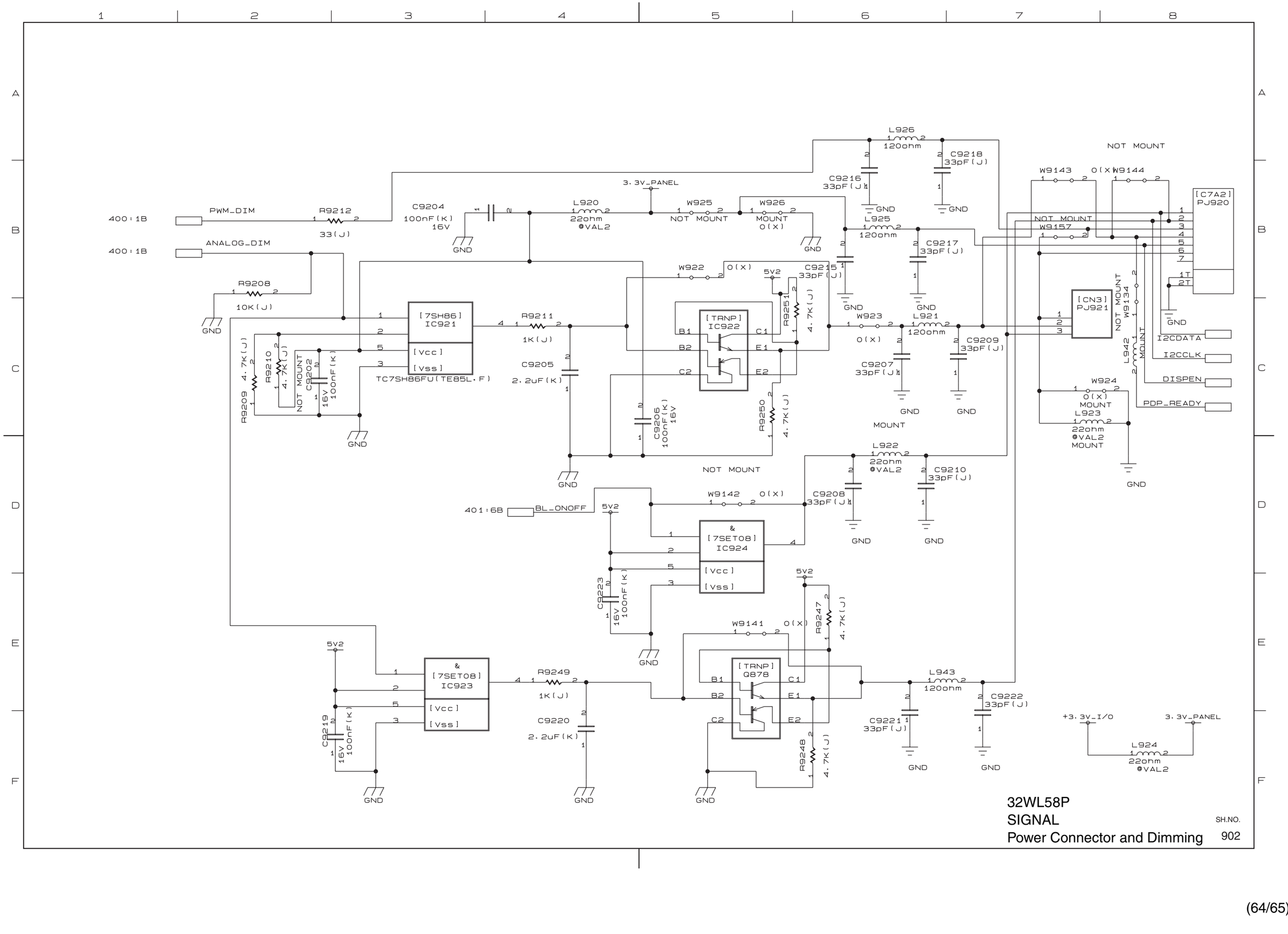


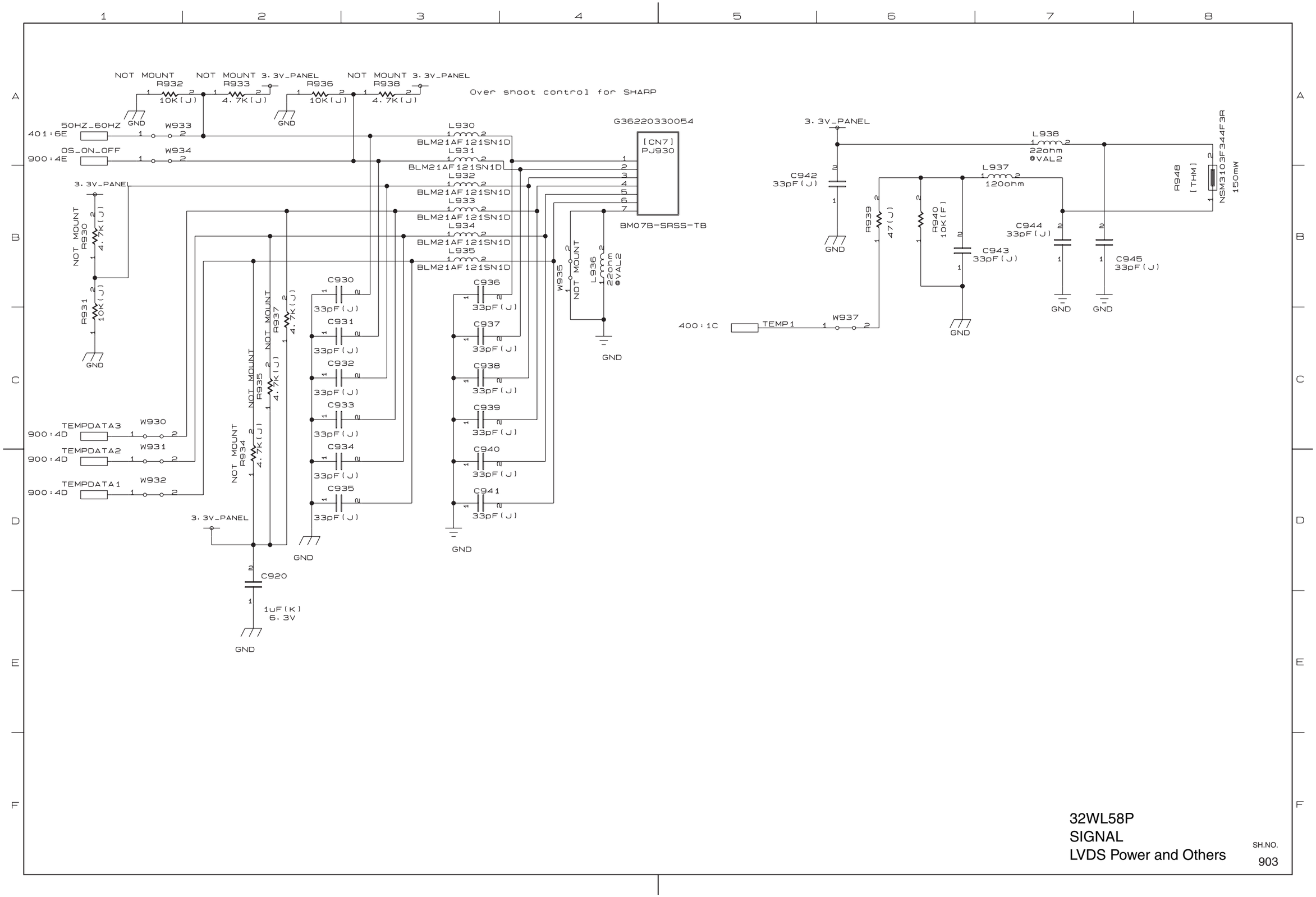
32WL58P
SIGNAL
LVDS OUT

SH.NO.
900


PJ80 [CN16]
GDM220000134
POWER_DB_DET
AC_DETECT
POWER_TV_DET
POWER_LCD
STOP_FAN
POWER_SIG_DET
405:8F
400:6C
405:4F:802:7D
401:6B
400:6C:600:4B
300:4E:401:8E:411:2E
18V
5V1
VCC_LCD
3.3V_PANEL
C913 100nF(K)
C914 1nF(J)
W9113 NOT MOUNT
L916 22ohm @VAL2 MOUNT
C915 100nF(K)
C916 100nF(K)
L912 BLM21AF121SN1D
C912 33pF(J) @NOTMOU
902:8C W9154
902:8C W9155
902:8C W9156
DISPEN
I2CCLK
I2CDATA
W9127
W9128
W9129
W9130
[to LCD LG]
R914 MOUNT
R915 0(X)
63mW 4.7K(J)
C919 1uF(K) 6.3V
L913 22ohm @VAL2
L914 22ohm @VAL2
W9126 NOT MOUNT
[CN32C] PJ910
W9131
W9132
W9133
W9120 MOUNT
W9121 MOUNT
W9122 MOUNT
W9123 MOUNT
W9124 MOUNT
900:5C RX0-1 W9102
900:5C RX0+1 W9103
900:5B RX1-1 W9104
900:5C RX1+1 W9105
900:5B RX2-1 W9106
900:5B RX2+1 W9107
900:5B RXCLK-1 W9108
900:5B RXCLK+1 W9109
900:5B RX3-1 W9110
900:5B RX3+1 W9111
900:5C RX4-1
900:5C RX4+1
411:4F SDA33 L940 MOUNT
411:4E SCL33 L941 MOUNT
900:4D PDP_DISPENA R9237 100(J) 63mW L939 22ohm @VAL2
C9211 MOUNT 33pF(J)
C9212 MOUNT 33pF(J)
C9213 NOT MOUNT 100nF(K)
C9214 100nF(K)
W9135 NOT MOUNT
W9136 NOT MOUNT
W9137 MOUNT
W9138 MOUNT
L910 BLM21AF121SN1D MOUNT
R910 NOT MOUNT
R912 4.7K(J) 63mW MOUNT
C910 33pF(J) @NOTMOU
L911 BLM21AF121SN1D MOUNT
R911 NOT MOUNT
R913 MOUNT
C911 33pF(J) @NOTMOU
L917 BLM21AF121SN1D MOUNT
L918 BLM21AF121SN1D MOUNT
W9112
L915 22ohm @VAL2 NOT MOUNT
W9117 NOT MOUNT @NOTMOUNT
W9118 MOUNT
W9119 NOT MOUNT @NOTMOUNT
W9125 MOUNT
C917 33pF(J)
C918 33pF(J)
GND
R/L U/D
L L Normal Indication
L H Flip Vertical
H L Flip Horizontal
L L Half Turn
32WL58P
SIGNAL
LVDS OUT(SHARP LCD)
SH.NO. 901

(63/65)


PWM_DIM
400:1B
R9212
33(J)
C9204
100nF(K)
16V
L920
220hm
@VAL2
3.3V_PANEL
W925
NOT MOUNT
W926
MOUNT
0(X)
L926
120ohm
C9216
33pF(J)
C9218
33pF(J)
NOT MOUNT
W9143
W9144
[C7A2]
PJ920
ANALOG_DIM
400:1B
R9208
10K(J)
L925
120ohm
C9217
33pF(J)
NOT MOUNT
W9157
W922
0(X)
5V2
C9215
33pF(J)
[7SH86]
IC921
[Vcc]
[Vss]
TC7SH86FU(TE85L.F)
R9211
1K(J)
C9205
2.2uF(K)
[TRNP]
IC922
B1
B2
C2
C1
E1
E2
R9251
4.7K(J)
W923
0(X)
L921
120ohm
C9209
33pF(J)
C9207
33pF(J)
[CN3]
PJ921
NOT MOUNT
W9134
L942
MOUNT
1T
2T
I2CDATA
I2CCLK
DISPEN
PDP_READY
R9209 4.7K(J)
R9210
4.7K(J)
NOT MOUNT
C9202
16V
100nF(K)
C9206
100nF(K)
16V
R9250
4.7K(J)
MOUNT
W924
0(X)
MOUNT
L923
220hm
@VAL2
MOUNT
NOT MOUNT
L922
220hm
@VAL2
C9210
33pF(J)
W9142
0(X)
C9208
33pF(J)
401:6B
BL_ONOFF
5V2
&
[7SET08]
IC924
[Vcc]
[Vss]
C9223
16V
100nF(K)
W9141
0(X)
R9247
4.7K(J)
5V2
&
[7SET08]
IC923
[Vcc]
[Vss]
C9219
16V
100nF(K)
R9249
1K(J)
C9220
2.2uF(K)
[TRNP]
Q878
B1
B2
C2
C1
E1
E2
R9248
4.7K(J)
L943
120ohm
C9221
33pF(J)
C9222
33pF(J)
+3.3V_I/O
3.3V_PANEL
L924
22ohm
@VAL2
GND
32WL58P
SIGNAL
Power Connector and Dimming
SH.NO.
902



32WL58P
SIGNAL
LVDS Power and Others

SH.NO.
903